

Features

- Low start-up voltage: 0.7V (Typ.)
- High efficiency: 85% (Typ.)
- High output voltage accuracy: $\pm 2.5\%$
- Output voltage: 2.7V, 3.0V, 3.3V, 5.0V
- Output current up to 200mA
- Ultra low supply current I_{DD} : 5 μ A (Typ.)
- Low ripple and low noise
- Low shutdown current: 0.5 μ A (Typ.)
- TO92, SOT89, SOT23 and SOT23-5 package

Applications

- Palmtops/PDAs
- Portable communicators/Smartphones
- Cameras/Camcorders
- Battery-powered equipment

General Description

The HT77XXA series is a set of PFM step-up DC/DC converter with high efficiency and low ripple. The series features extremely low start-up voltage and high output voltage accuracy. They require only three external components to provide a fixed output voltage of 2.7V, 3.0V, 3.3V or 5.0V. CMOS technology ensures ultra low supply current and makes them ideal for battery-operated applications powered from one or more cells.

The HT77XXA consists of an oscillator, a PFM control circuit, a driver transistor, a reference voltage unit, and a high speed comparator. They employ pulse frequency modulation (PFM) for minimum supply current and ripple at light output loading. These devices are available in space saving TO92, SOT89, SOT23 and SOT23-5 packages. For SOT23-5 package, it also build-in a chip enable function to reduce power consumption during shutdown mode.

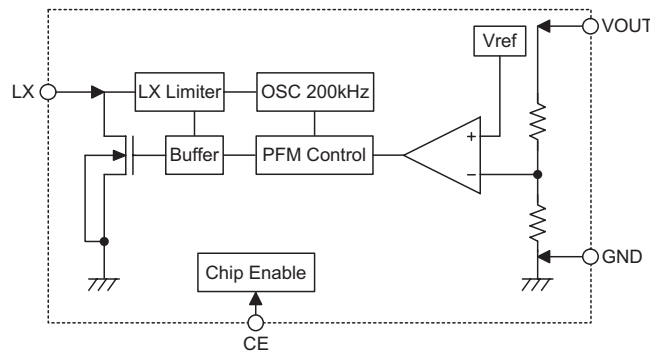
Selection Table

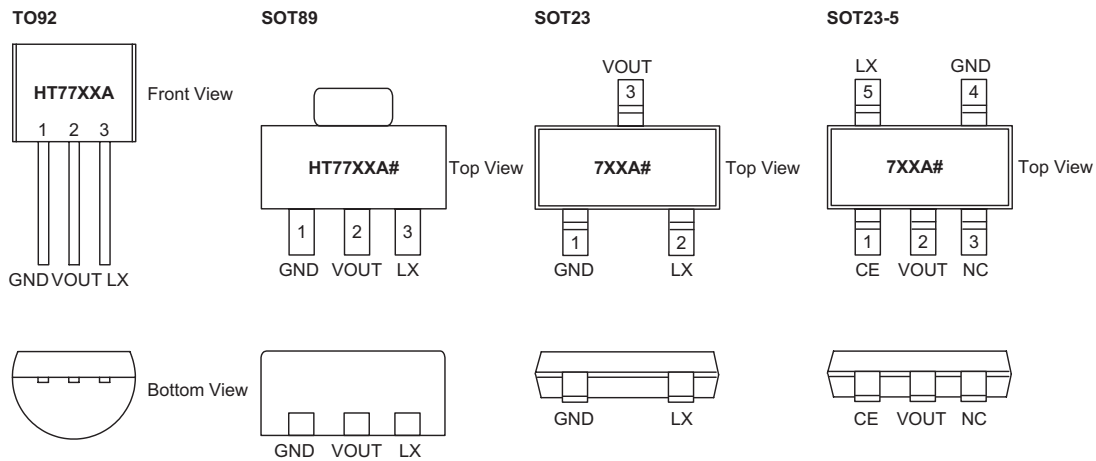
Part No.	Output Voltage	Package	Marking
HT7727A	2.7V	TO92 SOT89 SOT23 SOT23-5	HT77XXA (for TO92)
HT7730A	3.0V		HT77XXA# (for SOT89)
HT7733A	3.3V		7XXA# (for SOT23)
HT7750A	5.0V		7XXA# (for SOT23-5)

Note: "XX" stands for output voltages.

Only lead free devices are available. "#" stands for lead free devices. For the TO92 package, there will be a "#" mark at the end of the date code.

Block Diagram



Pin Assignment

Pin Description

Pin No.				Pin Name	Description
TO92	SOT89	SOT23	SOT23-5		
—	—	—	1	CE	Chip enable pin, high active
2	2	3	2	VOUT	DC/DC converter output monitoring pin
—	—	—	3	NC	No connection
1	1	1	4	GND	Ground pin
3	3	2	5	LX	Switching pin

Absolute Maximum Ratings

Supply Voltage $V_{SS}-0.3V$ to $V_{SS}+7V$ Storage Temperature $-50^{\circ}C$ to $125^{\circ}C$
 Operating Temperature $-40^{\circ}C$ to $85^{\circ}C$

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Thermal Information

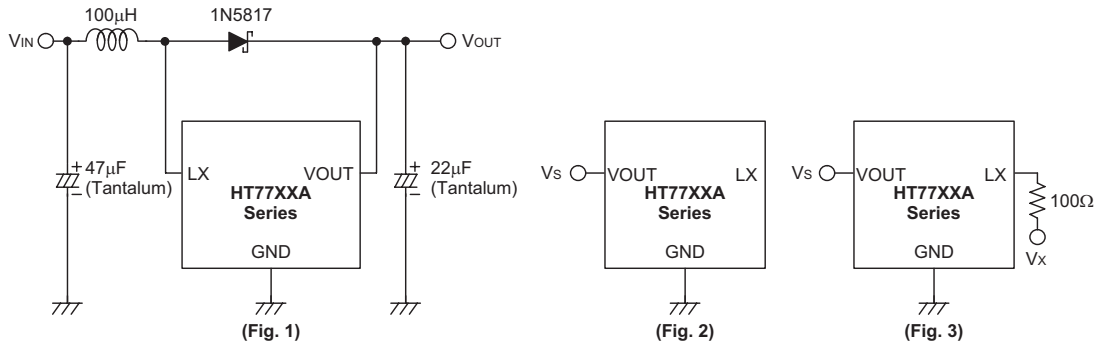
Symbol	Parameter	Package	Max.	Unit
θ_{JA}	Thermal Resistance (Junction to Ambient) (Assume no ambient airflow, no heat sink)	SOT89	300	$^{\circ}C/W$
		TO92	300	$^{\circ}C/W$
		SOT23	330	$^{\circ}C/W$
		SOT23-5	320	$^{\circ}C/W$
P_D	Power Dissipation	SOT89	0.33	W
		TO92	0.33	W
		SOT23	0.3	W
		SOT23-5	0.31	W

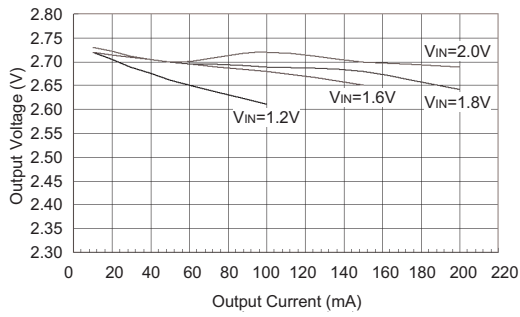
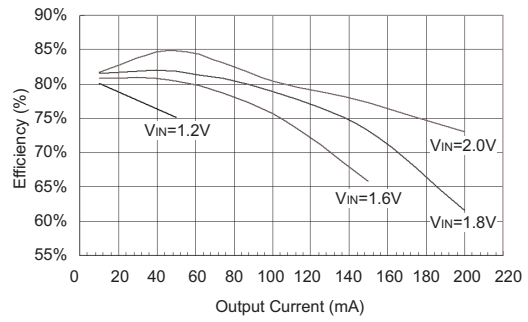
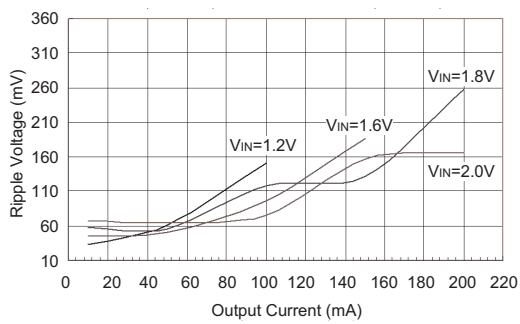
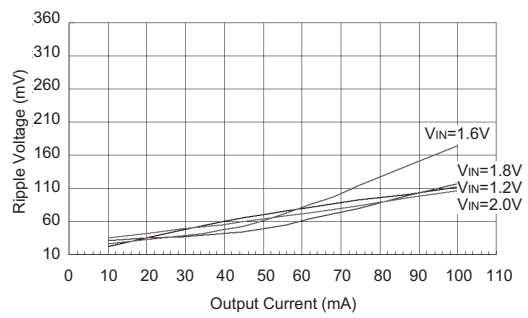
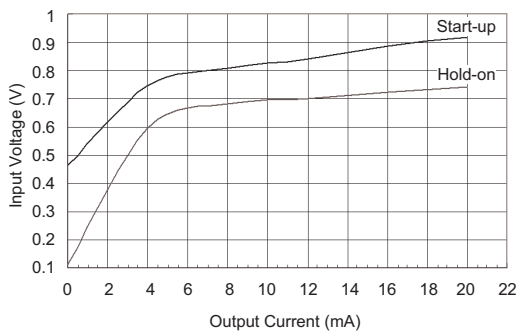
Note: P_D is measured at $T_a = 25^{\circ}C$

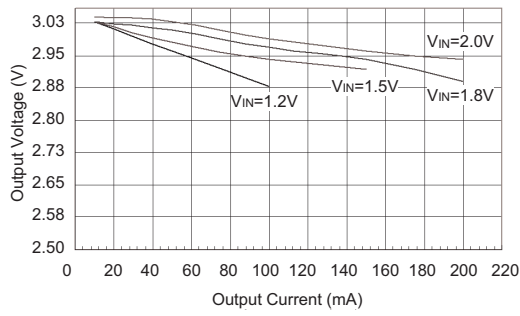
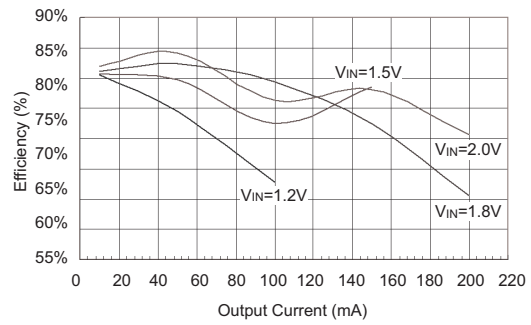
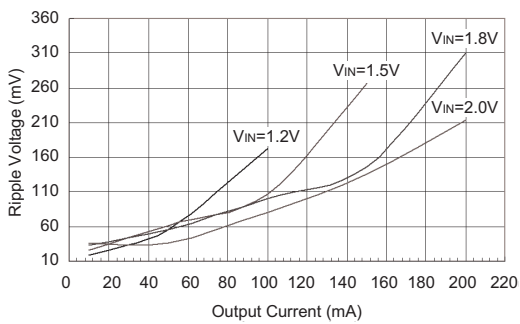
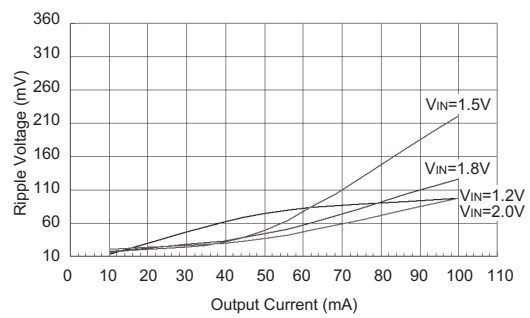
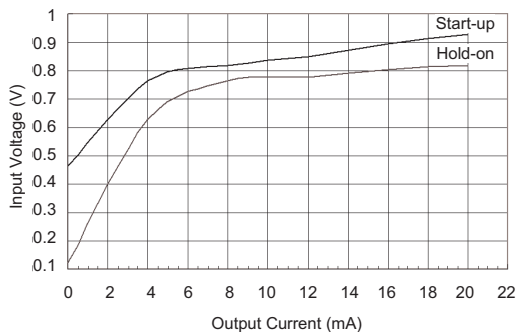
Electrical Characteristics
 $V_{IN}=V_{OUT}\times 0.6$; $I_{OUT}=10\text{mA}$; $T_a=25^\circ\text{C}$ (Unless otherwise specified)

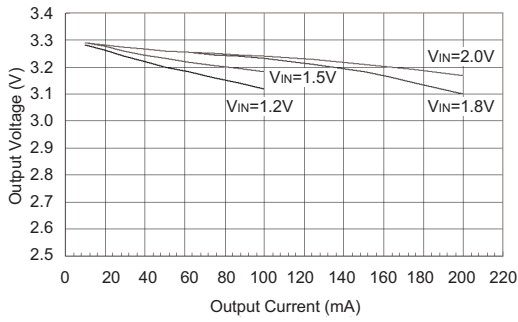
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_{IN}	Input Voltage	—	—	—	6	V
V_{OUT}	Output Voltage Tolerance	—	-2.5	—	2.5	%
V_{START}	Start-up Voltage (Fig. 1)	$V_{IN}: 0\rightarrow 2\text{V}$; $I_{OUT}=1\text{mA}$	—	0.7	0.9	V
V_{HOLD}	Minimum Hold-on Voltage (Fig. 1)	$V_{IN}: 2\rightarrow 0\text{V}$; $I_{OUT}=1\text{mA}$	—	—	0.7	V
I_{IN}	No-load Input Current (Fig. 1)	$I_{OUT}=0\text{mA}$	—	13	26	μA
I_{DD}	Supply Current (Fig. 2)	$V_S=V_{OUT}+0.5\text{V}$ Measured at V_{OUT} pin	—	5	10	μA
I_{SHDN}	Shutdown Current	$\text{CE}=\text{GND}$	—	0.5	1	μA
V_{IH}	CE High Threshold	—	2	—	—	V
V_{IL}	CE Low Threshold	—	—	—	0.4	V
I_{LEAK}	LX Leakage Current (Fig. 3)	$V_S=V_{OUT}+0.5\text{V}$, $V_X=6\text{V}$ Measured at the LX pin	—	—	0.9	μA
f_{OSC}	Maximum Oscillator Frequency (Fig. 3)	$V_S=V_{OUT}\times 0.95$, $V_X=6\text{V}$ Measured at LX pin	—	200	—	kHz
D_{OSC}	Oscillator Duty Cycle (Fig. 3)	$V_S=V_{OUT}\times 0.95$, $V_X=6\text{V}$ Measured at LX pin	65	75	85	%
η	Efficiency	—	—	85	—	%

Note: Absolute maximum ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but do not guarantee specific performance limits. The guaranteed specifications apply only for the test conditions listed.

Test Circuit


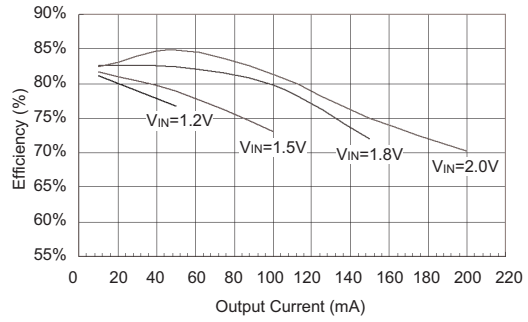
Typical Performance Characteristics

HT7727A Output Voltage v.s Output Current
($C_{IN}=47\mu F$ -Tantalum, $L=47\mu H$, $C_{OUT}=22\mu F$ -Tantalum)

HT7727A Efficiency v.s Output Current
($C_{IN}=47\mu F$ -Tantalum, $L=47\mu H$, $C_{OUT}=22\mu F$ -Tantalum)

HT7727A Ripple Voltage v.s Output Current
($C_{IN}=47\mu F$ -Tantalum, $L=47\mu H$, $C_{OUT}=22\mu F$ -Tantalum)

HT7727A Ripple Voltage v.s Output Current
($C_{IN}=47\mu F$ -Tantalum, $L=100\mu H$, $C_{OUT}=22\mu F$ -Tantalum)

HT7727A Start-up/hold-on Voltage
($C_{IN}=47\mu F$ -Tantalum, $L=47\mu H$, $C_{OUT}=22\mu F$ -Tantalum)


HT7730A Output Voltage v.s Output Current
($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

HT7730A Efficiency v.s Output Current
($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

HT7730A Ripple Voltage v.s Output Current
($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

HT7730A Ripple Voltage v.s Output Current
($C_{IN}=47\mu\text{F-Tantalum}$, $L=100\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

HT7730A Start-up/hold-on Voltage
($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



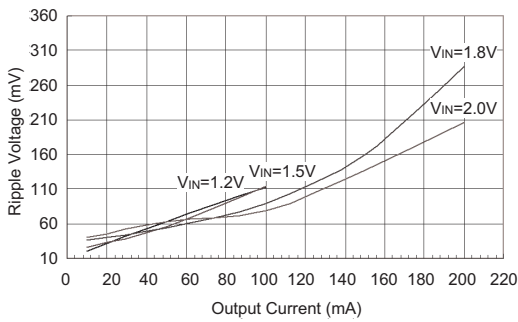
HT7733A Output Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



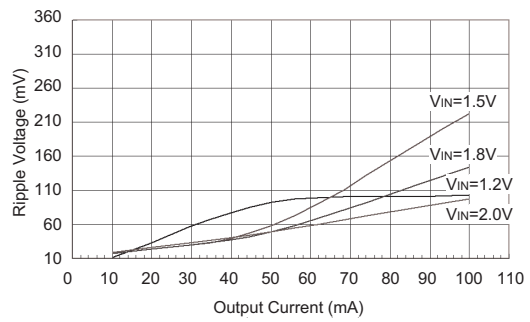
HT7733A Efficiency v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



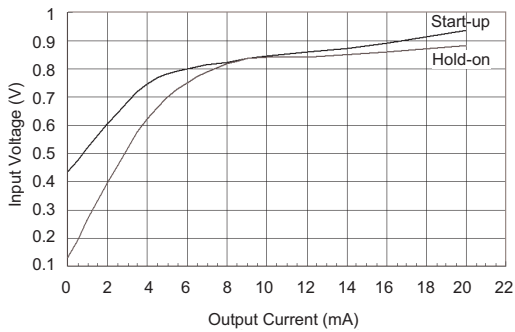
HT7733A Ripple Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



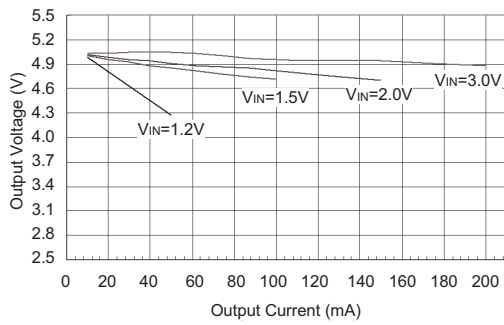
HT7733A Ripple Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=100\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



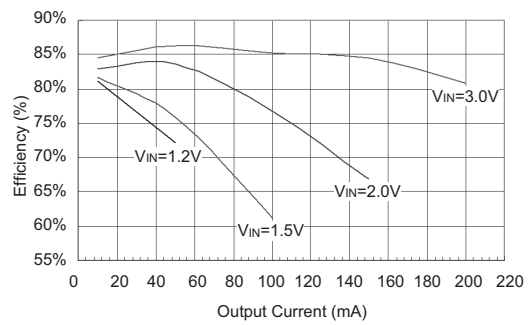
HT7733A Start-up/Hold-on Voltage

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



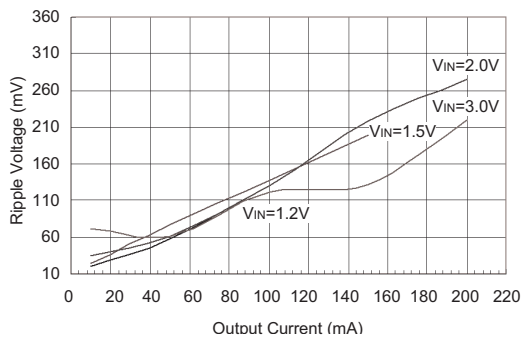
HT7750A Output Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



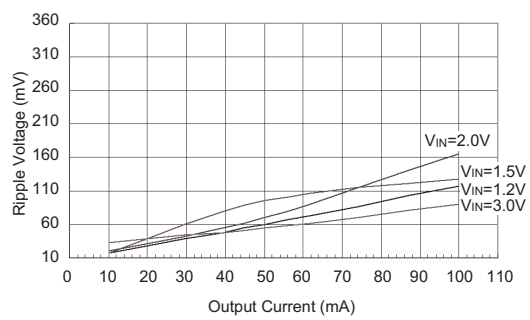
HT7750A Efficiency v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



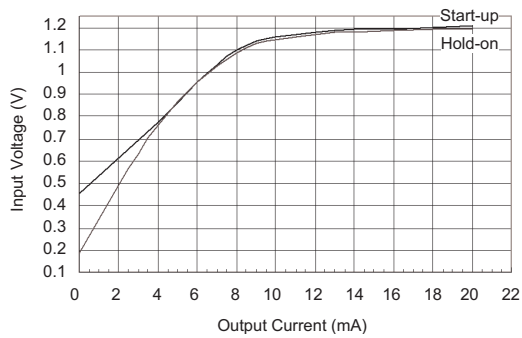
HT7750A Ripple Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)



HT7750A Ripple Voltage v.s Output Current

($C_{IN}=47\mu\text{F-Tantalum}$, $L=100\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

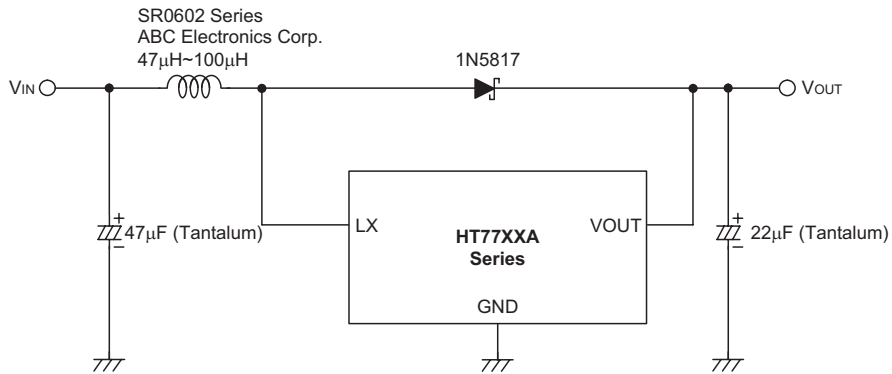


HT7750A Start-up/Hold-on Voltage

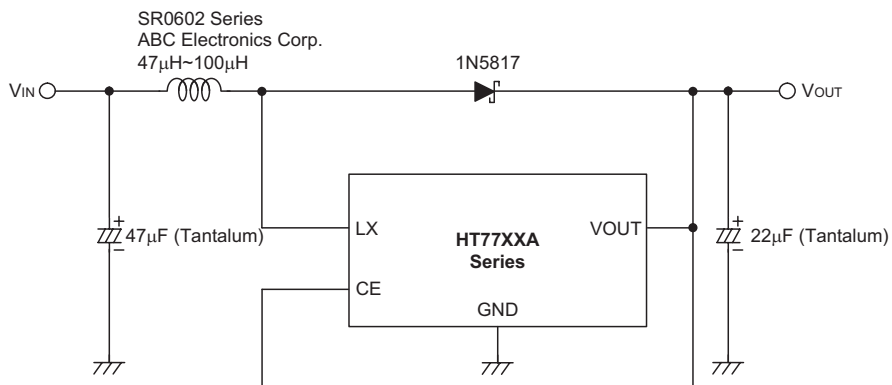
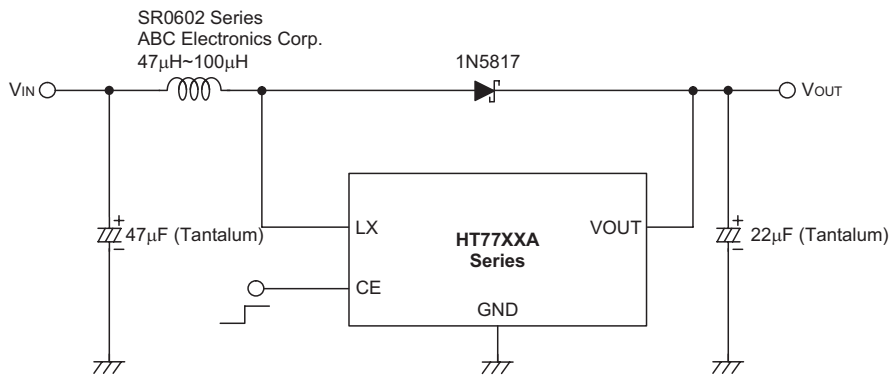
($C_{IN}=47\mu\text{F-Tantalum}$, $L=47\mu\text{H}$, $C_{OUT}=22\mu\text{F-Tantalum}$)

Application Circuits

Without CE Pin



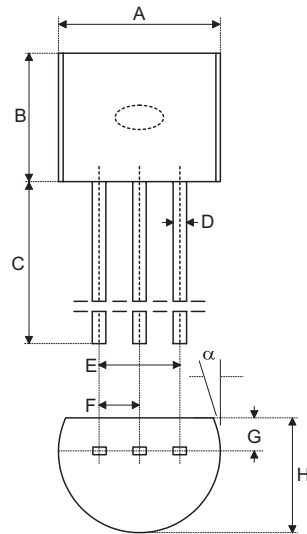
With CE Pin



Note: For the SOT23-5 package, when CE is pulled low, the internal blocks of the device, such as the reference band gap, gain block, and all feedback and control circuitry will be switched off. The boost converter's output, V_{OUT} , will be at a value one Schottky diode voltage drop below the input voltage and the LX pin remains in a high impedance condition. The output capacitor and load at V_{OUT} determine the rate at which V_{OUT} decays.

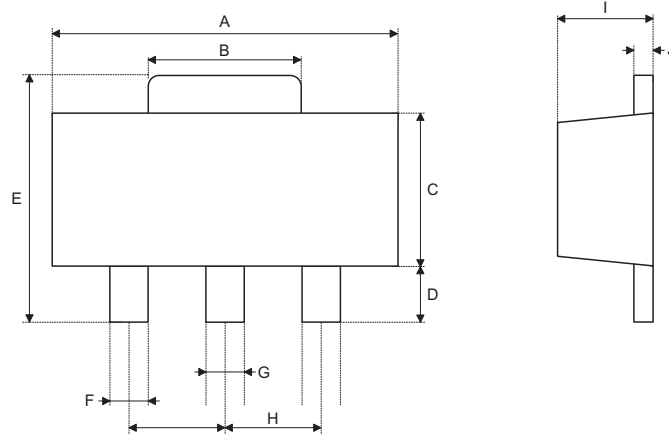
Package Information

3-pin TO92 Outline Dimensions



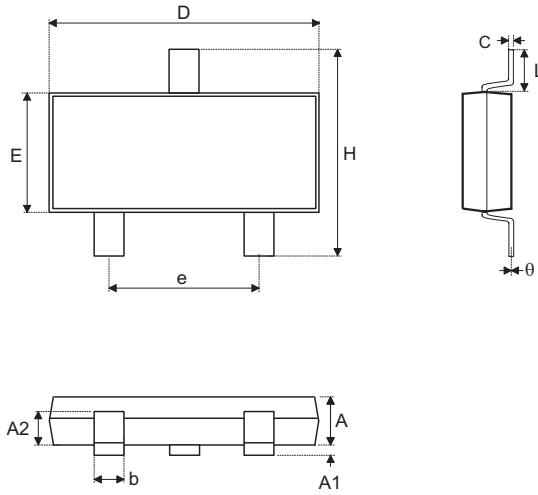
Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	170	—	200
B	170	—	200
C	500	—	—
D	11	—	20
E	90	—	110
F	45	—	55
G	45	—	65
H	130	—	160
I	8	—	18
α	4°	—	6°

3-pin SOT89 Outline Dimensions



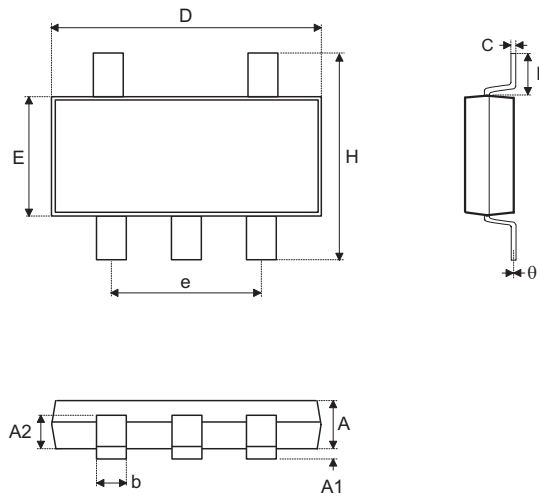
Symbol	Dimensions in mil		
	Min.	Nom.	Max.
A	173	—	181
B	59	—	72
C	90	—	102
D	35	—	47
E	155	—	167
F	14	—	19
G	17	—	22
H	—	59	—
I	55	—	63
J	14	—	17

3-pin SOT23 Outline Dimensions



Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	1.0	—	1.3
A1	—	—	0.1
A2	0.7	—	0.9
b	0.35	—	0.50
C	0.10	—	0.25
D	2.7	—	3.1
E	1.4	—	1.8
e	—	1.9	—
H	2.6	—	3.0
L	0.37	—	—
θ	1°	—	9°

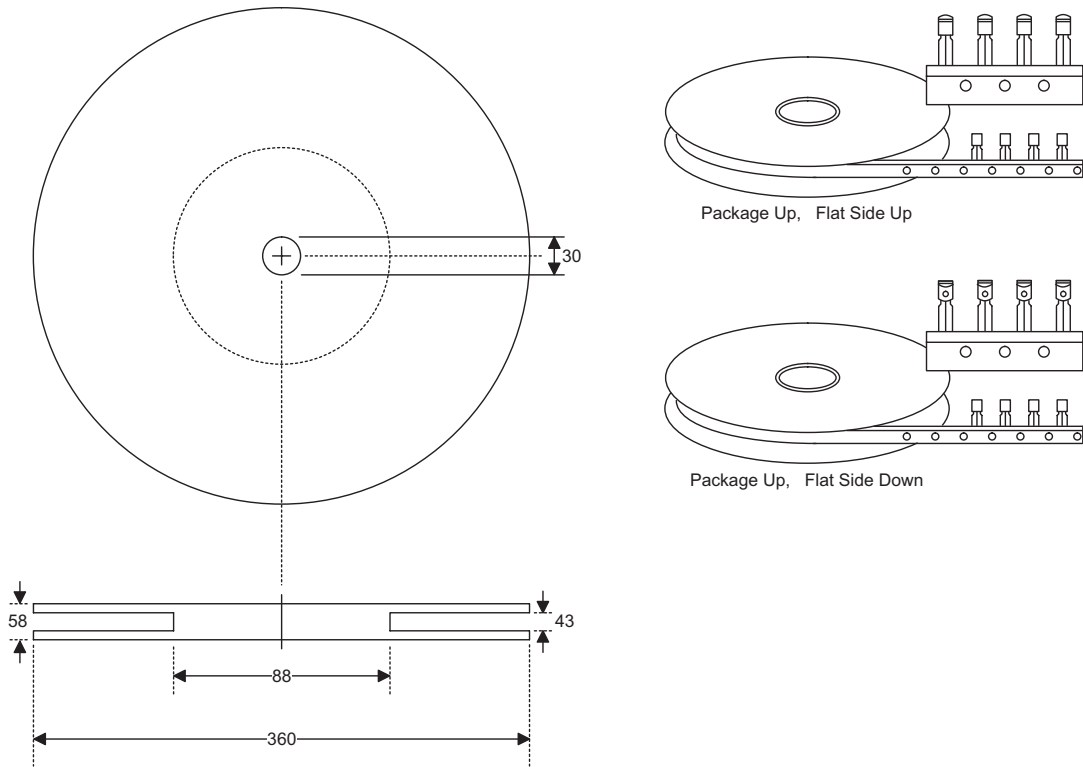
5-pin SOT23-5 Outline Dimensions



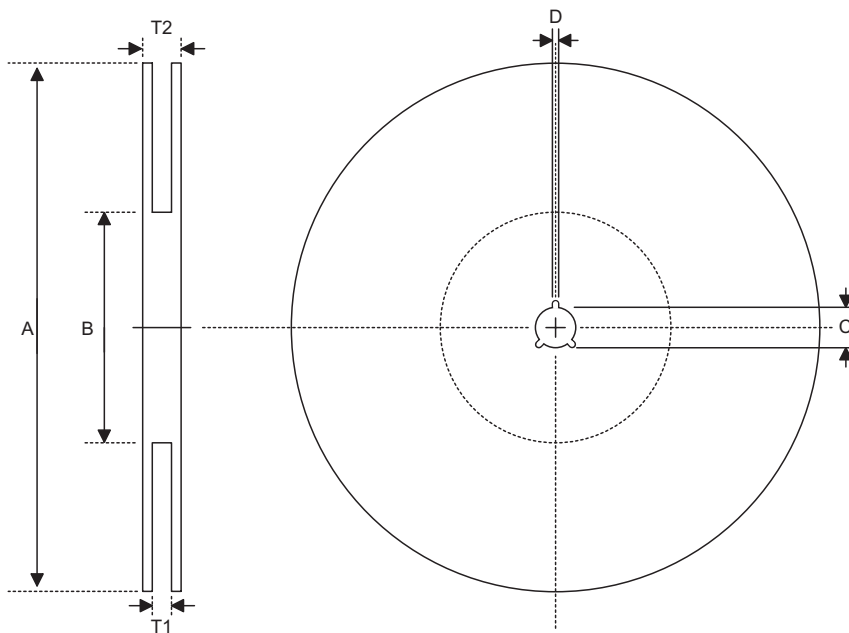
Symbol	Dimensions in mm		
	Min.	Nom.	Max.
A	1.0	—	1.3
A1	—	—	0.1
A2	0.7	—	0.9
b	0.35	—	0.50
C	0.10	—	0.25
D	2.7	—	3.1
E	1.4	—	1.8
e	—	1.90	—
H	2.6	—	3.0
L	0.37	—	—
θ	1°	—	9°

Product Tape and Reel Specifications

TO92 Reel Dimensions (Unit: mm)



Reel Dimensions

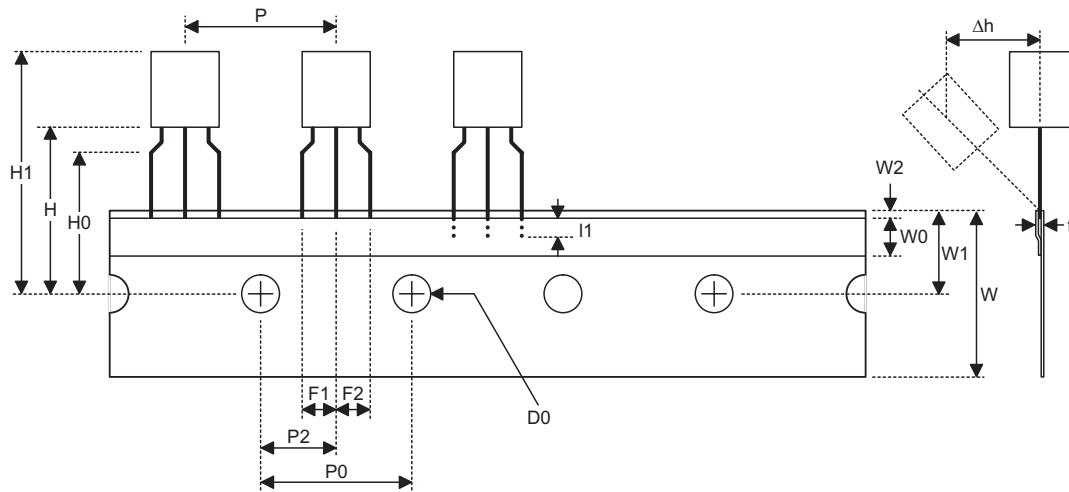


SOT89

Symbol	Description	Dimensions in mm
A	Reel Outer Diameter	180.0±1.0
B	Reel Inner Diameter	62.0±1.5
C	Spindle Hole Diameter	12.75 ^{+0.15/-0.00}
D	Key Slit Width	1.90±0.15
T1	Space Between Flange	12.4 ^{+0.2/-0.0}
T2	Reel Thickness	17.0 ^{+0.0/-0.4}

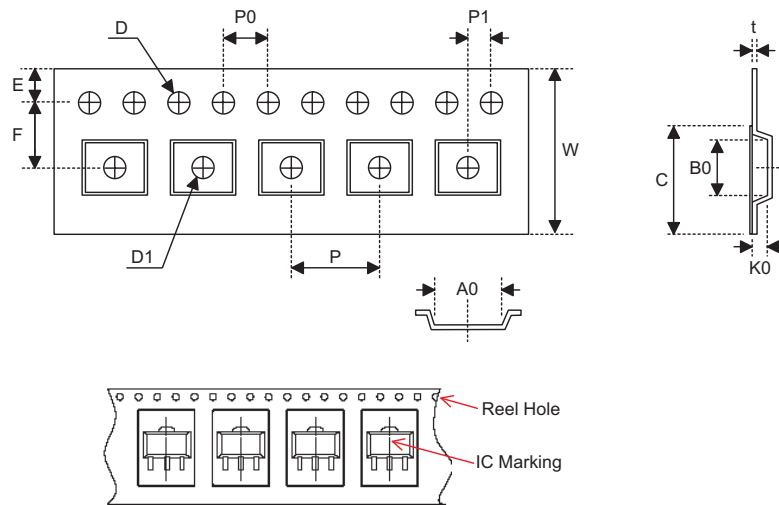
SOT23, SOT23-5

Symbol	Description	Dimensions in mm
A	Reel Outer Diameter	178.0±1.0
B	Reel Inner Diameter	62.0±1.0
C	Spindle Hole Diameter	13.0±0.2
D	Key Slit Width	2.50±0.25
T1	Space Between Flange	8.4 ^{+1.5/-0.0}
T2	Reel Thickness	11.4 ^{+1.5/-0.0}

Carrier Tape Dimensions

TO92

Symbol	Description	Dimensions in mm
l1	Taped Lead Length	(2.5)
P	Component Pitch	12.7±1.0
P ₀	Perforation Pitch	12.7±0.3
P ₂	Component to Perforation (Length Direction)	6.35±0.40
F ₁	Lead Spread	2.5 ^{+0.4/-0.1}
F ₂	Lead Spread	2.5 ^{+0.4/-0.1}
Δh	Component Alignment	0.0±0.1
W	Carrier Tape Width	18.0 ^{+1.0/-0.5}
W ₀	Hold-down Tape Width	6.0±0.5
W ₁	Perforation Position	9.0±0.5
W ₂	Hold-down Tape Position	(0.5)
H ₀	Lead Clinch Height	16.0±0.5
H ₁	Component Height	Less than 24.7
D ₀	Perforation Diameter	4.0±0.2
t	Taped Lead Thickness	0.7±0.2
H	Component Base Height	19.0±0.5

Note: Thickness less than 0.38±0.05mm~0.5mm
P0 Accumulated pitch tolerance: ±1mm/20pitches.
() Bracketed figures are for reference only.

Carrier Tape Dimensions

SOT89

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12.0 ^{+0.3/-0.1}
P	Cavity Pitch	8.0±0.1
E	Perforation Position	1.75±0.10
F	Cavity to Perforation (Width Direction)	5.50±0.05
D	Perforation Diameter	1.5 ^{+0.1/-0.0}
D1	Cavity Hole Diameter	1.5 ^{+0.1/-0.0}
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	4.8±0.1
B0	Cavity Width	4.5±0.1
K0	Cavity Depth	1.8±0.1
t	Carrier Tape Thickness	0.300±0.013
C	Cover Tape Width	9.3±0.1

SOT23, SOT23-5

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	8.0±0.3
P	Cavity Pitch	4.0±0.1
E	Perforation Position	1.75±0.10
F	Cavity to Perforation (Width Direction)	3.50±0.05
D	Perforation Diameter	1.5 ^{+0.1/-0.0}
D1	Cavity Hole Diameter	1.5 ^{+0.1/-0.0}
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.00±0.05
A0	Cavity Length	3.15±0.10
B0	Cavity Width	3.2±0.1
K0	Cavity Depth	1.4±0.1
t	Carrier Tape Thickness	0.20±0.03
C	Cover Tape Width	5.3±0.1

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