Device Modeling Report

COMPONENTS: BIPOLAR JUNCTION TRANSISTOR

PART NUMBER:2SC1740S MANUFACTURER:ROHM

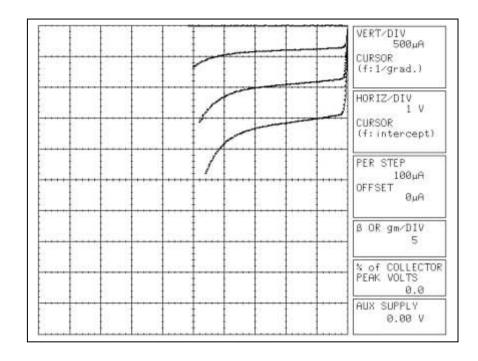


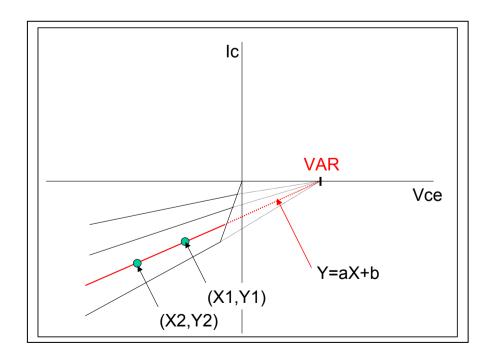
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Pspice			
·	Model description		
parameter			
IS Saturation Current			
BF Ideal Maximum Forward Beta			
NF Forward Current Emission Coefficient	Forward Current Emission Coefficient		
VAF Forward Early Voltage			
IKF Forward Beta Roll-off Knee Current			
ISE Non-ideal Base-Emitter Diode Saturation Curren	ıt		
NE Non-ideal Base-Emitter Diode Emission Coefficient	ent		
BR Ideal Maximum Reverse Beta			
NR Reverse Emission Coefficient			
VAR Reverse Early Voltage			
IKR Reverse Beta Roll-off Knee Current			
ISC Non-ideal Base-Collector Diode Saturation Curre	ent		
NC Non-ideal Base-Collector Diode Emission Coefficient	cient		
NK Forward Beta Roll-off Slope Exponent			
RE Emitter Resistance			
RB Base Resistance			
RC Series Collector Resistance			
CJE Zero-bias Emitter-Base Junction Capacitance			
VJE Emitter-Base Junction Potential			
MJE Emitter-Base Junction Grading Coefficient			
CJC Zero-bias Collector-Base Junction Capacitance			
VJC Collector-base Junction Potential			
MJC Collector-base Junction Grading Coefficient			
FC Coefficient for Onset of Forward-bias Depletion			
Capacitance			
TF Forward Transit Time			
XTF Coefficient for TF Dependency on Vce			
VTF Voltage for TF Dependency on Vce			
ITF Current for TF Dependency on Ic			
PTF Excess Phase at f=1/2pi*TF			
TR Reverse Transit Time			
EG Activation Energy			
XTB Forward Beta Temperature Coefficient			
XTI Temperature Coefficient for IS			

Reverse

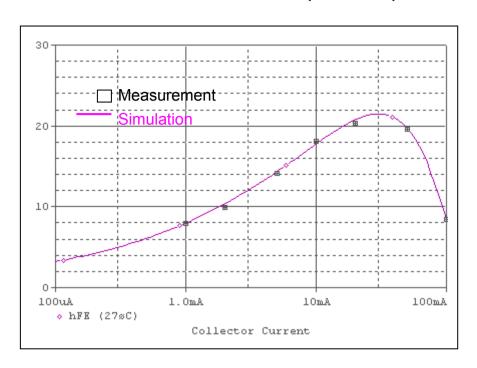
Reverse Early Voltage Characteristic





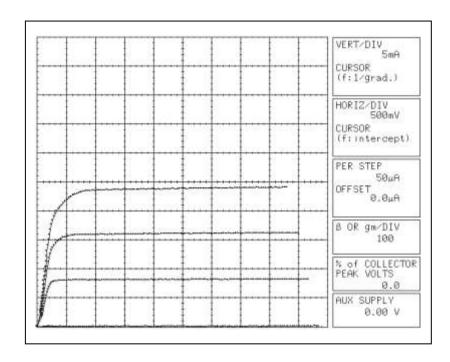
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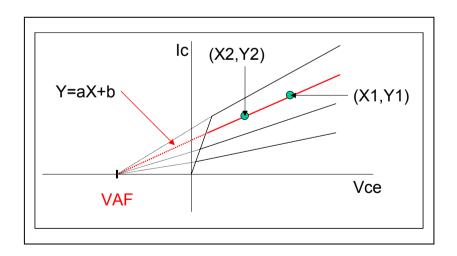
Reverse DC Beta Characteristic (le vs. hfe)



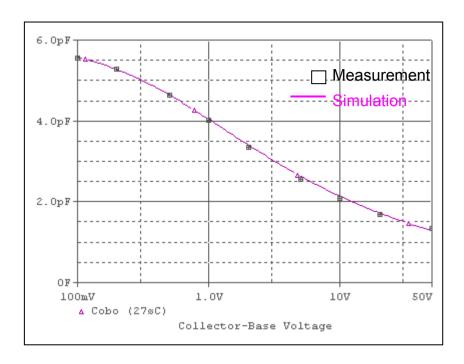
Forward

Forward Early Voltage Characteristic

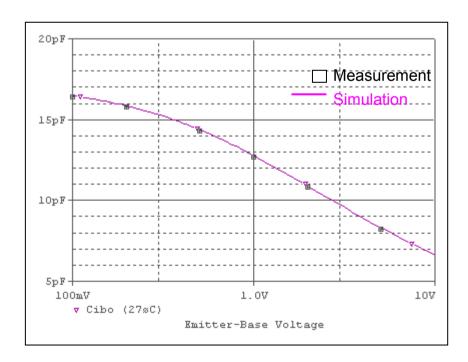




C-B Capacitance Characteristic

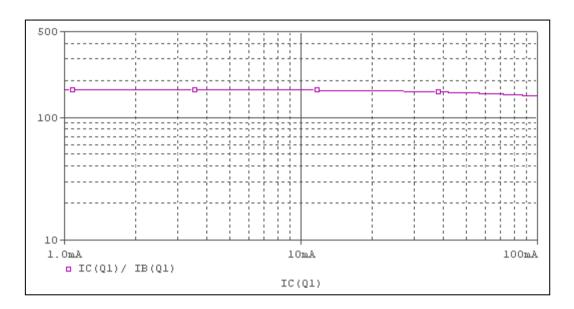


E-B Capacitance Characteristic

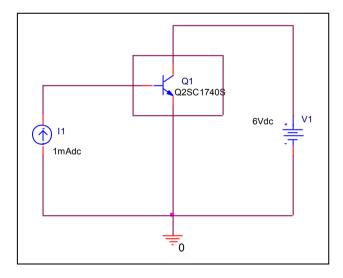


BJT Ic-hfe characteristics

Circuit simulation result

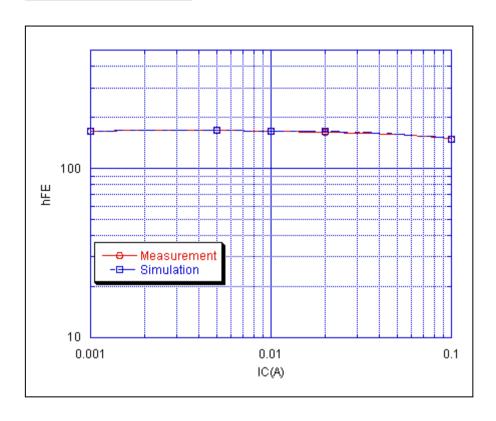


Evaluation circuit



Comparison Graph

Circuit simulation result

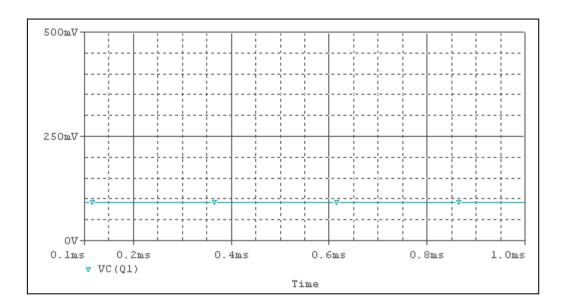


Simulation result

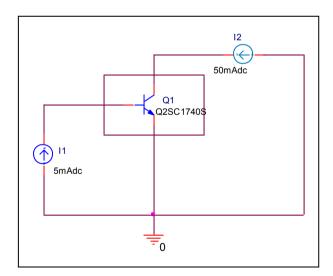
Ic(A)	hFE		%Error
	Measurement	Simulation	70E1101
0.001	166.93	166.92	0.005990535
0.002	167.49	167.51	0.011941011
0.005	167.22	167.527	0.18359048
0.01	166.39	166.741	0.210950177
0.02	164.39	164.764	0.227507756
0.05	159.24	158.661	0.36360211
0.1	148.81	148.774	0.024191923

BJT Vce(sat) voltage Characteristics

Circuit simulation result



Evaluation circuit



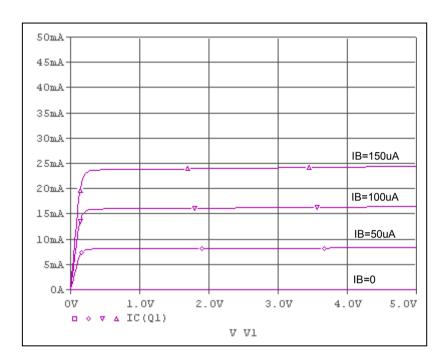
Simulation result

Test condition: IC/IB = 10, IC=50mA

Vce(sat)(V)			
Measurement	Simulation	Error(%)	
0.4[Max]	0.091	-	

Output Characteristics

Circuit simulation result



Evaluation circuit

