

Device Modeling Report

COMPONENTS: BIPOLAR JUNCTION TRANSISTOR
PART NUMBER: 2SA1015
MANUFACTURER: TOSHIBA



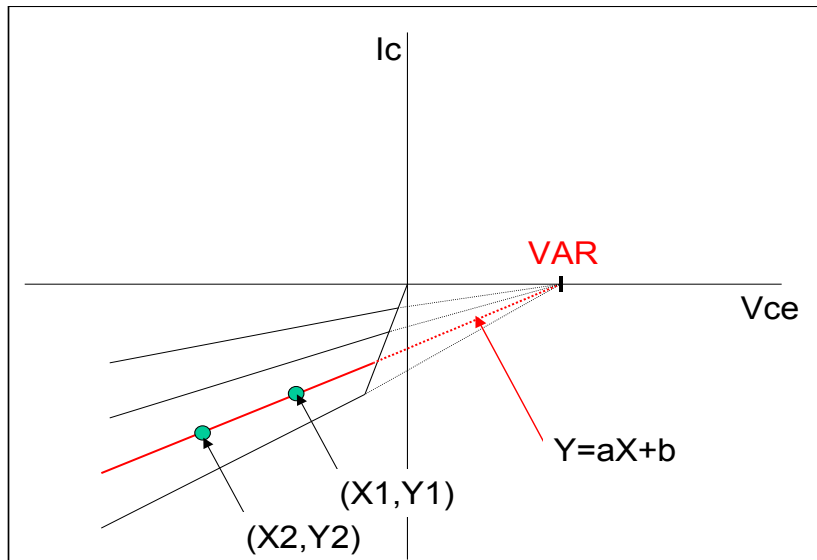
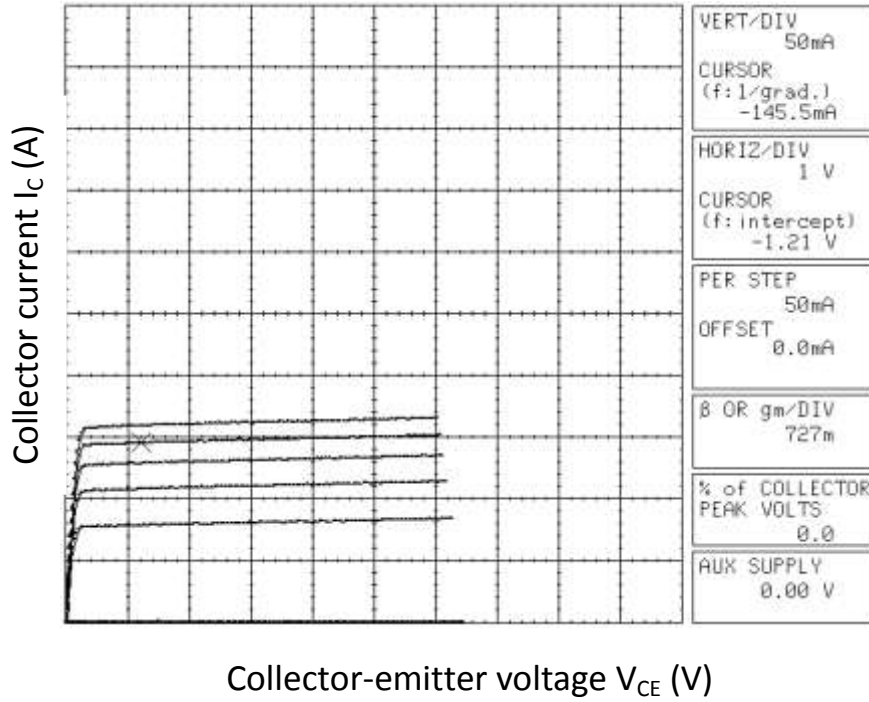
Bee Technologies Inc.

BJT SPICE Model Parameters

PSpice model parameter	Model description
IS	Saturation Current
BF	Ideal Maximum Forward Beta
NF	Forward Current Emission Coefficient
VAF	Forward Early Voltage
IKF	Forward Beta Roll-off Knee Current
ISE	Non-ideal Base-Emitter Diode Saturation Current
NE	Non-ideal Base-Emitter Diode Emission Coefficient
BR	Ideal Maximum Reverse Beta
NR	Reverse Emission Coefficient
VAR	Reverse Early Voltage
IKR	Reverse Beta Roll-off Knee Current
ISC	Non-ideal Base-Collector Diode Saturation Current
NC	Non-ideal Base-Collector Diode Emission Coefficient
NK	Forward Beta Roll-off Slope Exponent
RE	Emitter Resistance
RB	Base Resistance
RC	Series Collector Resistance
CJE	Zero-bias Emitter-Base Junction Capacitance
VJE	Emitter-Base Junction Potential
MJE	Emitter-Base Junction Grading Coefficient
CJC	Zero-bias Collector-Base Junction Capacitance
VJC	Collector-base Junction Potential
MJC	Collector-base Junction Grading Coefficient
FC	Coefficient for Onset of Forward-bias Depletion Capacitance
TF	Forward Transit Time
XTF	Coefficient for TF Dependency on Vce
VTF	Voltage for TF Dependency on Vce
ITF	Current for TF Dependency on Ic
PTF	Excess Phase at $f=1/2\pi*TF$
TR	Reverse Transit Time
EG	Activation Energy
XTB	Forward Beta Temperature Coefficient
XTI	Temperature Coefficient for IS

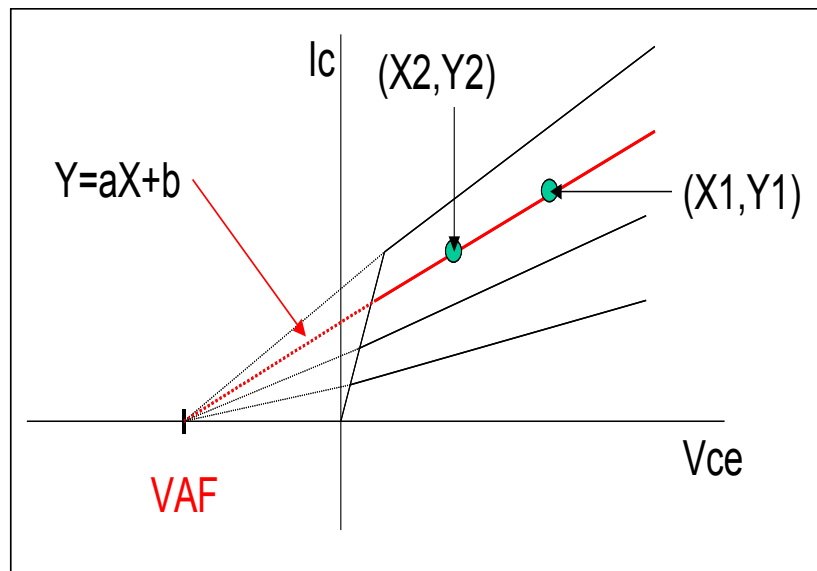
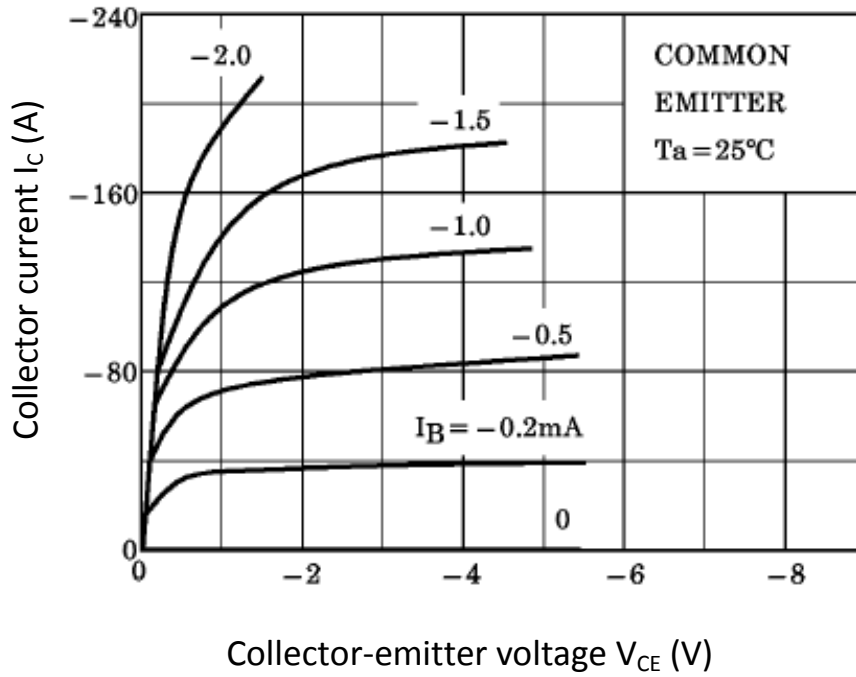
Reverse

Reverse Early Voltage Characteristic

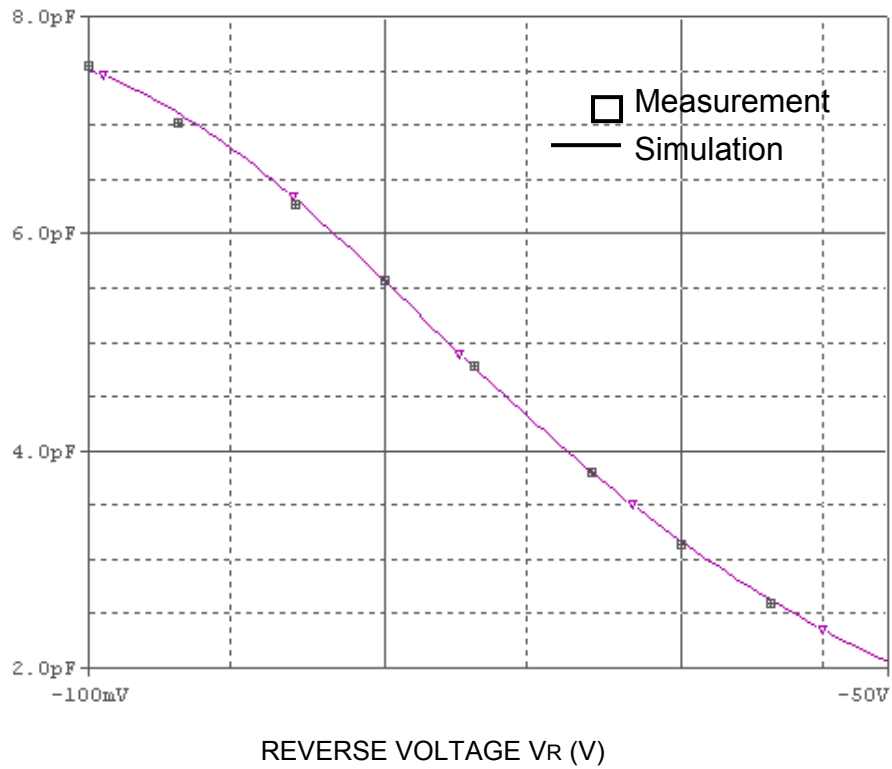


Forward

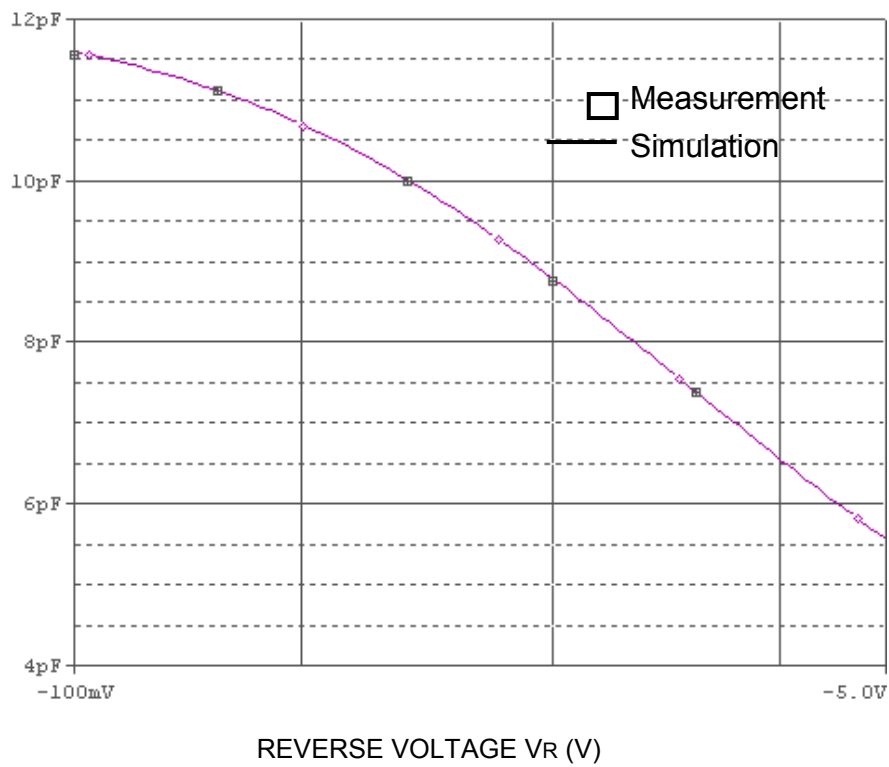
Forward Early Voltage Characteristic



C-B Capacitance Characteristics

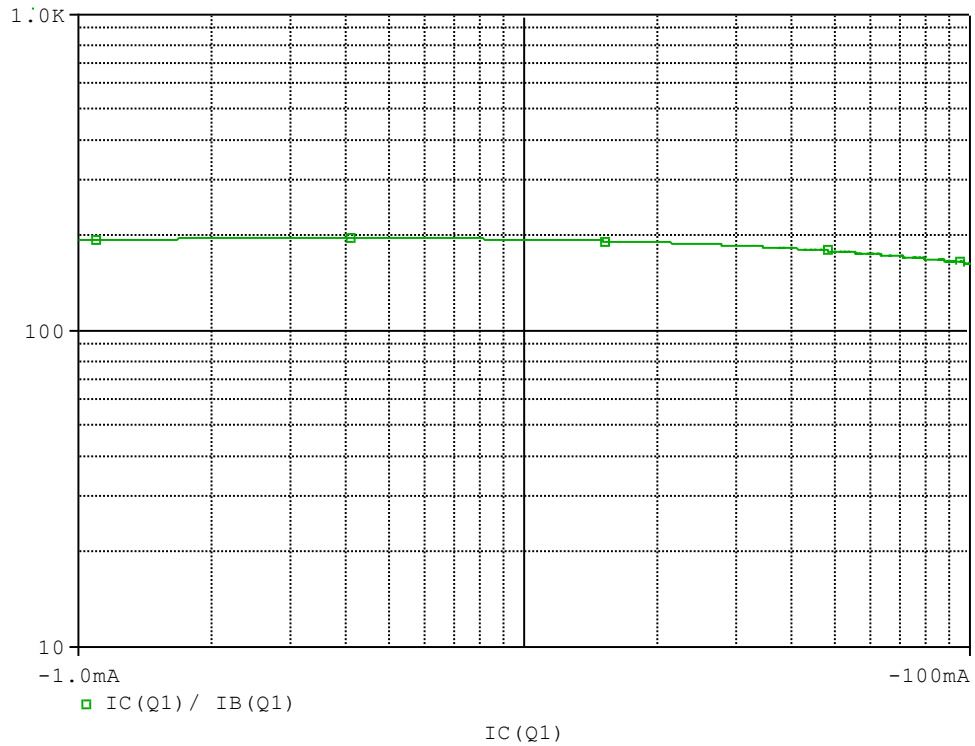


E-B Capacitance Characteristics

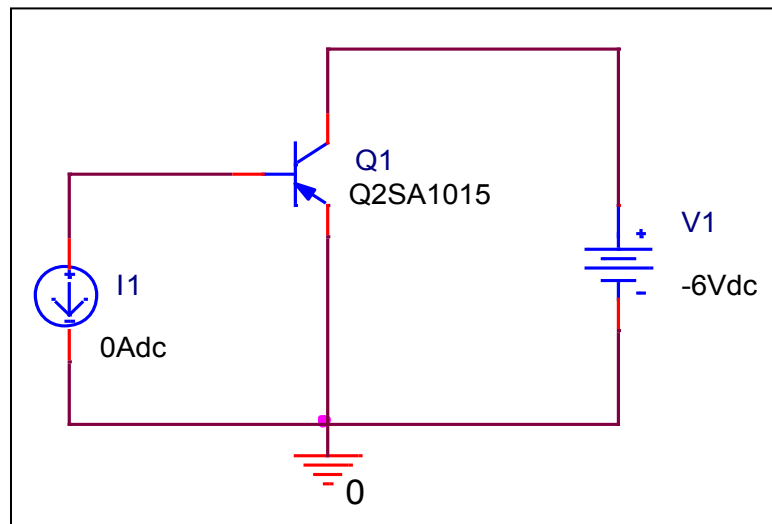


Transistor h_{FE} - I_C Characteristics

Simulation result

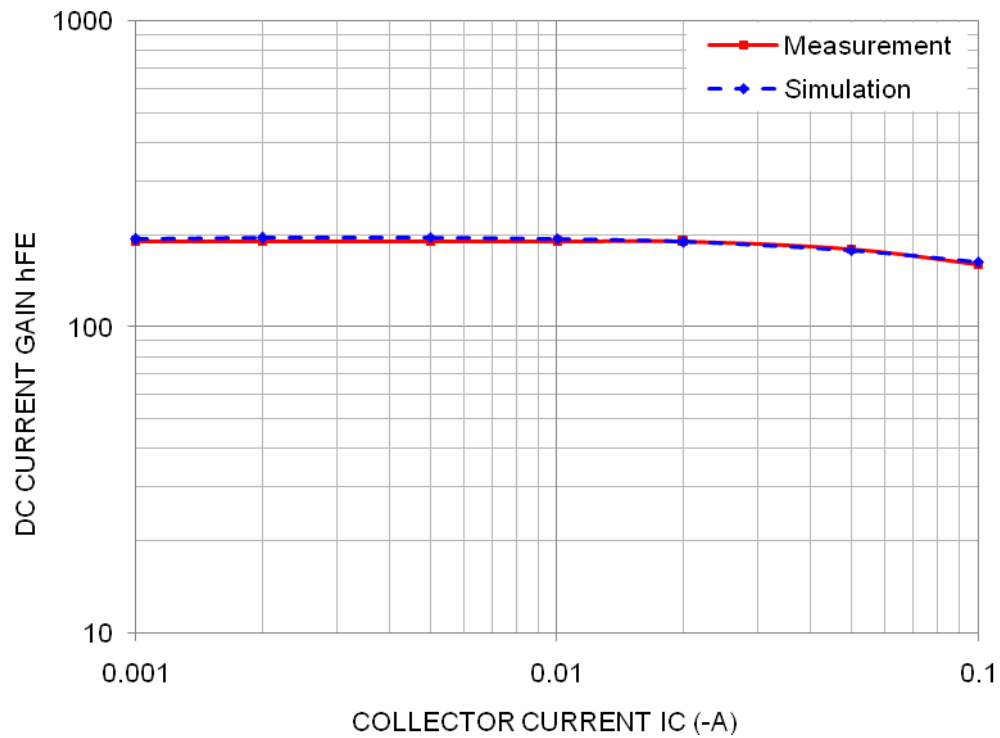


Evaluation circuit



Comparison Graph

Simulation result

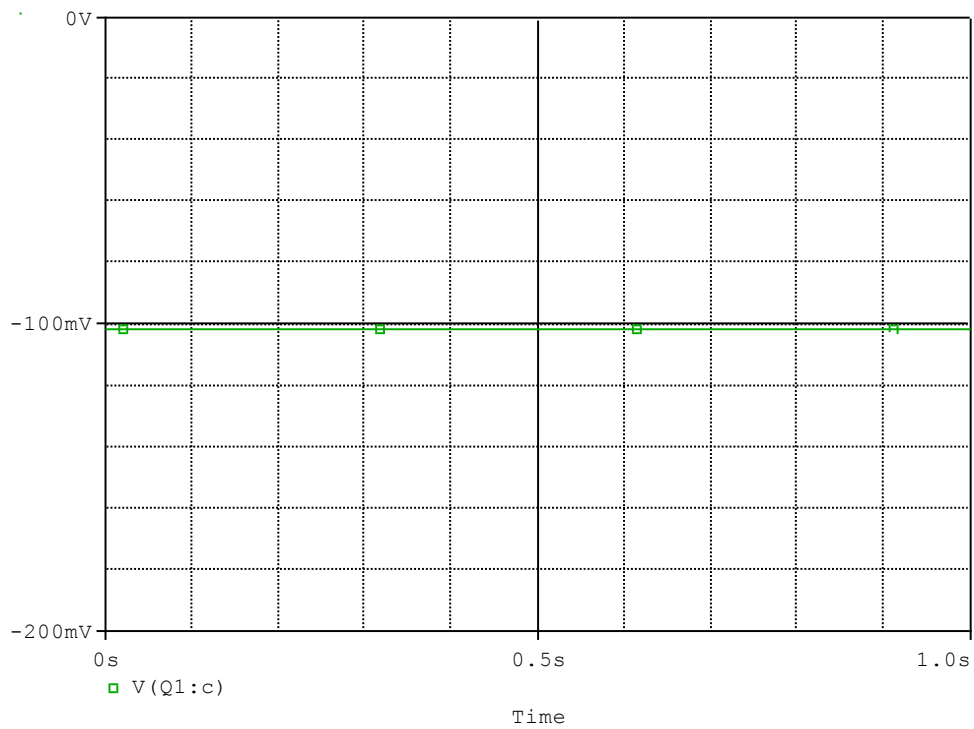


Comparison table

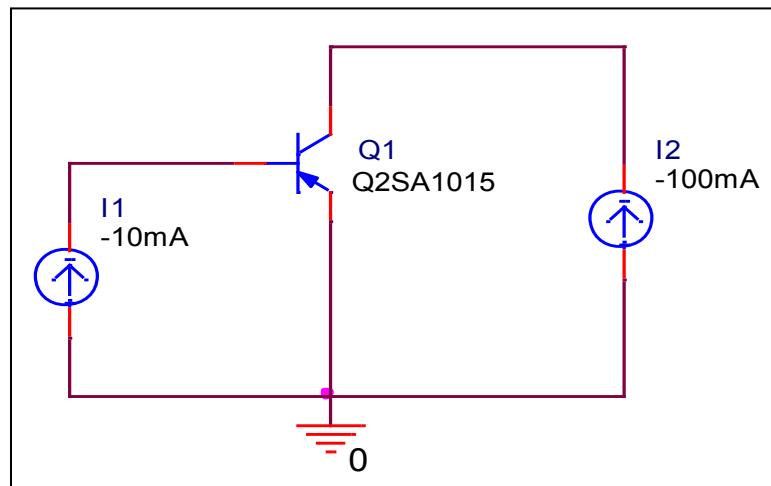
I_c (A)	h_{FE}		%Error
	Measurement	Simulation	
-0.001	190.000	193.958	2.083
-0.002	190.000	195.593	2.944
-0.005	190.000	196.106	3.214
-0.01	190.000	194.566	2.403
-0.02	190.000	190.320	0.168
-0.05	180.000	178.400	-0.889
-0.1	160.000	163.546	2.216

V_{CE(sat)}-I_c Characteristics

Simulation result



Evaluation circuit



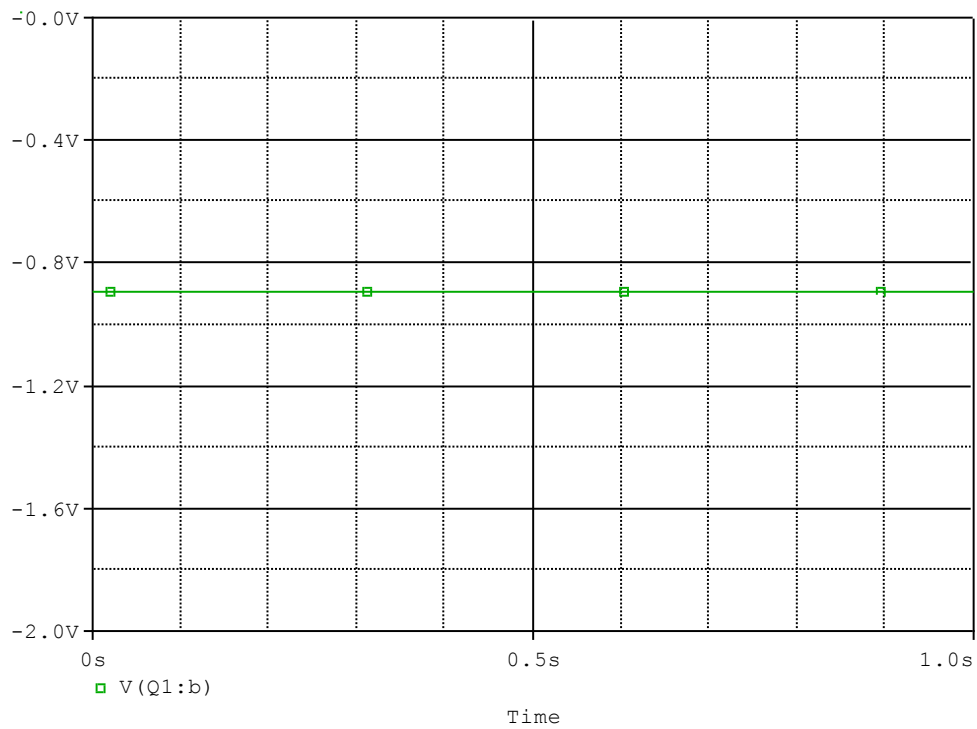
Comparison table

Test Condition: IC/IB = 10, IC=-100mA

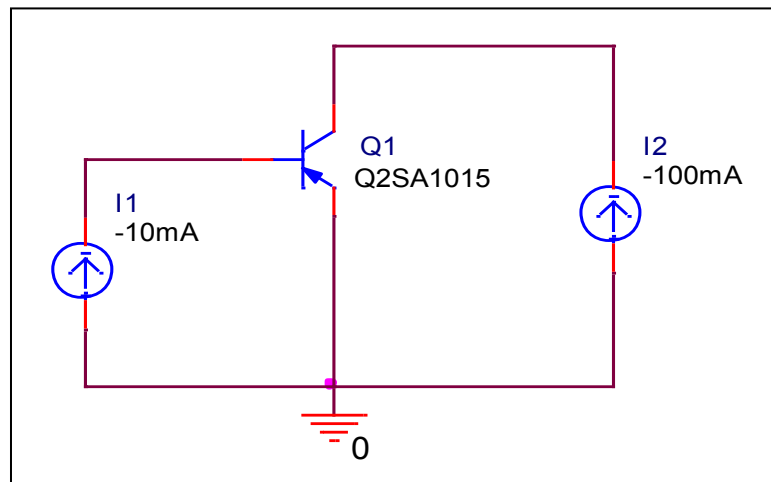
I _c (mA)	V _{CE(sat)} (V)		%Error
	Measurement	Simulation	
-100	-0.100	-0.101	1.000

$V_{BE(sat)}$ - I_c Characteristics

Simulation result



Evaluation circuit



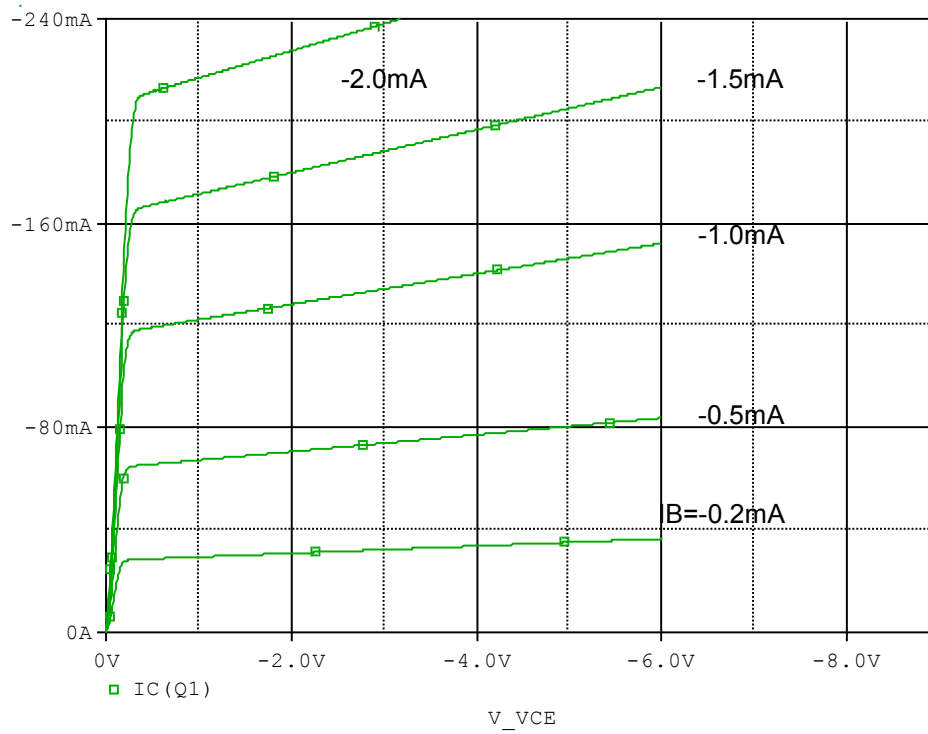
Comparison table

Test Condition: $I_C/I_B = 10$, $I_C = -100\text{mA}$

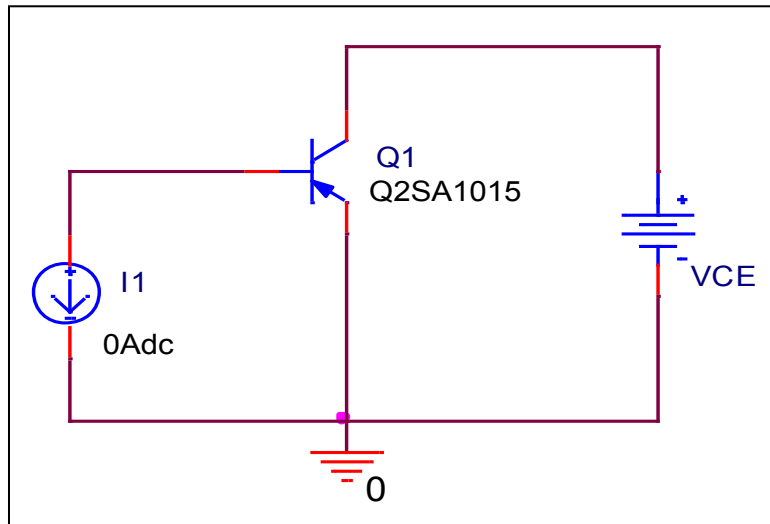
I_c (mA)	$V_{BE(sat)}$ (V)		%Error
	Measurement	Simulation	
-100	-0.900	-0.897	-0.333

Output Characteristics

Simulation result



Evaluation circuit



Output Characteristics

Reference

