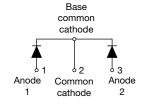


High Performance Schottky Rectifier New Generation 3, D-61 Package, 2 x 55 A

VS-112CNQ030APbF

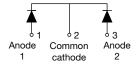




D-61-8

VS-112CNQ030ASMPbF

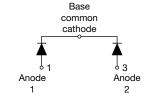




D-61-8-SM

VS-112CNQ030ASLPbF





D-61-8-SL

PRODUCT SUMMARY				
Package	D-61-8, D-61-8-SM, D-61-8-SL			
I _{F(AV)}	2 x 55 A			
V _R	30 V			
V _F at I _F	0.49 V			
I _{RM} max.	400 mA at 125 °C			
T _J max.	150 °C			
Diode variation	Common cathode			
E _{AS}	36 mJ			

FEATURES

- 150 °C T_J operation
- · Center tap module
- Very low forward voltage drop
- High frequency operation
- High power discrete
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and/or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details.

DESCRIPTION

The center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	110	A			
V _{RRM}		30	V			
I _{FSM}	t _p = 5 μs sine	5100	A			
V _F	55 A _{pk} , T _J = 125 °C (per leg)	0.39	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-112CNQ030APbF	UNITS		
Maximum DC reverse voltage	V_{R}	30	V		
Maximum working peak reverse voltage	V_{RWM}	30	V		



ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL TEST CONDITIONS		VALUES	UNITS				
Maximum average forward current	per leg	50 % duty ovelo et T = 121 °C rectongular way of arm			$_{\Delta M}$ 50 % duty cycle at T_C = 131 °C, rectangular waveform		55	A
See fig. 5	per device	I _{F(AV)}	50 % duty cycle at 1 _C = 131 °C, rectangular wavelonn		110			
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM}	5100	А		
			10 ms sine or 6 ms rect. pulse		880			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 8 A, L = 1.12 mH		36	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		8	Α		

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	55 A	T _J = 25 °C	0.49	V
Maximum forward voltage drop per leg		110 A		0.57	
See fig. 1		55 A	- T _J = 125 °C	0.39	
		110 A		0.51	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	3.5	mA
See fig. 2		T _J = 125 °C		400	IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		5100	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to case per leg		_	DC operation See fig. 4	0.5	°C/W
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.25	
Typical thermal resistance, case to heatsink (D-61-8 only)		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
Approximate weight				7.8	g
Approximate weight				0.28	OZ.
Mounting torque	minimum			40 (35)	kgf · cm
(D-61-8 only) maxir				58 (50)	(lbf · in)
Marking device			Case style D-61-8	112CN	Q030A
			Case style D-61-8-SM	112CNQ	030ASM
			Case style D-61-8-SL	112CNC	030ASL

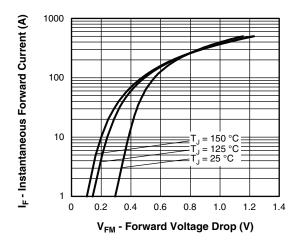


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

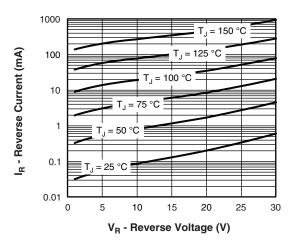


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

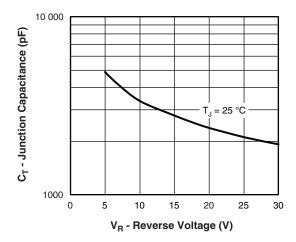


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

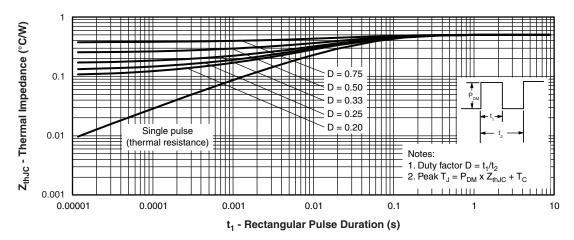


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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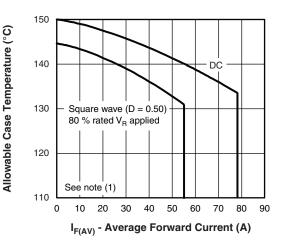


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

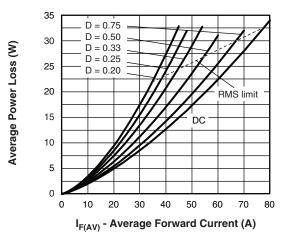


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

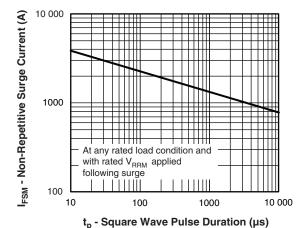


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

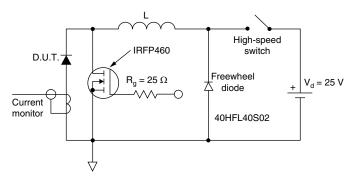


Fig. 8 - Unclamped Inductive Test Circuit

Note

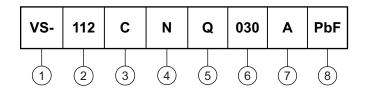
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

VS-112CNQ030APbF Series

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (110 A)

- Circuit configuration:

C = common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage rating (030 = 30 V)

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = standard production

• PbF = lead (Pb)-free

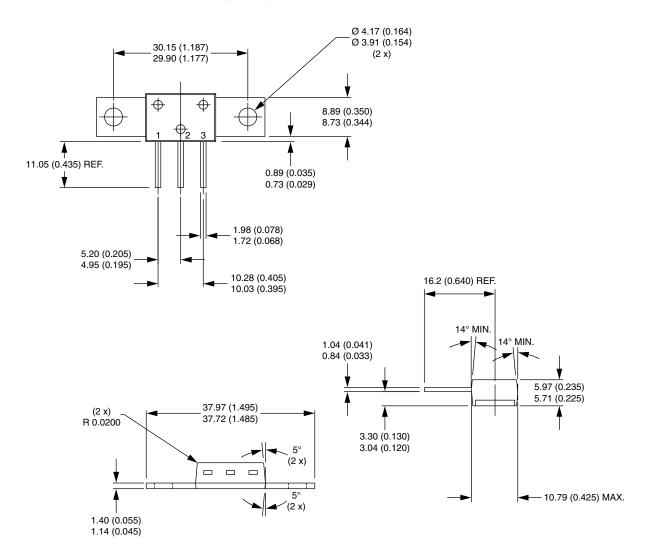
Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95354</u>					
Part marking information <u>www.vishay.com/doc?95356</u>					



D-61-8, D-61-8-SM, D-61-8-SL

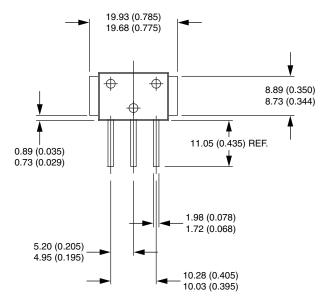
DIMENSIONS - D-61-8 in millimeters (inches)

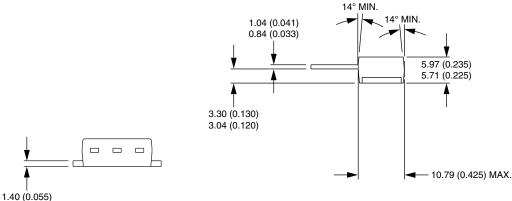




DIMENSIONS - D-61-8-SM in millimeters (inches)

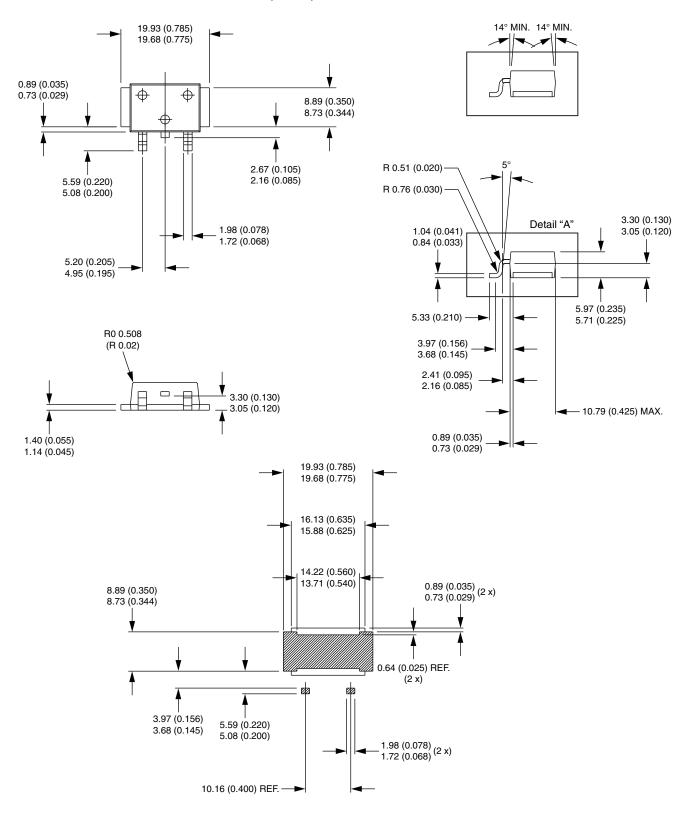
1.14 (0.045)







DIMENSIONS - D-61-8-SL in millimeters (inches)





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Vishay

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