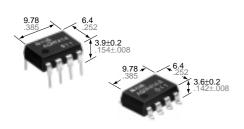


GU (General Use) Type 2-Channel (Form B) Type

PhotoMOS RELAYS

UL File No.: E43149 CSA File No.: LR26550

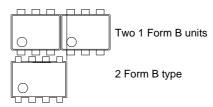


mm inch

FEATURES

1. Approx. 1/2 the space compared with the mounting of Two 1 Form B

PhotoMOS units



- 2. Applicable for 2 Form B use as well as two independent 1 Form B use
- 3. Low thermal electromotive force (Approx. 1 μ V)
- 4. Eliminates the need for a counter electromotive force protection diode in the drive circuits on the input side
- 5. Controls load currents up to 0.13 A with an input current of 5 mA
- 6. High speed switching: operate time typical of 300 µsec.
- 7. Eliminates the need for a power supply to drive the power MOSFET
- 8. Extremely low closed-circuit offset voltages to enable control of small analog signals without distortion
- 9. Surface-mount model available

TYPICAL APPLICATIONS

- High-speed inspection machines
- Telephone equipment
- Computer

TYPES

	Output rating*			Part		_		
	Load	Load	Through hole terminal	Su	rface-mount termi	Packing quantity		
	voltage current				Tape and ree	packing style		
	ronago		Tube pac	king style	Picked from the 1/2/3-pin side	Picked from the 4/5/6-pin side	Tube	Tape and reel
AC/DC type	400 V	100 mA	AQW414	AQW414A	AQW414AX	AQW414AZ	1 tube contains 40 pcs. 1 batch contains 400 pcs.	1,000 pcs.

^{*} Indicate the peak AC and DC values.

Note: For space reasons, the package type indicator "X" and "Z" are omitted from the seal.

RATING

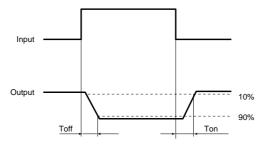
1. Absolute maximum ratings (Ambient temperature: 25°C 77°F)

	Item		Symbol	AQW414(A)	Remarks
	LED for	LED forward current		50 mA	
loout	LED rev	erse voltage	V_{R}	3 V	
Input	Peak for	ward current	I _{FP}	1 A	f = 100 HZ,Duty factor = 0.1 %
	Power dissipation		P _{in}	75 mA	
	Load vol	Load voltage		400 V	
Output	Continuous load current			0.1 A	Peak AC, DC I _L = Max. 0.13 A (when used for 1 From B)
·	Peak load current		I _{peak}	0.3 A	100 ms (1shot), V _L = DC
	Power dissipation		P _{out}	800 mW	
Tot	al power diss	sipation	P _T	850 mW	
I/O	I/O isolation voltage		V _{iso}	1,500 V AC	Between input and output/between contact sets
Tampara	_ Operating		T _{opr}	-20°C to +80°C −4°F to +176°F	Non-condensing at low temperatures
rempera	ture limits	Storage	T _{stg}	-40°C to +100°C -40°F to +212°F	

2. Electrical characteristics (Ambient temperature: 25°C 77°F)

	Item		Symbol	AQW414(A)	Condition
	LED operate (OFF) current	Minimum Typical Maximum	I _{Foff}	0.7 mA 3 mA	I _L = 100 mA
Input	LED reverse (ON) current	Minimum Typical Maximum	I _{Fon}	0.4 mA 0.64 mA	I _L = 100 mA
	LED dropout voltage	Minimum Typical Maximum	VF	1.14 V (1.25 V at I _F = 50 mA) 1.5 V	I _F = 5 mA
Output	On resistance	Minimum Typical Maximum	R _{on}	26 Ω 50 Ω	$I_F = 0 \text{ mA}$ $I_L = 100 \text{ mA}$ Within 1 s on time
Output	Off state leakage current	Minimum Typical Maximum	_	1 μΑ	I _F = 5 mA V _L = 400 V
	Operate (OFF) time*	Minimum Typical Maximum	T _{off}	0.46 ms 1 ms	$I_F = 0 \text{ mA} \rightarrow 5 \text{ mA}$ $I_L = 100 \text{ mA}$
Transfer characteristics	Reverse (ON) time*	Minimum Typical Maximum	T _{on}	0.40 ms 1 ms	$I_F = 5 \text{ mA} \rightarrow 0 \text{ mA}$ $I_L = 100 \text{ mA}$
	I/O capacitance	Minimum Typical Maximum	C _{iso}	0.8 pF 1.5 pF	f = 1 MHz V _B = 0
	Initial I/O isolation resistance	Minimum Typical Maximum	R _{iso}	1,000 ΜΩ	500 V DC

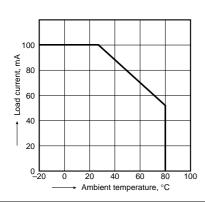
*Operate/Reverse time



REFERENCE DATA

1. Load current vs. ambient temperature characteristics
Allowable ambient temperature: -20°C to +80°C

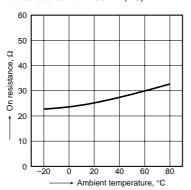
-4°F to +176°F



2. On resistance vs. ambient temperature characteristics
Measured portion: between terminals 5 and 6, 7 and 8;

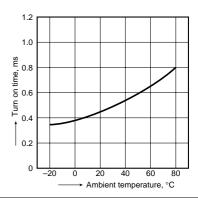
LED current: 0 mA;

Continuous load current: 100 mA (DC)



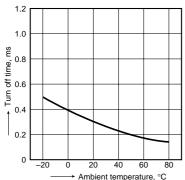
3. Turn on time vs. ambient temperature

characteristics LED current: 5 mA; Load voltage: 400 V (DC); Continuous load current: 100mA (DC)

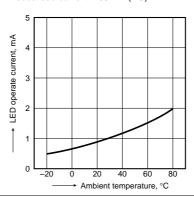


4. Turn off time vs. ambient temperature characteristics

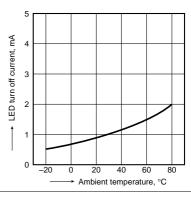
LED current: 5 mA; Load voltage: 400 V (DC); Continuous load current: 100 mA (DC)



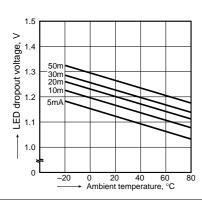
5. LED operate current vs. ambient temperature characteristics Load voltage: 400 V (DC); Continuous load current: 100 mA (DC)



6. LED turn off current vs. ambient temperature characteristics Load voltage: 400 V (DC); Continuous load current: 100 mA (DC)

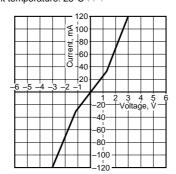


7. LED dropout voltage vs. ambient temperature characteristics LED current: 5 to 50 mA



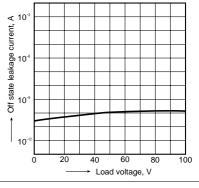
8. Voltage vs. current characteristics of output at MOS portion

Measured portion: between terminals 5 and 6, 7 and 8; Ambient temperature: 25°C 77°F



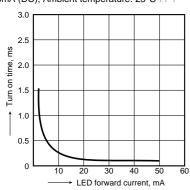
9. Off state leakage current

Measured portion: between terminals 5 and 6, 7 and 8; Ambient temperature: 25°C 77°F



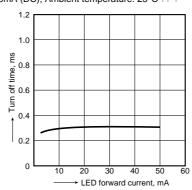
10. LED forward current vs. turn on time characteristics

Measured portion: between terminals 5 and 6, 7 and 8; Load voltage: 400V (DC); Continuous load current: 100mA (DC); Ambient temperature: 25°C 77°F



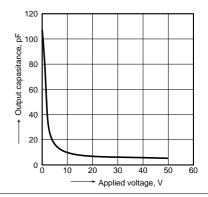
11. LED forward current vs. turn off time characteristics

Measured portion: between terminals 5 and 6, 7 and 8; Load voltage: 400V (DC); Continuous load current: 100mA (DC); Ambient temperature: 25°C 77°F



12. Applied voltage vs. output capacitance characteristics

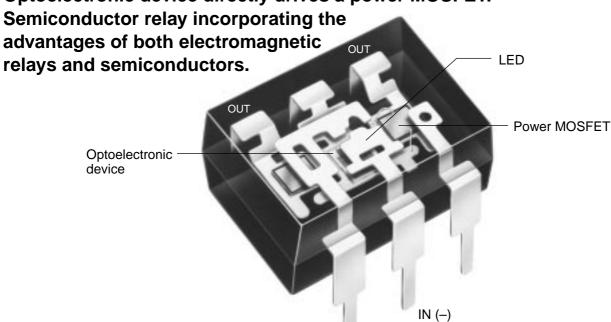
Measured portion: between terminals 5 and 6, 7 and 8; Frequency: 1 MHz; Ambient temperature: 25°C 77°F



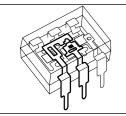
PhotoMOS Relay Technical Information

How PhotoMOS Relays Operate:

Optoelectronic device directly drives a power MOSFET.



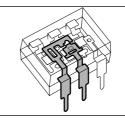
When operated



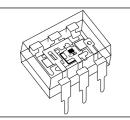
When a signal current flows to the input terminals the LED on the input side emits light.

When turned off

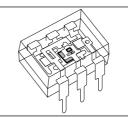
IN (+)



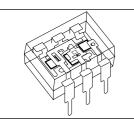
When the signal current at the input terminal is cut off, the LED stops emitting light.



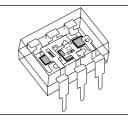
The emitted light passes through transparent silicon and reaches the photoelectric element (solar cell) which is mounted opposite the LED.



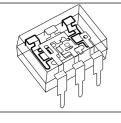
When the emitted light from the LED stops, the voltage of the photoelectric element decreases.



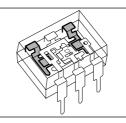
The photoelectric element converts the received light to a voltage corresponding to the quantity of light. This voltage passes through a control circuit and charges the MOSFET gate on the output side.



When the voltage supplied from photoelectric element decrease, the control circuit rapidly discharges the gate charge of MOSFET.



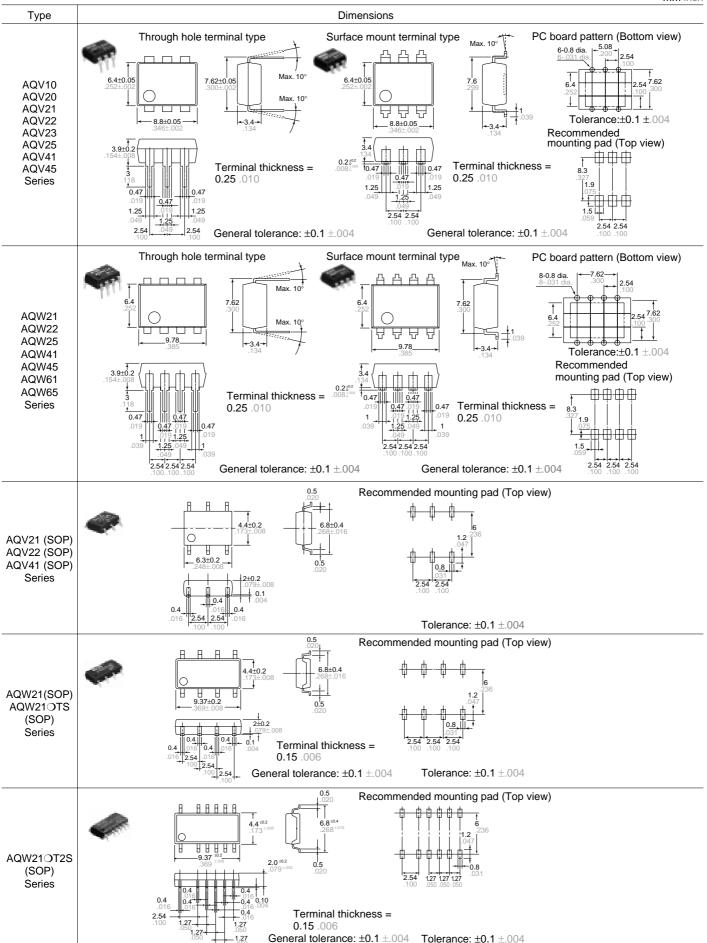
When the MOSFET gate voltage supplied from the photoelectric element reaches a preset voltage value, the MOSFET begins to conduct and turns on the load.

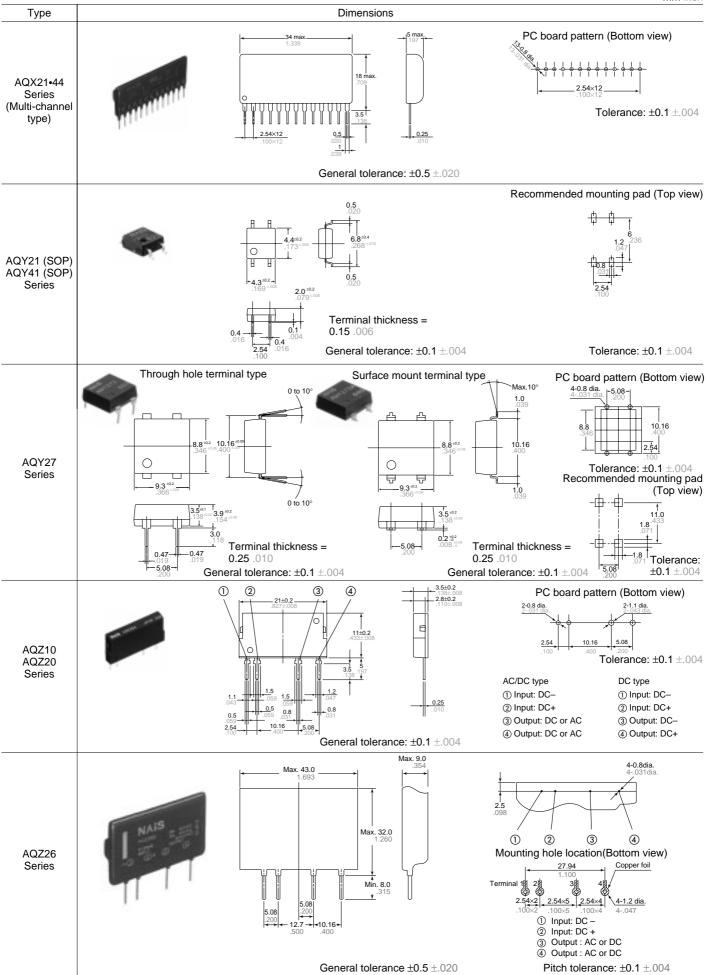


This control circuit makes MOSFET stop conducting and immediately turns off the load.

PhotoMOS Relay Dimensions

mm inch





Terminology

	Term	Symbol	Description
	LED forward current	lF	Current that flows between the input terminals when the input diode is forward biased.
	LED reverse voltage	V_{R}	Reverse breakdown voltage between the input terminals.
	Peak forward current	I_{FP}	Maximum instantaneous value of the forward current.
Input	LED operate current	I _{FON}	Current when the output switches on (by increasing the LED current) with a designated supply voltage and load connected between the output terminals.
	LED turn off current	I _{FOff}	Current when the output switches off (by decreasing the LED current) after operating the relay with a designated supply voltage and load connected between the output terminals.
	LED dropout voltage	V_{F}	Dropout voltage between the input terminals due to forward current.
	Power dissipation	Pin	Allowable power dissipation between the input terminals.
	Load voltage	V_{L}	Supply voltage range at the output used to normally operate the PhotoMOS relay. Represents the peak value for AC voltages.
	Continuous load current I _L		Maximum current value that flows continuously between the output terminals of the PhotoMOS relay under designated ambient temperature conditions. Represents the peak value for AC current.
Output	On resistance R ₀		Obtained using the equation below from dropout voltage V_{DS} (on) between the output terminals (when a designated LED current is made to flow through the input terminals and the designated load current through the output terminals.) $R_{OD} = V_{DS}$ (on)/ I_{L}
	Off state leakage current leak		Current flowing to the output when a designated supply voltage is applied between the output terminals with no LED current flow.
	Power dissipation	Pout	Allowable power dissipation between the output terminals.
	Turn on time Ton		Delay time until the output switches on after a designated LED current is made to flow through the input terminals.
	Turn off time	T _{off}	Delay time until the output switches off after the designated LED current flowing through the input terminals is cut off.
	I/O capacitance	Ciso	Capacitance between the input and output terminals.
	Output capacitance	Cout	Capacitance between output terminals when LED current does not flow.
Electrical characteristics	I/O isolation resistance	Riso	Resistance between terminals (input and output) when a specified voltage is applied between the input and output terminals.
	Total power dissipation	PT	Allowable power dissipation in the entire circuit between the input and output terminals.
	I/O isolation voltage	Viso	Critical value before dielectric breakdown occurs, when a high voltage is applied for 1 minute between the same terminals where the I/O isolation resistance is measured.
	Operating temperature	Topr	Ambient temperature range in which the PhotoMOS relay can operate normally with a designated load current conditions.
	Storage temperature	T _{stg}	Ambient temperature range in which the PhotoMOS relay can be stored without applying voltage.

Reliability tests

Classification	Item	Condition	Purpose		
	High temperature storage test	T _{stg} (Max.)	Determines resistance to long term storage at high temperature.		
	Low temperature storage test	T _{stg} (Min.)	Determines resistance to long term storage at low temperature.		
Life tests	High temperature and high humidity storage test	85°C 185°F, R.H. 85%	Determines resistance to long term storage at high temperature and high humidity.		
	Continuous operation life test	VL = Max., IL = Max., IF = LED operate current (Max.)	Determines resistance to electrical stress (voltage and current).		
Thermal	Temperature cycling test	Low storage temperature (T _{stg} Min.) High storage temperature (T _{stg} Max.)	Determines resistance to exposure to both low temperatures and high temperatures.		
environment tests	Thermal shock test	Low temperature (0°C) (32°F), High temperature (100°C) (212°F)	Determines resistance to exposure to sudden changes in temperature.		
	Solder burning resistance	260±5°C 500±41°F, 10 s	Determines resistance to thermal stress occurring while soldering		
	Vibration test	196 m/s ² {20 G}, 20 to 2,000 Hz* ¹	Determines the resistance to vibration sustained during shipment or operation.		
Mechanical	Shock test	9,800 m/s ² {1,000 G} 0.5 ms* ² ; 4,900 m/s ² {500 G} 1 ms	Determines the mechanical and structural resistance to shock.		
environment tests	Drop test	Dropped at a height of 80 cm on oak board	Determines the mechanical resistance to drops sustained during shipment or operation.		
	Terminal strength test	Determined from terminal shape and cross section	Determines the resistance to external force on the terminals of th PhotoMOS relay mounted on the PC board while wiring or operating.		
	Solderability	230°C 446°F 5 s (with soldering flux)	Evaluates the solderability of the terminals.		
	•	*1.40.	a FF LIT at dauble amplitude of 2 mm for Dawer DhataMOC relays		

 $^{^{*1}}$ 10 to 55 Hz at double amplitude of 3 mm for Power PhotoMOS relays. *2 4,900 m/s², 1 ms for Power PhotoMOS relays.

PhotoMOS Relay Schematic and Wiring Diagrams

Туре	Schematic	Output configura-	Load	Con- nection	Wiring diagram
			AC/DC	А	E ₁ T F 2
AQV21 AQV21 (SOP) AQV22 AQV22 (SOP)	1 6 6 0 5 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	1a	DC	B*	$E_1 \xrightarrow{\frac{1}{ F }} \underbrace{\frac{1}{2}}_{0} \underbrace{\frac{1}{ L }}_{0} \underbrace{\frac{1}$
AQV23 AQV25 Series	1 6				E ₁ T F ₂ 2 5 Load - 4 L _L V _L (DC) 5 1 Load + C
	(AQV254R only)		DC	С	$E_1 \xrightarrow{F} 2$ 3 0 0 0 0 0 0 0 0 0 0
AQW21 AQW21 (SOP) AQW22 AQW25	1 2 2 3 6 4 5 5	7 7 6 2a		_	(1) Two independent 1 Form A use E ₁
Series					E ₁ F ₂ S C C C C C C C C C
AQW21OTS Series	* 1	Relay portion 1a Detecter portion 1a	Relay portion AC/DC Detecter portion DC	_	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$
AQW21OT2S Series	10 12 13 13 14 15 16 16 17 18 18 18 18 18 18 18 18 18 18 18 18 18	Relay portion 1a Detecter portion 2a	Relay portion AC/DC Detecter portion DC	_	$E_1 \xrightarrow{I_{F1}} 2$ $Load 3$ $(DC) \bigvee_{L2} I_{L2} 4$ $Load 5$ $(DC) \bigvee_{L3} I_{L3} 6$ $I_{F3} \xrightarrow{I_{F3}} E_3$ $I_{F3} \xrightarrow{I_{F3}} E_3$ $I_{F3} \xrightarrow{I_{F3}} E_3$

^{*}Can be also connected as 2 Form A type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

**Can be also connected as 2 Form B type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

Notes: 1. E₁: Power source at input side; V_{IN}: Input voltage; I_F: LED forward current; V_L: Load voltage; I_L: Load current; R: Current limit resistor.

2. Method of connecting the load at the output is devided into 3 types.

Туре	Schematic	Output configuration	Load	Con- nection	Wiring diagram
			AC/DC	A	E1 T F 2
AQV41 AQV41 (SOP) AQV45	1 6 5 5	1b	DC	B**	$E_1 \xrightarrow{\frac{1}{F}} \underbrace{\frac{1}{2}}_{\text{Load}} \underbrace{\frac{6}{5}}_{\text{Li}} \underbrace{\frac{1}{V_L(DC)}}_{\text{Load}} \underbrace{\frac{6}{5}}_{\text{Load}} \underbrace{\frac{1}{V_L(DC)}}_{\text{Load}}$
Series	3 4				$E_1 \xrightarrow{\overline{JF}} 2$ 3 1 4 L_L $V_L(DC)$ 4 L_Dad 1 1 1 1 1 1 1 1 1 1
			DC	С	E ₁ T F ₂ C C C C C C C C C C C C C C C C C C C
AQW61 AQW65 Series	1 N.C. 3 2 7 7 3 6 6 0 4 N.O. 5	1a1b	AC/DC	_	(1) Two independent 1 Form A & 1 Form B use E1
					$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$
AQW41 AQW45	1 8 8 ° 7 ° 7 ° 8 ° 6 ° 6 ° 5 ° 5 ° 6 ° 5 ° 6 ° 7 ° 7 ° 7 ° 7 ° 7 ° 7 ° 7 ° 7 ° 7	2b	AC/DC	_	(1) Two independent 1 Form B use E1
Series					$\begin{bmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 $
AQV10 Series	Terminal 3 cannot be used, since it is in the internal circuit of the relay.	1a	DC	A	E ₁ T F 2

^{*}Can be also connected as 2 Form A type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

**Can be also connected as 2 Form B type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

Notes: 1. E₁: Power source at input side; V_{IN}: Input voltage; I_F: LED forward current; V_L: Load voltage; I_L: Load current; R: Current limit resistor.

2. Method of connecting the load at the output is devided into 3 types.

Туре	Schematic	Output configura-tion	Load	Con- nection	Wiring diagram
	1 6 6 5 5 3 4 4 0		AC/DC	A	$V_{\text{IN}} \xrightarrow{\frac{1}{ F }} \underbrace{\frac{1}{2}}_{\text{Load}} \underbrace{\frac{6}{5}}_{\text{Load}} \underbrace{\frac{6}{1}}_{\text{Load}} \underbrace{\frac{6}_{1}}_{\text{Load}} \underbrace{\frac{6}_{1}}_{\text{Load}} \frac$
AQV20 Series		1a	DC	B*	$V_{\text{IN}} \xrightarrow{\text{I}} \begin{array}{c} 1 \\ \text{I} \\ \text{I} \end{array} \begin{array}{c} 0 \\ \text{I} \end{array} \begin{array}{c} 0 \\ \text{I} \\ \text{I} \end{array} \begin{array}{c} 0 \\ \text{I} \end{array} \begin{array}{$
	Terminal 3 cannot be used, since it is in the internal circuit of the relay.		DC	С	$V_{\text{IN}} \xrightarrow{\text{IF}} 2$ 3 0 0 0 0 0 0 0 0 0 0
AQX21•44 Series (Multi- channel type)	(a) Input Common: DC+ (b) Input 1: DC- (c) Input 1: DC- (d) Input 3: DC- (d) Input 3: DC- (d) Input 3: DC- (e) Input 4: DC- (e) Output 4: DC- (e) Output 4: DC- (f) Output 2: (IN.O.): (f) Cor AC (g) Output 2: (IN.O.): (g) Cor AC (h) Output 3: (IN.O.): (g) Output 4: (IN.O.): (g) Output 4: (IN.O.): (g) Output 3: (IN.O.): (g) Output 4: (IN.O.): (4a	AC/DC		E1
AQY21 (SOP) AQY27 Series		1a	AC/DC	_	E ₁ T _{IF} 2 Load V _L (AC,DC) 3 D _{Load} V _L (AC,DC) 3 D _{Load} V _L (AC,DC)
AQY41 (SOP) Series		1b	AC/DC	_	E ₁ T I _F 2 Load V _L (AC,DC) 3 Load V _L (AC,DC)

^{*}Can be also connected as 2 Form A type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

**Can be also connected as 2 Form B type. (However, the sum of the continuous load current should not exceed the absolute maximum rating.)

Notes: 1. E₁: Power source at input side; V_{IN}: Input voltage; I_F: LED forward current; V_L: Load voltage; I_L: Load current; R: Current limit resistor.

2. Method of connecting the load at the output is devided into 3 types.

Туре	Schematic	Output configura-tion	Load	Wiring diagram
AQZ20 Series		1a	AC/DC	O 1 2 3 4 Load VL (AC or DC) Load VL (AC or DC)
AQZ10 Series	1 2 3 4 - +	1a	DC	O 1 2 3 4 Load +
AQZ20·V Series	1 2 3 4 - +	1a	AC/DC	Todd VL (AC or DC)
AQZ20·D Series	1 2 3 4	1a	AC/DC	O 1 2 3 4 Load VL (AC or DC)
AQZ10·D Series	1 2 3 4 - + - +	1a	DC	O 1 2 3 4 Load +
AQZ26 Series	1 2 3 4	1a	AC/DC	The second secon

Notes: 1. E₁: Power source at input side; V_{IN}: Input voltage; I_F: LED forward current; V_L: Load voltage; I_L: Load current; R: Current limit resistor. 2. Method of connecting the load at the output is devided into 3 types.

PhotoMOS Relay Cautions for Use

SAFETY WARNINGS

- Do not use the product under conditions that exceed the range of its specifications. It may cause overheating, smoke, or fire.
- Do not touch the recharging unit while the power is on. There is a danger of electrical shock. Be sure to turn off the power when performing mounting, maintenance, or repair operations on the relay (including connecting parts such as the terminal board and socket).
- Check the connection diagrams in the catalog and be sure to connect the terminals correctly. Erroneous connections could lead to unexpected operating errors, overheating, or fire.

NOTES

■ PhotoMOS Relays excluding Power PhotoMOS Relays

1. Unused terminals

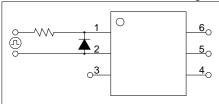
The No. 3 terminal is used with the circuit inside the relay. Therefore, do not connect it to the external circuitry with either connection method A, B or C.

2. Short across terminals

Do not short circuit between terminals when relay is energized, since there is the possibility of breaking the internal IC.

3. Surge voltages at the input

If reverse surge voltages are present at the input terminals, connect a diode in reverse parallel across the input terminals and keep the reverse voltages be- low the reverse breakdown voltage.



4. Recommended LED forward current (IF)

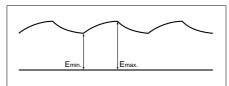
It is recommended that the LED forward current (IF) of each PhotoMOS Relay should be set according to the following table.

Туре		Product name	Recommended LED forward current (I _F)			
		AQV10,20 Series	10 mA			
		AQY27 Series*	5 to 10 mA			
DIP SMD type	Standard I/O isolation type (1,500 V AC)	AQV21 Series (including SOP) AQV22 Series (including SOP) AQV25 Series AQV45 Series AQW21 Series (including SOP) AQW21OTS,T2S Series AQW41 Series AQW61 Series AQW25 Series AQW25 Series AQW45 Series AQW45 Series AQW45 Series AQW45 Series AQW45 Series AQW45 Series	5 mA			
		AQV23 Series	2 mA			
	Reinforced I/O isolation (5,000 V AC)	AQV21,41 Series AQV25,45 Series	5 to 10 mA			
SIL	AQX21 Se	eries	5 mA			
type	AQZ10,20	,26 Series	5 to 10 mA			
*Standard I/O insolation type (2 500 V AC)						

*Standard I/O insolation type (2,500 V AC)

5. Ripple in the input power supply If ripple is present in the input power supply, observe the following:

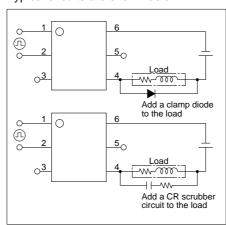
- 1) For LED operate current at E_{min}, maintain the value mentioned in the table of "Note 4. Recommended LED forward current (I_F)."
- 2) Keep the LED operate current at 50 VmA (25 mA for PhotoMOS HE Relay with LED display type) or less at E_{max}.



6. Output spike voltages

1) If an inductive load generates spike voltages which exceed the absolute maximum rating, the spike voltage must be limited.

Typical circuits are shown below.



2) Even if spike voltages generated at the load are limited with a clamp diode if the circuit wires are long, spike voltages will occur by inductance. Keep wires as short as possible to minimize inductance.

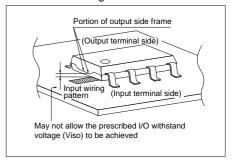
7. Cleaning solvents compatibility

Dip cleaning with an organic solvent is recommended for removal of solder flux, dust, etc. Select a cleaning solvent from the following table. If ultrasonic cleaning must be used, the severity of factors such as frequency, output power and cleaning solvent selected may cause loose wires and other defects. Make sure these conditions are correct before use. For details, please consult us.

CI	Compatibility (O: Yes) ×: No	
Chlorine- base	I.I.I. Trichloroethlene (Chloroethlene) Trichloroethlene (Trichlene) Perchloroethlene Methlene chloride	O
Adueous	• Indusco 624, 1000 • Hollis 310 • Lonco Terg	0
Alcohol- base	IPA Ethanol	0
Others	Thinner Gasoline	×

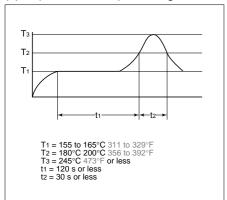
8. INPUT WIRING PATTERN

With AQY or AQW types, avoid installing the input (LED side) wiring pattern to the bottom side of the package if you require the specified I/O isolation voltage (Viso) after mounting the PC board. Since part of the frame on the output side is exposed, it may cause fluctuations in the I/O isolation voltage.



9. Soldering

- 1) When soldering PC board terminals, keep soldering time to within 10 s at 260°C 500°F.
- 2) When soldering surface-mount terminals, the following conditions are recommended.
- (1) IR (Infrared reflow) soldering method



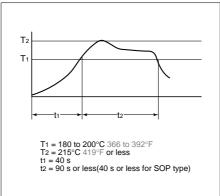
(4) Soldering iron method

Tip temperature: 280 to 300°C 536 to

572°F

Wattage: 30 to 60 W Soldering time: within 5 s

(2) Vapor phase soldering method

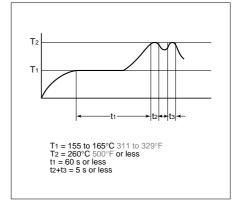


(5) Others

Check mounting conditions before using other soldering methods (hot-air, hot plate, pulse heater, etc.)

• The temperature profile indicates the temperature of the soldered terminal on the surface of the PC board. The

(3) Double wave soldering method



ambient temperature may increase excessively. Check the temperature under mounting conditions.

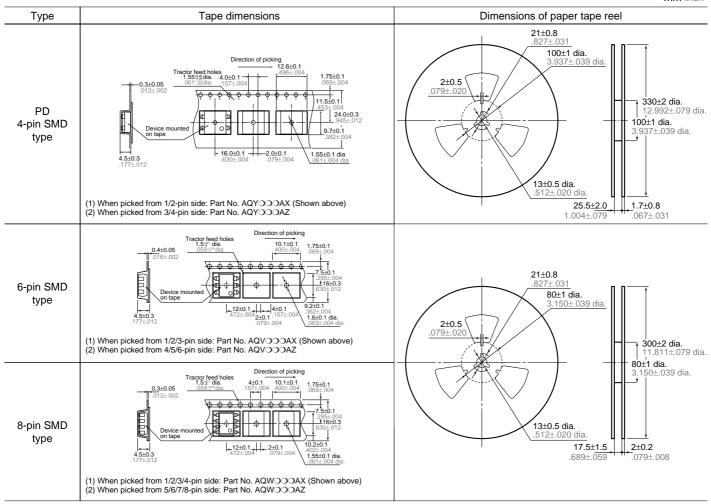
• The conditions for the infrared reflow soldering apply when preheating using the VPS method.

10. The following shows the packaging format

1) Tape and reel

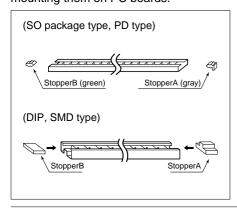
Туре Tape dimensions Dimensions of paper tape reel 0.3±0.05 21±0.3 80+1 dia)39 <u>dia</u> SO package 4-pin type 2±0.5 12±0.3 2±0.1 250±2 dia. 9.843±.079 dia. (1) When picked from 1/2-pin side: Part No. AQYODOSX (Reel color: blue) (Shown above) (2) When picked from 3/4-pin side: Part No. AQYODOSZ (Reel color: orange) 80±1 dia. 3.150±.039 dia. Direction of picking 4±0.1 7.2±0.1 0.3±0.06 φ φ φ φ SO package 13+0.5 dia 6-pin type 14±1.5 2±0.5 12±0.3 2±0.1 .55±0.1 dia .079±.020 (1) When picked from 1/2/3-pin side: Part No. AQVOOOSX (Reel color: blue) (Shown above) (2) When picked from 4/5/6-pin side: Part No. AQVOOOSZ (Reel color: orange) Tractor feed hole: 1.55±0.05 dia 1.75±0.1 0.3±0.05 21±0.8 80±1 dia SO package .295±.00-±16±0.3 39 dia 8-pin type 4±0.1 2±0.5 2.8±0.3 3±.004 1.55±0.1 dia. **250±2 dia.** 9.843±.079 dia. (1) When picked from 1/2/3/4-pin side: Part No. AQWOOOSX (Shown above) (2) When picked from 5/6/7/8-pin side: Part No. AQWOOOSZ 80±1 dia. 3.150±.039 dia. Direction of pic 0.3±0.05 SO package . 13+0.5 dia 12-pin type 4±0.1 17.5±1.5 2±1 2.8±0.3 .079±.039 (1) When picked from 1/2/3/4/5/6-pin side; Part No. AQWOOOT2SX (Shown above) (2) When picked from 7/8/9/10/11/12-pin side: Part No. AQWOOT2SZ

mm inch



2) Tube

(1) Devices are packaged in a tube so pin No. 1 is on the stopper B side. Observe correct orientation when mounting them on PC boards.



(2) Storage

PhotoMOS relays implemented in SO packages are sensitive to moisture and come in sealed moisture-proof packages. Observe the following cautions on storage.

- After the moisture-proof package is unsealed, take the devices out of storage as soon as possible (within 1 month at the most).
- If the devices are to be left in storage for a considerable period after the moisture-proof package has been unsealed, it is recommended to keep them in another moisture-proof bag containing silica gel (within 3 months at

the most).

11. Transportation and storage

- Extreme vibration during transport will warp the lead or damage the relay.
 Handle the outer and inner boxes with care.
- 2) Storage under extreme conditions will cause soldering degradation, external appearance defects, and deterioration of the characteristics. The following storage conditions are recommended:
- Temperature: 5 to 30°C 41 to 86°F
- Humidity: Less than 60% R.H.
- Atomosphere: No harmful gasses such as sulfurous acid gas, minimal dust.

■ Power PhotoMOS Relays

1.-1) Input LED current (Standard type and Internal varistor type)

For rising and dropping ratio of input LED current (di/dt), maintain min. 100 µA/s.

1.-2) Input voltage (Voltage sensitive type)

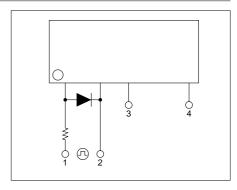
For rising and dropping ratio of input voltage (dv/dt), maintain min. 100 mV/s.

2. Short across terminals

Do not short circuit between terminals when relay is energized, since there is possibility of breaking of the internal IC.

3. Surge voltages at the input

If reverse surge voltages are present at the input terminals, connect a diode in reverse parallel across the input terminals and keep the reverse voltages be low the reverse breakdown voltage.



4. Recommended load voltage

As a guide in selecting PhotoMOS Relays, please refer to the following table.

1) Power photoMOS relays

	T	Absolute rat	Recom- mended		
	Туре	Load voltage	Load current	load voltage	
	AQZ202	Peak AC 60 V	Peak AC 3.0 A	12 V AC; 5, 12,24 V DC	
AC/DC type	AQZ205	Peak AC 100 V	Peak AC 2.0 A	24 V AC 48 V DC	
AC/D0	AQZ207	Peak AC 200 V	Peak AC 1.0 A	48 V AC 100 V DC	
	AQZ204	Peak AC 400 V	Peak AC 0.5 A	100 V AC 200 V DC	
	AQZ102	60 V DC	4.0 A DC	5,12,24 V DC	
DC type	AQZ105	100 V DC	2.6 A DC	48 V DC	
DC t	AQZ107	200 V DC	1.3 A DC	100 V DC	
	AQZ104	400 V DC	0.7 A DC	200 V DC	

2) Power PhotoMOS relay high capacity type

	Tuna	Absolute rat	Recom- mended		
	Туре	Load voltage	Load current	load voltage	
S e	AQZ262	Peak AC, DC 60V	Peak AC, DC 6A	12V AC 5,12,24V DC	
AC/Detay	AQZ264	Peak AC, DC 400V	Peak AC, DC 1A	AC100V DC200V	

3) Power photoMOS relays (Voltage sensitive type)

Tuno		Absolute rat	Recom- mended								
	Type	Load voltage	Load current	load voltage							
	AQZ202D	Peak AC 60 V	Peak AC 2.7 A	12 V AC; 5, 12,24 V DC							
AC/DC type	AQZ205D	Peak AC 100 V	Peak AC 1.8 A	24 V AC 48 V DC							
	AQZ207D	Peak AC 200 V	Peak AC 0.9 A	48 V AC 100 V DC							
	AQZ204D	Peak AC 400 V	Peak AC 0.45 A	100 V AC 200 V DC							
	AQZ102D	60 V DC	3.6 A DC	5,12,24 V DC							
DC type	AQZ105D	100 V DC	2.3 A DC	48 V DC							
BC t	AQZ107D	200 V DC	1.1 A DC	100 V DC							
	AQZ104D	400 V DC	0.6 A DC	200 V DC							

4) Power photoMOS relays with internal varistor type

Туре		Absolute rat	Recom- mended load voltage	
		Load Load voltage current		
AC/DC type	AQZ202V	17 V AC 22 V DC	Peak AC 3.0 A	12 V AC; 5,12 V DC
	AQZ205V	30 V AC 38 V DC	Peak AC 2.0 A	24 V AC 48 V DC
	AQZ207V	60 V AC 85 V DC	Peak AC 1.0 A	48 V AC 100 V DC
	AQZ204V	140 V AC 180 V DC	Peak AC 0.5 A	100 V AC 200 V DC

5.-1) Ripple in the input power supply (Standard type and high capacity type and internal varistor type)

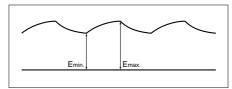
If ripple is present in the input power supply, observe the following:

- 1) For LED operate current at E_{min}, maintain min. 5 mA
- 2) Keep the LED operate current at 50 mA or less at E_{max} .

5.-2) Ripple in the input power supply (Voltage sensitive type)

If ripple is present in the input power supply, observe the following:

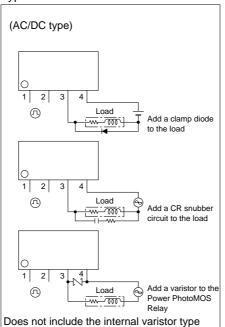
- 1) For input voltage at Emin, maintain min. 4 V
- 2) Keep input voltage at 30 V or less at Emax.



6. Output spike voltages

1) If an inductive load generates spike voltages which exceed the absolute maximum rating, the spike voltage must he limited

Typical circuits are shown below.



2) Even if spike voltages generated at the load are limited with a clamp diode if the circuit wires are long, spike voltages will occur by inductance. Keep wires as short as possible to minimize inductance.

7. Adjacent mounting

1) When relavs are mounted close together with the heat-generated devices, ambient temperature may rise abnormally. Mounting layout and ventilation should be considered.

2) When many relays are mounted close together, load current should be reduced. (Refer to the date of "Load current vs. ambient temperature characteristics in adjacent mounting.")

8. Cleaning solvents compatibility

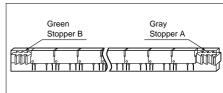
Dip cleaning with an organic solvent is recommended for removal of solder flux. dust, etc. Select a cleaning solvent from the following table. If ultrasonic cleaning must be used, the severity of factors such as frequency, output power and cleaning solvent selected may cause loose wires and other defects. Make sure these conditions are correct before use. For details, please consult us.

CI	eaning solvent	Compatibility (O: Yes) ×: No				
Chlorine- base	I.I.I. Trichloroethlene (Chloroethlene) Trichloroethlene (Trichlene) Perchloroethlene Methlene chloride	O				
Adueous	• Indusco 624, 1000 • Hollis 310 • Lonco Terg	О				
Alcohol- base	IPA Ethanol	0				
Others	• Thinner					

9. Soldering

When soldering PC board terminals, keep soldering time to within 10 s at 260°C 500°F

10. Packing style



The power photoMOS relavs are stick packed so that the number 1 terminal is in the direction of stopper B.

One stick contains 25 power photoMOS relays.

11. Transport and storage

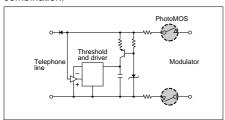
- 1) If the product is subject to extreme vibration during transport, the lead may warp or the main unit may become damaged. Handle the outer and inner boxes with care.
- 2) If the storage environment is extremely bad, it may give rise to deterioration of the soldering, external appearance defects, and degradation the characteristics of the product. The following conditions are recommended for the storage location:
- Temperature: 5 to 30°C 41 to 86°F
- Humidity: Less than 60% RH
- Environment: No hazardous substances such as sulfurous acid gases, and little dust.

PhotoMOS Relays for Various Applications



Automatic meter reading

The needs of centralized remote meter reading systems for water, gas and electricity in medium and high rise apartments and new subdivisions are now increasing. PhotoMOS relays are capable of controlling from low level signals up to power signals and feature low leakage current and noise from the optoelectronic device and power MOSFET combination.

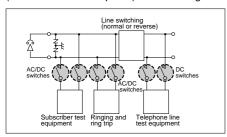




Telecommunications

A variety of signals, with levels from millivolts (at microamperes) to tens of volts (at several hundred milliamperes), AC or DC, and even high bit-rate signals, can be superimposed on telephone lines, the heart of

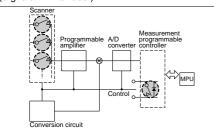
telecommunication networks. The switches in telecommunication circuits, which normally carry DC signals, also carry AC signals on top of the DC level when an intermittent signal (e.g. ringer signal) is being sent. PhotoMOS relays are capable of controlling small level (millivolts at microamperes) AC or DC signals.





Instrumentation

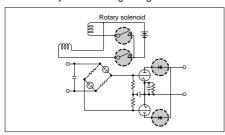
With the spread of microcomputer chips, the latest instruments are required to measure a variety of signals at high speeds under various conditions. PhotoMOS relays are recommended for measurement scanning functions, automatic zero-point compensation to eliminate zero-point error, and measurement sequence interfaces (e.g. alarm interface.)





Medical equipment

Medical equipment which processes low level signals includes electrocardiographs, electroencephalographs, and X-ray CT scanners. PhotoMOS relays accurately transfer low level signals (less than several hundred millivolts). Furthermore, they are also convenient in driving rotary solenoids such as those used to automatically switch voltage ranges.

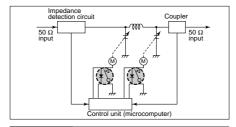




Communications equipment

The future of communications is in satellite communications. Satellite-communications feature many advantages such as indifference to terrestrial disasters, wide service areas, simple circuit modification and simultaneous conversations. An important control operation in communications equipment is fast automatic tuning.

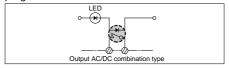
PhotoMOS relays can easily be connected in parallel, difficult with conventional transistor type. As a result, a variety of circuit connection are possible and power circuits can also be designed.





Programmable controller

The output circuit of a programmable controller requires various interfaces to match the load type. Recently, as the computing speed and data processing speed increase, problems may arise from noise at the input interface as well as at the output interface. PhotoMOS relays are resistant to inrush current (due to phase shift) and eliminate the need for snubber circuits as long as they are operated within the ratings. Furthermore, use of PhotoMOS relays decreases the mounting area requirements, resulting in more compact programmable controllers.



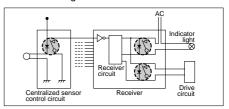


Security Equipment

There are many types of security systems from home and office security to building security. PhotoMOS relays are ideal for use as input interfaces for system sensors and output interfaces for alarms.

Input interface: Low leakage current makes use possible for low level voltage and current input.

Output interface: Outputs either AC or DC up to a load voltage of 400 V.



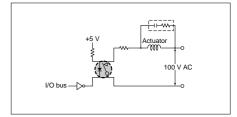


OA equipment

OA equipment usually contains a sensor control unit (for temperature, speed, torque, etc.), drive unit, power supply unit, and a processing unit which controls the overall system. It is organized similarly to compact factory

automation machinery. PhotoMOS relays have wide application in the interfaces for signals which connect the functions of these units.

- Operates on a 24 mW input to enable direct control of C-MOS devices.
- Signal transfer through optical coupling achieves high resistance to noise and transients, eliminating the need for adding a snubber circuit to the output to control the load voltage.
- Advantages in the total cost and reliability in the control system result from the absence of AC leakage current related to the snubber circuit.



If you are a user experiencing difficulty with solid-state relays and triacs:

If you would like to control small analog signals with a photocoupler and solid-state relays.

If you require a device with a small leakage current (as opposed to bipolar devices having large internal leakage currents).

If you would like to directly control analog signals and you would like a device integrating a photocoupler, driver and analog IC to simplify the circuit as much as possible.

If you require a snubber circuit with a triac or solid-state relay, but are concerned about the snubber circuit's AC leakage current

If you require a device for AC control that is resistant to ambient temperature changes and input signal noise.

PhotoMOS relays feature low offset voltages and on resistances of 0.25 Ω or less. (AQV251 Connection)

PhotoMOS relays have leakage currents in the order of microamperes and can control up to 1500 V (peak). (AQV258)

PhotoMOS relays contain all of these functions in a single package.

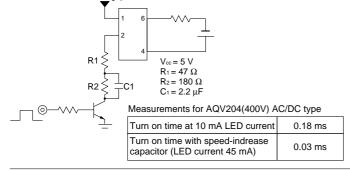
Furthermore, circuit design is simplified as a power supply is unnecessary since the internal optoelectronic device directly drives the power MOSFET.

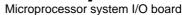
PhotoMOS relays are resistant to transients and as long as they are operated within the maximum ratings, eliminate the need for adding a snubber circuit to the output to control the rise in load voltage. Leakage current ceases to be a problem, with cost and reliability being other advantages.

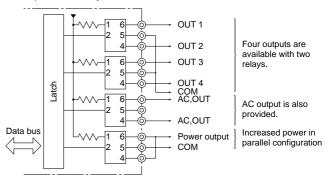
PhotoMOS relays do not employ the self-trigger mechanism used in SCRs and triacs. Therefore, they do not switch on accidentally. Furthermore, the noise suppression characteristics of optoelectronic devices make them highly resistant to ambient noise for operation at temperatures up to 80°C 176°F.

PhotoMOS Relay Application Examples

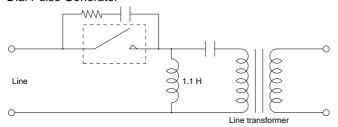
High Response Speed



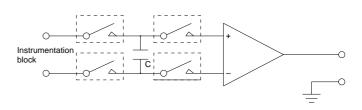




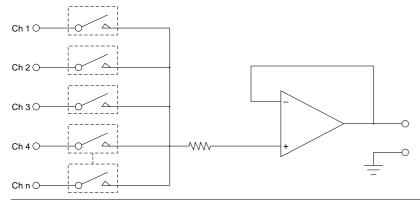




Capacitor Switch Circuit



Scanner



Part No. vs. Load Voltage Quick Reference

PhotoMOS Relays

Form A Type

Group	Down No.	Pack-	Number of channels	Load voltage	40 V	60 V	100 V (RF:80 V)	200 V	250 V	350 V	400 V	600 V	1000 V	1500 V										
name	Part No.	age style		Third digit	1	2	5	7	3	0	4	6	9	8										
AQV25O	AQV25O		1-channel	Standard Viso	AQV251	AQV252	AQV255	AQV257	AQV253		AQV254 AQV254R		AQV259	AQV258										
		DIP		High Viso					AQV253H		AQV254H													
	AQW25O		2-channels	Standard Viso							AQW254													
HE Soft ON/OFF type	AQV25OM	DIP	1-channel	Standard Viso				AQV257M																
	AQV21O		1-channel	Standard Viso		AQV212	AQV215	AQV217		AQV210	AQV214	AQV216												
		DIP		High Viso							AQV214H													
	AQW21O		2-channels	Standard Viso		AQW212	AQW215	AQW217		AQW210	AQW214	AQW216												
	AQX2144O	SIL	4-channels	Standard Viso							AQX21444													
GU	AQY21OS		1-channel (4 pin)	Standard Viso						AQY210S	AQY214S													
GU	AQV21OS	SOP	1-channel (6 pin)	Standard Viso		AQV212S	AQV215S	AQV217S		AQV210S	AQV214S	AQV216S												
	AQW21OS		2-channels	Standard Viso						AQW210S	AQW214S													
	AQW21OTS		2-channels (MOSFET+ optocoupler)	Standard Viso						AQW210TS														
	AQW21O T2S	SOP	3-channels (MOSFET+ 2optocouplers)	Standard Viso						AQW210T2S														
GU-E	AQV21OE	DIP	DIP	1-channel	Standard Viso						AQV210E	AQV214E												
	7.4.2.32														1 Gridinio	High Viso						AQV210EH	AQV214EH	
RF	AQV22O	DIP	1-channel	Standard Viso	AQV221		AQV225																	
	AQV22ON	DID	1-channel	Standard Viso			AQV225N	AQV227N			AQV224N													
RF Low-ON	AQW22ON	DIP	2-channels	Standard Viso			AQW225N	AQW227N			AQW224N													
type	AQV22ONS	SOP	1-channel	Standard Viso			AQV225NS	AQV227NS			AQV224NS													
HS	AQV23O	DIP	1-channel	Standard Viso							AQV234													
-	AQV10O		1-channel	Standard Viso	AQV101	AQV102			AQV103		AQV104													
HF	AQV20O	DIP	1-channel	Standard Viso	AQV201	AQV202			AQV203		AQV204													
PD	AQY27O	DIP	1-channel	Standard Viso		AQY272	AQY275	AQY277			AQY274													
Form P	Type				-	1		Form A	Form B	Type	1	l		1										

Form B Type

Group	Part No.	Package	Number of	Load voltage 300		400 V
name	Part No.	style	channels	Third digit	3	4
HE	10/450		1-channel	Standard Viso	AQV453	AQV454
	AQV45O	DIP	1-channel	High Viso		AQV454H
	AQW45O		2-channels	Standard Viso		AQW454
	AQV41O	DID	1-channel	Standard Viso		AQV414
011	AQW41O	DIP	2-channels	Standard Viso		AQW414
GU	AQY41OS		1-channel (4-pin)	Standard Viso		AQY414S
	AQV41OS	SOP	1-channel (6-pin)	Standard Viso		AQV414S
GU-E	AQV41OE	DIP	1-channel	Standard Viso		AQV414E
GO-L	AQV41OL	Dii	1-Chamilei	High Viso		AQV414EH

Form A Form B Type

Group	Part No.	Package	Number of	Load voltage	400 V	
namė	Рап по.	style	channel	Third digit	4	
HE	AQW65O	DIP	2-channel	Standard Viso	AQW654	
GU	AQW61O	DIP	2-channel	Standard Viso	AQW614	

Power PhotoMOS Relays Form A Type

Group name	Part No.	Package style	Number of channels	Load voltage	40 V	60 V	100 V	200 V	250 V	350 V	400 V		
	Fait No.			Third digit	1	2	5	7	3	0	4		
Standard type	AQZ10O					AQZ102	AQZ105	AQZ107			AQZ104		
	AQZ20O					AQZ202	AQZ205	AQZ207			AQZ204		
Varistor incorporated type	AQZ20OV	SIL	1-channel	Standard		AQZ202V	AQZ205V	AQZ207V			AQZ204V		
Voltage sensitive type	AQZ10OD		1-Chamilei	Viso	Viso	VISO		AQZ102D	AQZ105D	AQZ107D			AQZ104D
	AQZ20OD					AQZ202D	AQZ205D	AQZ207D			AQZ204D		
High capacity type	AQZ26O					AQZ262					AQZ264		

O stands for third digit. Notes: 1. Standard Viso: 1,500 V between I/O. 2.High Viso: 5,000 V between I/O.