統合開発環境 MD-Studioを活用した MD6602のCHEWING GUMの DCDCコンバータのプログラム書き込み

2018年4月5日

## (1) MD-Studioを起動する [File]->[Open File]

新しいフォルダー						8== 🗸	
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MDStudio-1.1.							

(2)ソースコー	ドの確認
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Explorer 23	LC main.c La naroware.n L .project LC main.c 💥	- U <u>6</u> .	Problems Q Com	isole 🛄 Propertie	s S Serial Port 23	Memory an Module	
DC	2* // MD6682	^	Port Name	Baud Rate	Reset	Board Type	Description
Includes	14 #include "defines.h"	1	COM1	300000	Non Reset	A-Type(Echo back)	-141
asm	15 #include "hardware.h"		COM2	200000	Non Poret	A Type(Echo back)	-64
s crtxinit.s	16 #include "dsp.h"			300000	Non Reset	A Type(Echo back)	Ciliana Laba C
eader	17		COMIS	300000	INON Keset	A-Type(Echo back)	Silicon Labs Ci
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h] dsp_init.h							
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.c] main.c	27 return;						
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	29⊖ void isr_02 (void)interrupt ( 2)using (BANK_INTL)						
	30 {						
	31 return;						
	24 } 339 wold ise 03 (wold) interput ( 3) using (RANK INTL)						
	34 {						
	35 return;						
	36 }						
	37⊖ void isr_04 (void)interrupt ( 4)using (BANK_INTL)						
	38 {						
	49 b						
	410 void isr 05 (void) interrupt ( 5) using (BANK INTL)						
	42 {						
	43 return;						
	44 }						
	450 void isr_06 (void)interrupt ( 6)using (BANK_INTL)						
	40 {						
	48 }						
	49⊖ void isr 07 (void) interrupt ( 7) using (BANK INTL)						
	50 {						
	51 return;						
	52 }						
	54 Vold 157 08 (Vold)interrupt ( 8)using (BARK_INIL)						
aritariar 🔒 Hirton 😒 😐 🗖	55 return:						
istories in History 23	56 }						
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	58 {						
	so return;						
	51 word ise 10 (word) interpret (10) using (RANK INTH)						
	62 {						
	63 return;						
	64 }						
	650 void isr_11 (void)interrupt (11)using (BANK_INTL)						
	67 return;						
	69 void isr 12 (void) interrupt (12) using (BANK INTL)						
	70 {						
	71 return;						
	72 }						
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	/* 1 75 return:						
	76 }						
	77@ void isr_14 (void)interrupt (14)using (BANK_INTL)						
	78 {						
	79 return;						
	80 }						
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## (3)Build Allを実施 [Project]->[Build All]

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 	Git Repositories History X	56 }		
CickAblickie	🗞 🧟 🛃 👘 🔺	570 void isr_09 (void)interrupt ( 9)using (BANK_INTL)		
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● 1 void isr_12 (void)interrupt (13)using (BANK_INTL)         ● 2 (i void)interrupt (12)using (BANK_INTL)         ● 3 (i void)interrupt (12)using (BANK_INTL)         ● 4 (i void)interrupt (13)using (BANK_INTL)         ● 7 (i return;         ● 9 (void isr_13 (void)interrupt (14)using (BANK_INTL)         ● 7 (i return;         ● 7 (i return;         ● 9 (void isr_13 (void)interrupt (14)using (BANK_INTL)         ● 7 (i return;         ● 9 (void isr_13 (void)interrupt (15)using (BANK_INTL)         ● 1 ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ● ●		60 }		
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 		66 {		
● void isr_12 (void)interrupt (12)using (BAWK_INTL)         76       return;         73       void isr_13 (void)interrupt (13)using (BAWK_INTL)         74       return;         75       return;         75       return;         76       return;         77       void isr_14 (void)interrupt (14)using (BAWK_INTL)         76       return;         77       void isr_15 (void)interrupt (14)using (BAWK_INTL)         76       return;         81       void isr_15 (void)interrupt (15)using (BAWK_INTL)         72       return;         81       0         72       xickAb/Cdgg         12       1223         1323       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234         12       1234 </td <td></td> <td>67 return; 68 }</td> <td></td> <td></td>		67 return; 68 }		
70 {       return;         72 }       return;         73 void isr_13 (void)interrupt (13)using (BAIK_IIITL)       74 {         75 }       return;         76 }       return;         77 void isr_14 (void)interrupt (14)using (BAIK_IIITL)       76 {         77 void isr_15 (void)interrupt (15)using (BAIK_IIITL)       76 {         78 {       return;         81 void isr_15 (void)interrupt (15)using (BAIK_IIITL)       78 {         82 {       2 {         10 @       @       @       0 @		69⊖ void isr_12 (void)interrupt (12)using (BANK_INTL)		
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## (4) プログラムの書き込み [Project]->[Write ihx file]

- Paul Data					
300000	Reset Non Reset	Board Type A-Type(Echo back)	Description zMI	Device	Status
300000	Non Reset	A-Type(Echo back)	z[M]		
300000	Non Reset	A-Type(Echo back)	Silicon Labs CP210x USB to UART	MD6602	Run
	300000 300000 300000	300000         Non Reset           300000         Non Reset           300000         Non Reset	300000     Non Reset     A-Type(Echo back)       300000     Non Reset     A-Type(Echo back)       300000     Non Reset     A-Type(Echo back)	300000     Non Reset     A-Type(Echo back)     zM        300000     Non Reset     A-Type(Echo back)     zM        300000     Non Reset     A-Type(Echo back)     Silicon Labs CP210x USB to UART	300000     Non Reset     A-Type(Echo back)     zM        300000     Non Reset     A-Type(Echo back)     zM        300000     Non Reset     A-Type(Echo back)     Silicon Labs CP210x USB to UART

Save Connection

Connection

## COM3を選択して、Connectionボタンをクリックする



Cancel

(5) MD6602のCHEWING GUMをデジタル電源ボード【MSE-MD6602-DPS】 と合体します。



(6) デジタル電源ボード【MSE-MD6602-DPS】の出力電圧を確認する⇒1.8[V]

