## QUAD SINGLE-SUPPLY OPERATIONAL AMPLIFIER

#### GENERAL DESCRIPTION

JRC

The NJM2902 consists of four independent high-gain operational amplifiers that are designed for single-supply operation.

Operation from split power supplies is also possible and the low power supply drain is independent of the magnitude of the power supply voltage.

Used with a dual supply the circuit will operate over a wide range of supply voltages. However, a large amount of crossover distortion may occur with loads to ground. An external current-sinking resistor to-V<sub>S</sub> will reduce crossover distortion.

There is no crossover distortion problem in single-supply operation if the load is direct-coupled to ground.

#### ■ FEATURES

- Single Supply
- Operating Voltage (+3V~+30V)
- High Output Voltage (V<sup>+</sup>-2V)
- Slew Rate
- Low Operating Current
- Package Outline
- Bipolar Technology

#### ■ PIN CONFIGURATION

NJM2902N, NJM2902M NJM2902E, NJM2902V

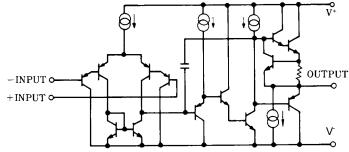
	<b>A</b> "	PIN FUNCTION	
2	13	1.A OUTPUT	8.C OUTPUT
		2.A-INPUT	9.C -INPUT
rd'		3.A +INPUT	10.C +INPUT
<b>1</b>	"	4.V <sup>+</sup>	11.V <sup>-</sup>
	10	5.B +INPUT	12.D +INPUT
	ᢤᡒ᠊ᡱ᠋	6.B –INPUT	13.D -INPUT
J.Y	۲Ľ	7.B OUTPUT	14.D OUTPUT
<b>L</b>	8		

(0.5V/µs typ.)

DIP14, DMP14, EMP14, SSOP14

(1mA typ.)

#### ■ EQUIVALENT CIRCUIT (1/4 Shown)



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#### ■ PACKAGE OUTLINE



NJM2902N



NJM2902M

NJM2902E

NJM2902V



#### ■ ABSOLUTE MAXIMUM RATINGS

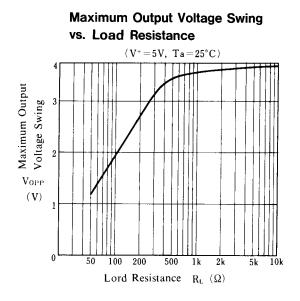
			( Ta=25°C )	
PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V <sup>+</sup> (V <sup>+</sup> /V <sup>-</sup> )	32 ( or ± 16 )	V	
Differential Input Voltage	VID	32	V	
Input Voltage	VIC	V-0.3~V+32	V	
Power Dissipation	PD	(DIP14)570 (DMP14)300 (EMP14)300 (SSOP14)300	mW	
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C	
Storage Temperature Range	T <sub>stg</sub>	-50~+125	С°	

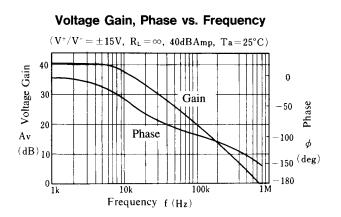
#### ■ ELECTRICAL CHARACTERISTICS

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PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>	R <sub>s</sub> =0Ω	-	2	10	mV
Input Offset Current	I <sub>IO</sub>		-	5	50	nA
Input Bias Current	IB	IIN <sup>+</sup> or IIN <sup>-</sup>	-	20	500	nA
Large Signal Voltage Gain	Av	R <sub>L</sub> >2kΩ	-	100	-	V/mV
Maximum Output Voltage Swing	V <sub>OM</sub>	R <sub>L</sub> =2kΩ	3.5	-	-	V
Input Common Mode Voltage Range	VICM		0~3.5	-	-	V
Common Mode Rejection Ratio	CMR		-	85	-	dB
Supply Voltage Rejection Ratio	SVR		-	100	-	dB
Output Source Current	ISOURCE	V <sub>IN</sub> <sup>+</sup> =1V,V <sub>IN</sub> <sup>-</sup> =0V	20	40	-	mA
Output Sink Current	I <sub>SINK</sub>	V <sub>IN</sub> <sup>+</sup> =0V,V <sub>IN</sub> <sup>-</sup> =1V	8	20	-	mA
Channel Separation	CS	f=1k~20kHz,Input Referred	-	120	-	dB
Operating Current	Icc	R∟=∞	-	1	2	mA
Slew Rate	SR	V <sup>+</sup> /V <sup>-</sup> =±15V	-	0.5	-	V/µs
Gain Bandwidth Product	GB	V <sup>+</sup> /V <sup>-</sup> =±15V	-	0.5	-	MHz

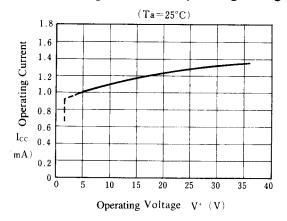
( Ta=25°C,V⁺=5V )

#### TYPICAL CHARACTERISTICS



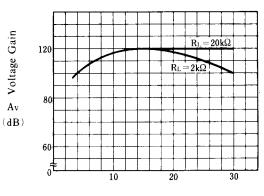


**Operating Current vs. Operating Voltage** 



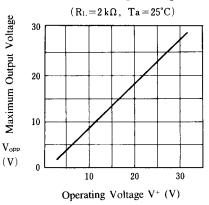
Input Offset Voltage vs. Temperature  $(V^+ = 5V)$ 5 4 Input Offset Voltage 3 2 1 0 -1 -2-- 3 Vio -4  $(\mathbf{m}\mathbf{V})$ - 5 -50-25 0 25 50 75 100 125 Ambient Temperature Ta (°C)

Voltage Gain vs. Operating Voltage

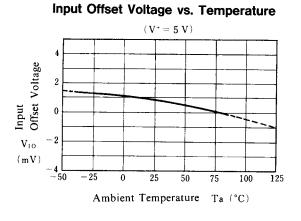


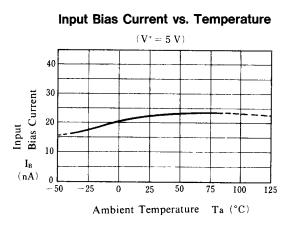
Operating Voltage  $V^+$  (V)

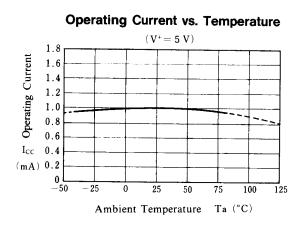
# Maximum Output Voltage vs. Operating Voltage

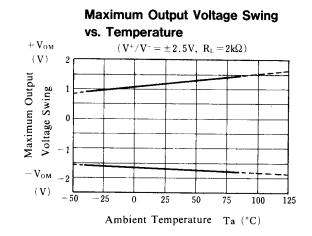


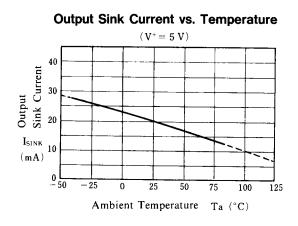
### ■ TYPICAL CHARACTERISTICS



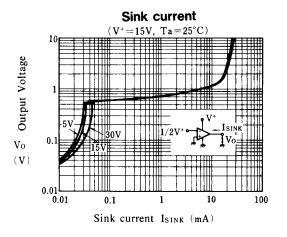


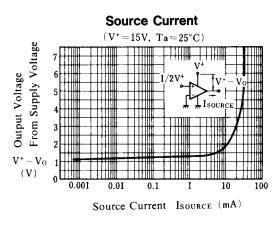


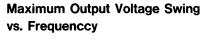


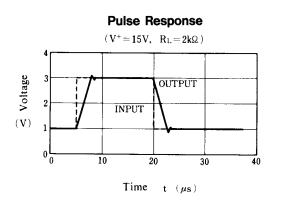


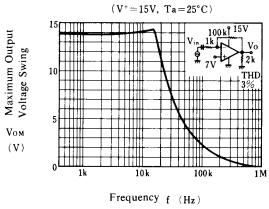
#### ■ TYPICAL CHARACTERISTICS







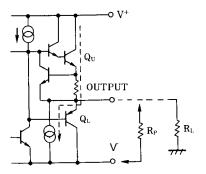




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#### ■ APPLICATION

Improvement of Cross-over Distortion Equivalent circuit at the output stage



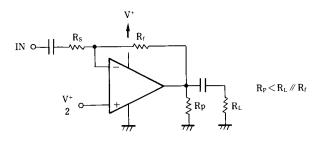
IN  $\sim$   $R_s$   $R_t$   $R_t$ 

NJM2902,in its static state ( No in and output condition ) when design,  $Q_U$  being biassed by constant current ( break down beam ) yet,  $Q_L$  stays OFF.

While using with both power source mode, the cross-over distortion might occur instantly when Q<sub>L</sub> ON.

There might be cases when application for amplifier of audio signals, not only distortion but also the apparent frequency bandwidth being narrowed remarkably.

It is adjustable especially when using both power source mode, constantly to use with higher current on  $Q_U$  than the load current (including feedback current), and then connect the pull-down resister  $R_P$  at the part between output and V pins.



[CAUTION]

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