

# PMEG4050EP

# 5 A low V<sub>F</sub> MEGA Schottky barrier rectifier Rev. 01 — 14 September 2009

**Product data sheet** 

## **Product profile**

## 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

#### 1.2 Features

- Average forward current: I<sub>F(AV)</sub> ≤ 5 A
- Reverse voltage: V<sub>R</sub> ≤ 40 V
- Low forward voltage
- High power capability due to clip-bond technology
- AEC-Q101 qualified
- Small and flat lead SMD plastic package

## 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications

#### 1.4 Quick reference data

Table 1. Quick reference data  $T_i = 25 \,^{\circ}C$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; $f$ = 20 kHz; $T_{sp} \le$ 130 °C	-	-	5	Α
$V_R$	reverse voltage		-	-	40	V
$V_{F}$	forward voltage	$I_F = 5 A$	-	430	490	mV
$I_R$	reverse current	$V_R = 40 V$	-	60	300	μΑ



## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	cathode	[1]	. [4]
2	anode	1 2	1 1 2
		<u> </u>	sym001

<sup>[1]</sup> The marking bar indicates the cathode.

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG4050EP	-	plastic surface-mounted package; 2 leads	SOD128

## 4. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4050EP	AF

## 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_R$	reverse voltage	$T_j = 25  ^{\circ}C$	-	40	V
I <sub>F(AV)</sub>	average forward current	square wave; $\delta$ = 0.5; $f$ = 20 kHz			
		T <sub>amb</sub> ≤ 0 °C	<u>[1]</u> _	5	Α
		T <sub>sp</sub> ≤ 130 °C	-	5	Α
I <sub>FSM</sub>	non-repetitive peak forward current	square wave; t <sub>p</sub> = 8 ms	[2] _	70	Α
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25  ^{\circ}C$	[3][4]	625	mW
			[3][5]	1050	mW
			[3][1]	2100	mW

Table 5. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-55	+150	°C
T <sub>stg</sub>	storage temperature		-65	+150	°C

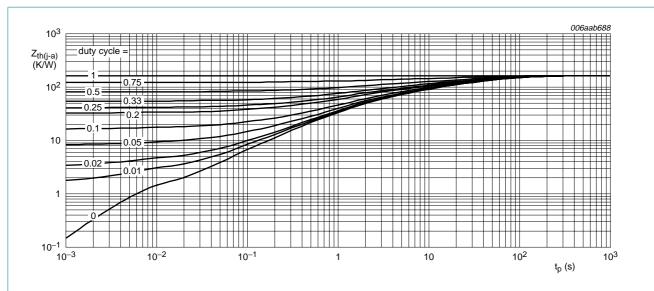
- [1] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [2]  $T_i = 25$  °C prior to surge.
- [3] Reflow soldering is the only recommended soldering method.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

#### 6. Thermal characteristics

Table 6. Thermal characteristics

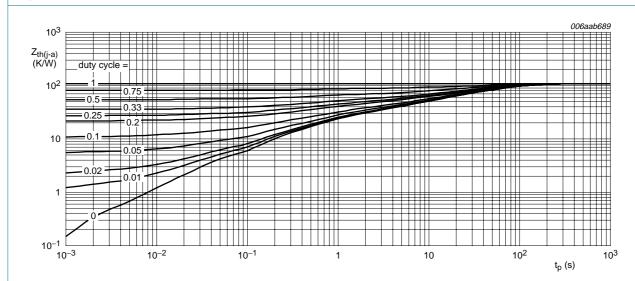
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
· · · · · · · · · · · · · · · · · · ·	thermal resistance from	in free air	[1][2]			
	junction to ambient		[3]	-	200	K/W
			<u>[4]</u> -	-	120	K/W
			<u>[5]</u> _	-	60	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		<u>[6]</u> _	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P<sub>R</sub> are a significant part of the total power losses.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.
- [5] Device mounted on a ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint.
- [6] Soldering point of cathode tab.



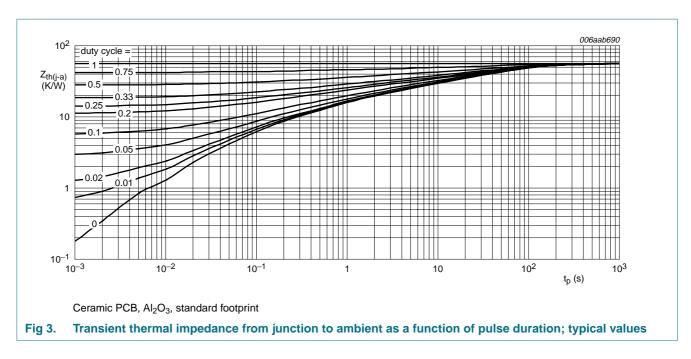
FR4 PCB, standard footprint

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode 1 cm<sup>2</sup>

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

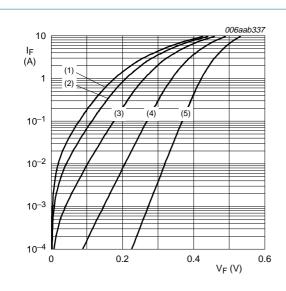


## 7. Characteristics

Table 7. Characteristics

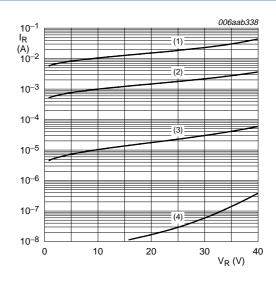
 $T_i = 25 \,^{\circ}C$  unless otherwise specified.

Parameter	Conditions	Min	Тур	Max	Unit
forward voltage	$I_F = 0.1 A$	-	270	310	mV
	I <sub>F</sub> = 1 A	-	340	390	mV
	I <sub>F</sub> = 5 A	-	430	490	mV
reverse current	V <sub>R</sub> = 10 V	-	10	-	μΑ
	V <sub>R</sub> = 40 V	-	60	300	μΑ
diode capacitance	f = 1 MHz				
	$V_R = 1 V$	-	600	-	pF
	V <sub>R</sub> = 10 V	-	220	-	pF
	forward voltage	$I_{F} = 0.1 \text{ A}$ $I_{F} = 1 \text{ A}$ $I_{F} = 5 \text{ A}$ $I_{R} = 10 \text{ V}$ $V_{R} = 40 \text{ V}$ $V_{R} = 40 \text{ V}$ $V_{R} = 1 \text{ V}$	$ \begin{array}{c} \text{forward voltage} & I_F = 0.1 \text{ A} & - \\ \hline I_F = 1 \text{ A} & - \\ \hline I_F = 5 \text{ A} & - \\ \hline \text{reverse current} & V_R = 10 \text{ V} & - \\ \hline \text{diode capacitance} & f = 1 \text{ MHz} \\ \hline V_R = 1 \text{ V} & - \\ \hline \end{array} $	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$



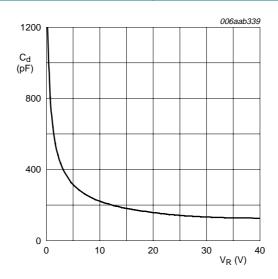
- (1)  $T_j = 150 \,^{\circ}\text{C}$
- (2)  $T_i = 125 \, ^{\circ}C$
- (3)  $T_i = 85 \,^{\circ}C$
- (4)  $T_j = 25 \,{}^{\circ}C$
- (5)  $T_j = -40 \, ^{\circ}C$

Fig 4. Forward current as a function of forward voltage; typical values



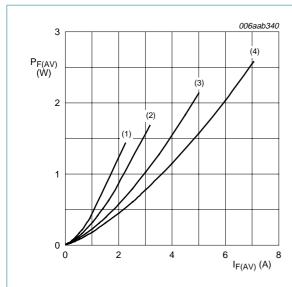
- (1) T<sub>j</sub> = 125 °C
- (2)  $T_j = 85 \, ^{\circ}C$
- (3)  $T_j = 25 \,^{\circ}C$
- (4)  $T_j = -40 \, ^{\circ}C$

Fig 5. Reverse current as a function of reverse voltage; typical values



f = 1 MHz; T<sub>amb</sub> = 25 °C

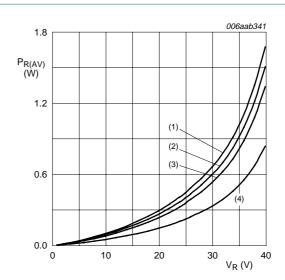
Fig 6. Diode capacitance as a function of reverse voltage; typical values



T<sub>i</sub> = 150 °C

- (1)  $\delta = 0.1$
- (2)  $\delta = 0.2$
- (3)  $\delta = 0.5$
- (4)  $\delta = 1$

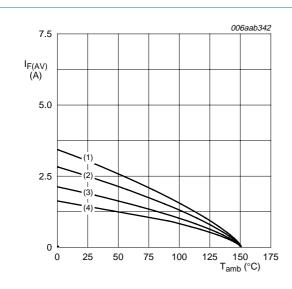
Fig 7. Average forward power dissipation as a function of average forward current; typical values



T<sub>i</sub> = 125 °C

- (1)  $\delta = 1$
- (2)  $\delta = 0.9$
- (3)  $\delta = 0.8$
- (4)  $\delta = 0.5$

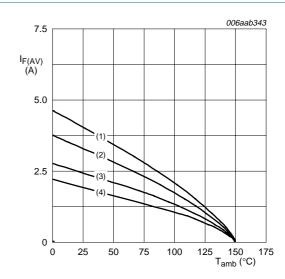
Fig 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

Fig 9. Average forward current as a function of ambient temperature; typical values

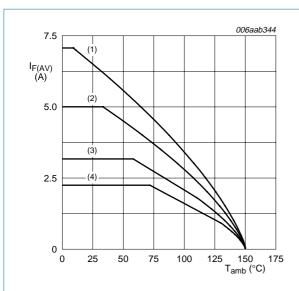


FR4 PCB, mounting pad for cathode 1  $\mbox{cm}^2$ 

$$T_j = 150 \, ^{\circ}C$$

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ; f = 20 kHz
- (3)  $\delta = 0.2$ ; f = 20 kHz
- (4)  $\delta = 0.1$ ; f = 20 kHz

Fig 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al<sub>2</sub>O<sub>3</sub>, standard footprint

T<sub>i</sub> = 150 °C

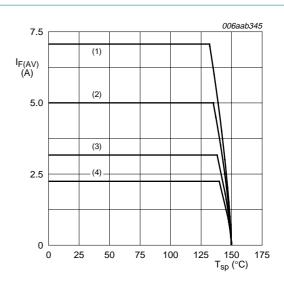
(1)  $\delta = 1$ ; DC

(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 11. Average forward current as a function of ambient temperature; typical values



T<sub>j</sub> = 150 °C

(1)  $\delta = 1$ ; DC

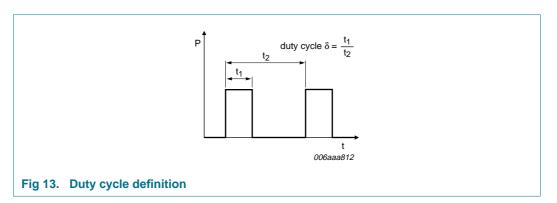
(2)  $\delta = 0.5$ ; f = 20 kHz

(3)  $\delta = 0.2$ ; f = 20 kHz

(4)  $\delta = 0.1$ ; f = 20 kHz

Fig 12. Average forward current as a function of solder point temperature; typical values

## 8. Test information

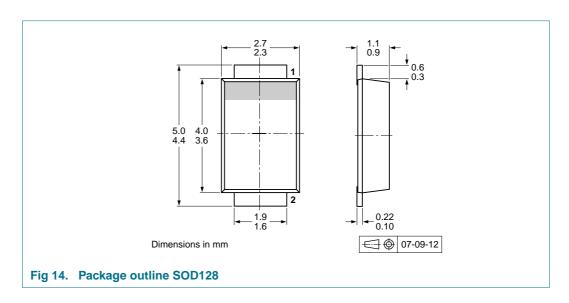


The current ratings for the typical waveforms as shown in <u>Figure 9</u>, <u>10</u>, <u>11</u> and <u>12</u> are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

## 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

## 9. Package outline



## 10. Packing information

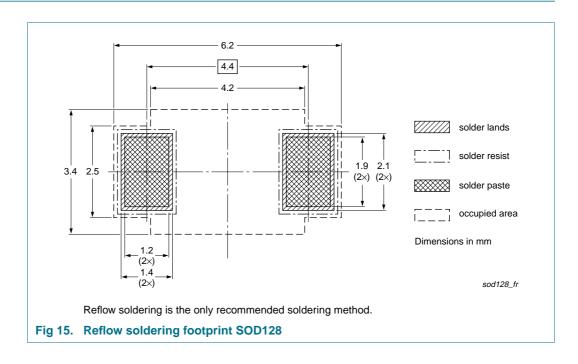
Table 8. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	Packing quantity
			3000
PMEG4050EP	SOD128	4 mm pitch, 12 mm tape and reel	-115

<sup>[1]</sup> For further information and the availability of packing methods, see Section 14.

## 11. Soldering





# 12. Revision history

## Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG4050EP_1	20090914	Product data sheet	-	-

## 13. Legal information

#### 13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions"
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# PMEG4050EP

## 5 A low V<sub>F</sub> MEGA Schottky barrier rectifier

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