(Unit: mm)

# **PQ30RV31**

# Variable Output Low Power-Loss Voltage Regulator

#### Features

• Maximum output current: 3A

• Compact resin full-mold package

• Low power-loss(Dropout voltage: MAX.0.5V)

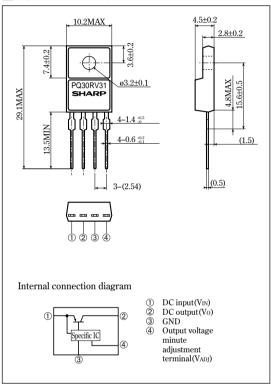
• Variable output voltage (setting range: 1.5 to 30V)

• Built-in ON/OFF control function.

# Applications

- Power supply for print concentration control of word processors
- Series power supply for motors and solenoid
- Series power supply for VCRs and TVs

### Outline Dimensions



# ■ Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
*1 Input voltage	Vin	35	V
*1 Output adjustment terminal voltage	Vadj	7	V
Output current	Io	3	A
Power dissipation (No heat sink)	P <sub>D1</sub>	2.0	W
Power dissipation (With infinite heat sink)	P <sub>D2</sub>	20	W
*2 Junction temperature	Tj	150	°C
Operating temperature	Topr	-20 to +80	°C
Storage temperature	Tstg	-40 to +150	°C
Soldering temperature	Tsol	260 (For 10s)	°C

<sup>\*1</sup> All are open except GND and applicable terminals.

• Please refer to the chapter " Handling Precautions ".

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<sup>\*2</sup> Overheat protection function may operate at 125<=Tj<=150°C.

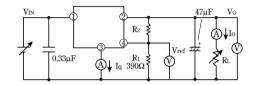
## Electrical Characteristics

(Unless otherwise specified, condition shall be V<sub>IN</sub>=12V, Vo=10V, Io=1.5A, R<sub>1</sub>=390Ω, T<sub>a</sub>=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input voltage	Vin	_	4.5	-	35	V
output voltage	Vo	_	1.5	-	30	V
Load regulation	RegL	Io=5mA to 3A	_	0.5	2.0	%
Line regulation	RegI	V <sub>IN</sub> =11 to 21V, Io=0.5mA	_	0.5	2.5	%
Ripple rejection	RR	Refer to Fig. 2	45	70	ı	dB
Reference voltage	Vref	_	1.225	1.25	1.275	V
Temperature coefficient of reference voltage	TcVref	T <sub>j</sub> =0 to 125°C,I <sub>0</sub> =5mA	_	±1.0	ı	%/°C
Dropout voltage	V <sub>i-O</sub>	*3, Io=3A	-	0.3	1.0	v
		*3, Io=2A	_	0.2	0.5	
Quiescent current	$I_{\mathrm{q}}$	Io=0	_	ı	7	mA

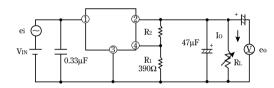
<sup>\*3</sup> Input voltage shall be the value when output voltage is 95% in comparison with the initial value.

Fig. 1 Test Circuit



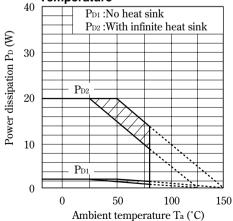
$$V_0$$
=Vref ×  $\left(1 + \frac{R_2}{R_1}\right)$   
[R<sub>1</sub>=390Ω, Vref Nearly=1.25V]

Fig. 2 Test Circuit of Ripple Rejection



$$\label{eq:continuous_loss} \begin{split} & \text{Io=0.5A, V}_{\text{IN}} \text{=} 12\text{V, V}_{\text{O}} \text{=} 10\text{V} \\ & \text{f=120Hz(sine wave)} \\ & \text{ei(rms)=0.5V}_{\text{rms}} \\ & \text{RR=20 log(ei(rms)/eo(rms))} \end{split}$$

Fig. 3 Power Dissipation vs. Ambient Temperature



Note) Oblique line portion: Overheat protection may operate in this area.

Fig. 4 Overcurrent Protection Characteristics (Typical Value)

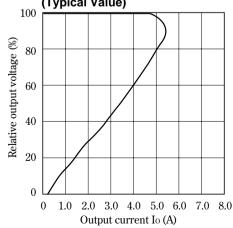


Fig. 5 Output Voltage Adjustment Characteristics (Typical value)

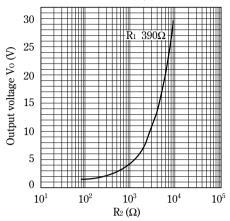


Fig. 7 Dropout Voltage vs. Junction Temperature

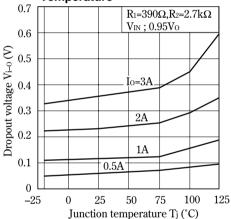


Fig. 9 Ripple Rejection vs. Output Current

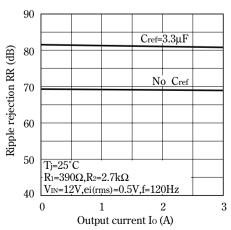


Fig. 6 Output Voltage vs. Input Voltage

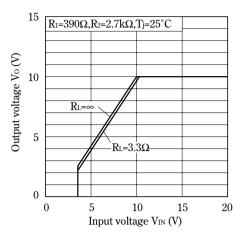


Fig. 8 Ripple Rejection vs. Input Ripple Frequency

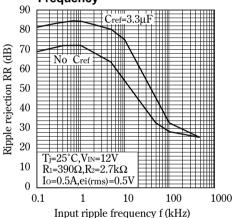


Fig.10 Output Peak Current vs. Dropout Voltage (Typical value)

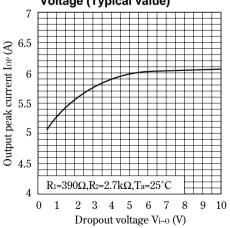
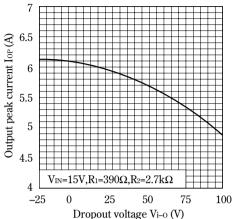
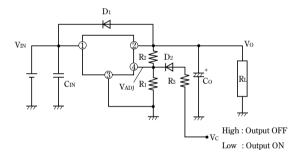
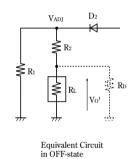


Fig.11 Ripple Rejection vs. Input Ripple Frequency



## ON/OFF Operation





- ON/OFF operation is available by mounting externally D<sub>2</sub> and R<sub>3</sub>.
- When Vadj is forcibly raised above VREF (1.25V TYP) by applying the external signal, the output is turned off (pass transistor of regulator is turned off. When the output is OFF, Vadj must be higher then VREF MAX., and at the same time must be lower than maximum rating 7V.

In OFF-state, the load current flows to R<sub>L</sub> from V<sub>ADJ</sub> through R<sub>2</sub>. Therefore the value of R<sub>2</sub> must be as high as possible.

• Vo'=V<sub>ADJ</sub>XR<sub>L</sub>/(R<sub>L</sub>+R<sub>2</sub>) occurs at the load. OFF-state equivalent circuit R<sub>1</sub> up to 10kΩ is allowed. Select as high value of R<sub>L</sub> and R<sub>2</sub> as possible in this range. In some case, as output voltage is getting lower(Vo<1V), impedance of load resistance rises. In such condition, it is sometime impossible to obtain the minimum value of Vo'. So add the dummy resistance indicated by R<sub>D</sub> in the figure to the circuit parallel to the load.

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