

(0.50 mm) .0197"

QSH SERIES

HIGH SPEED GROUND PLANE SOCKET

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QSH

Insulator Material: Liquid Crystal Polymer
Contact Material: Phosphor Bronze Plating: Au or Sn over 50µ" (1.27 µm) Ni Current Rating: Contact: 2 A per pin (1 pin powered per row)

Ground Plane: 25 A per ground plane (1 ground plane powered) Operating Temp Range:

-55°C to +125°C **Voltage Rating:** 175 VAC (5 mm Stack Height) Max Cycles: 100 RoHS Compliant: Yes

Processing:

Lead-Free Solderable: Yes SMT Lead Coplanarity: (0.10 mm) .004" max (030-060) (0.15 mm) .006" max (090) Board Stacking: For applications requiring prore than two connectors per

more than two connectors per board contact ipg@samtec.com

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality





ALSO AVAILABLE (MOQ Required)

- 15 mm, 22 mm and 30 mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
- 30μ" (0.76 μm) Gold (Specify -H plating for Data Rate cable mating applications.)
- Edge Mount & Guide Posts
- 80 (-DP), 120, 150 positions per row
- Retention Option Contact Samtec.

Board Mates:

Cable Mates:

HQCD, HQDF (See Also Available note)



QTH/QSH 5 mm Stack Height

QSH

(7.49)

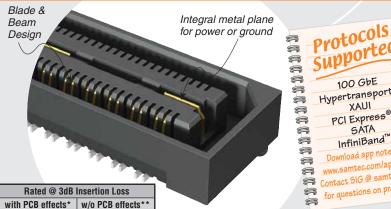
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Single-Ended Signaling

Differential Pair Signaling

Differential Pair Signaling



	nateu @ Sub Hisertion Loss	
	w/o PCB effects**	with PCB effects*
	9.5 GHz / 19 Gbps	9 GHz / 18 Gbps
s	10.5 GHz / 21 Gbps	8 GHz / 16 Gbps
s	16.5 GHz / 33 Gbps	9.5 GHz / 19 Gbps
_		

01

-DP *Performance data includes effects of a non-optimized PCB. Test board losses de-embedded from performance data.

-D

-D

Performance data for other stack heights and complete test data available at www.samtec.com?QSH or contact sig@samtec.com

PINS PER ROW

NO. OF PAIRS

-030, -060, -090 (60 total pins per bank = -D)

-020, -040, -060 (20 pairs per bank = -D-DP)

-D = (No. of Pins per Row/30) x - (20.00) .7875 + (1.27) .050 -

-DP = (No. of Pairs per Row/20) x -- (20.00) .7875 + (1.27) .050 --

(0.50)

.0197

(20.00) .7875

.285

035

(0.76)

100 GbE Hypertransport™ XAUI PCI Express® SATA InfiniBand™ Download app notes at www.samtec.com/appnote Contact SIG @ samtec.com for questions on protocols

PLATING OPTION

_F

= Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails

= 10µ" (0.25 µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails

$-C^*$

= Electro-Polished Selective 50μ" (1.27 μm) min Au over 150µ" (3.81 µm) Ni on Signal Pins in contact area, 10μ" (0.25 μm) min Au over 50μ" (1.27 μm) Ni on Ground Plane in contact area, Matte Tin over 50µ" (1.27 µm) min Ni on all solder tails

–D = Single-**Ended** D-DP = Differential Pair (-01 only)

QT LEA

STY

TYPE

QTH LEAD STYLE	MATED HEIGHT WITH QSH*	
-01	(5.00) .197	
-02	(8.00) .315	
-03	(11.00) .433	
-04	(16.00) .630	
-05	(19.00) .748	
-07	(25.00) .984	
-09	(14.00) .551	
*Processing		

= (8,25 mm) .325" DIA Polyimide Film Pick & Place Pad

OTHER

OPTION

–TR = Tape & Reel (-090 positions maximum)

-L = Latching Option (Not available on -060 (-D-DP) & -090 positions)

-0 *Pro conditions will affect mated height.

*Note: -C Plating passes 10 year MFG testing

Note: Some lengths, styles and options are non-standard,

OTHER **SOLUTIONS**

• Board Spacing Standoffs. See SO Series

(0.15)

.006

(3.25)

(0.64)