

QSH-060-01-L-D-DP-A

QSH-060-01-L-D-A-K



QSH-030-01-F-D-A

Integral metal plane

for power or ground

inal nch

CERTIFIED

QSH SERIES

Protocols

Supported

100 GbE

Hypertransport™

XAUI

PCI Express®

SÁTA InfiniBand™

Download app notes at

www.samtec.com/appnote

Contact SIG @ samtec.com

for questions on protocols

Gbps

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(0,50 mm) .0197"

HIGH SPEED GROUND PLANE SOCKET

Blade &

Beam

Design

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?QSH

Insulator Material: Liquid Crystal Polymer Contact Material: Phosphor Bronze Plating: Au or Sn over 50μ" (1,27 μm) Ni Current Rating: Contact: 2 A per pin (1 pin powered per row) Ground Plane: 25 A per ground plane (1 ground plane powered) Operating Temp Range: -55°C to +125°C Voltage Rating: 125 VAC (5 mm Stack Height) Max Cycles: 100 RoHS Compliant: Yes

Processing: Lead-Free Solderable: Yes SMT Lead Coplanarity: (0,10 mm) .004" max (030-060) (0,15 mm) .006" max (090) Board Stacking: For applications requiring mere then two competence per more than two connectors per board contact ipg@samtec.com

RECOGNITIONS

For complete scope of recognitions see www.samtec.com/quality



ALSO AVAILABLE (MOQ Required)

- 14 mm, 15 mm, 22 mm and 30 mm stack height (Caution: Some automatic placement/inspection machines may have component height restrictions. Please consult machinery specifications.)
- 30µ" (0,76 µm) Gold (Specify -H plating for Data Rate cable mating applications.)
- Edge Mount & Guide Posts • 80 (-DP), 120, 150
- positions per row Retention Option

Contact Samtec.

Board Mates: Cable Mates:

HQCD, HQDF (See Also Available note)



.035 DIA

(0, 89)

QTH/QSH	Туре	Rated @ 3dB Insertion Loss						
5 mm Stack Height		with PCB effects*	w/o PCB effects**					
Single-Ended Signaling	-D	9 GHz / 18 Gbps	9.5 GHz / 19 Gbps					
Differential Pair Signaling	-D	8 GHz / 16 Gbps	10.5 GHz / 21 Gbps					
Differential Pair Signaling	-DP	9.5 GHz / 19 Gbps	16.5 GHz / 33 Gbps					
*Performance data includes effects of a non-optimized PCB. **Test board losses de-embedded from performance data.								
Performance data for other stack heights and complete test data								

QSH 🗕	PINS PER ROW NO. OF PAIRS	= 01 =	PLATING OPTION	TY	'PE <mark>=</mark> A	
(7,49) (7,49) (7,49) (0000000 01 01 (3,05) .120	-030, -060, -09 (60 total pins per bank = - -020, -040, -06 (20 pairs per bank = -D-D -D = (No. of Pins per Row/30 - (20,00) .7875 + (1,27) .050 DP = (No. of Pairs per Row/2 - (20,00) .7875 + (1,27) .050 (20,00) .050 (20,00) .7875 + (1,27) .050 (20,00) .7875 + (1,27) .050 (20,00	$ \begin{array}{c} D \\ 0 \\ P \end{array} \\ 0 \\ x \\ 0 \\ x \\ \hline \end{array} $	-F = Gold Flash on Signal Pins and Ground Plane, Matte Tin on tails $-L$ = 10µ" (0,25 µm) Gold on Signal Pins and Ground Plane, Matte Tin on tails $-C^*$ = Electro-Polished Selective 50µ" (1,27 µm) min Au over 150µ" (3,81 µm) Ni on Signal Pins in contact area, 10µ" (0,25 µm) min Au over 50µ" (1,27 µm) Ni on Ground Plane in contact area, Matte Tin over 50µ" (1,27 µm) min Ni on all solder tails	QTH LEAD STYLE -01 -02 -03 -04 -05 -07 *Proces conditia affect r	-D Single- Ended D-DP ifferential Pair 01 only) MATED HEIGHT WITH QSH* (5,00) .197 (8,00) .315 (11,00) .433 (16,00) .630 (19,00) .748 (25,00) .984 sing ons will nated height.	-K = (8,25 mm) .325" DIA Polyimide Film Pick & Place Pad -TR = Tape & Reel (-090 positions maximum) -L = Latching Option (Not available on -060 (-D-DP) & -090 positions)
	7,24) .285 (0,76) .030 Γ		*Note: -C Plating passe 10 year MFG testing Note: Some lengths, sty			THER UTIONS

and options are non-standard. non-returnable.



See SO Series

WWW.SAMTEC.COM

(0,64)

.025

Due to technical progress, all designs, specifications and components are subject to change without notice.