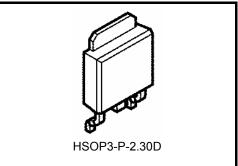
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA78M05F,TA78M06F,TA78M08F,TA78M09F,TA78M10F TA78M12F,TA78M15F,TA78M18F,TA78M20F,TA78M24F

Output Current of 0.5 A, Three-Terminal Positive Voltage Regulators 5 V, 6 V, 8 V, 9 V, 10 V, 12 V, 15 V, 18 V, 20 V, 24 V

Features

- Suitable for CMOS, TTL and the power supply of the other digital ICs
- Internal overheating protection.
- Internal overcurrent protection.
- Maximum output current of 0.5 A.
- Packaged in New PW-Mold (Surface-mount type).

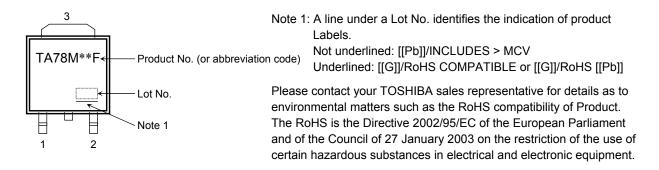


Pin Assignment



HSOP3-P-2.30D Weight HSOP3-P-2.30D: 0.36 g (typ.)

Marking



Note 2: The "**" part of each product number varies according to the output voltage of the product.

Ordering Method

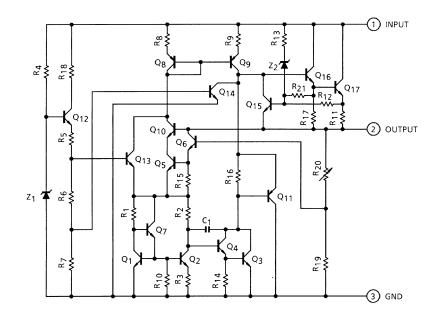
Product Name	Package (Lead Type)	Packing Form
TA78M**F (TE16L1, NQ	New PW-Mold: Surface-mount	Tape (2000 pcs./reel)

Note: The "**" in each pro-forma product name is replaced with the output voltage of each product.

The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

Equivalent Circuit

TOSHIBA



Absolute Maximum Ratings (Ta = 25°C)

Characteris	tics	Symbol	Rating	Unit
	TA78M05F			
	TA78M06F			
	TA78M08F			
	TA78M09F		35	
Input voltage	TA78M10F	Max		V
Input voltage	TA78M12F	V _{IN}		v
	TA78M15F			
	TA78M18F			
	TA78M20F		40	
	TA78M24F			
Output current		IOUT	0.5	А
Power dissipation	(Ta = 25°C)	PD	1	W
Fower dissipation	(Tc = 25°C)	FD	10	vv
Operating junction temp	berature	Tj _{opr}	-30 to 150	°C
Storage temperature		T _{stg}	-55 to 150	°C
Junction temperature		Tj	150	°C
Thormal registeres	Thermal resistance		12.5	°C/W
mermai resistance		R _{th (j-a)}	125	C/W

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

TA78M05F Electrical Characteristics (Unless otherwise specified, V_{IN} = 10 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	s	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		4.8	5.0	5.2	V
Line regulation		Reg·line	1	T _i = 25°C	7 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	4	100	mV
		Regime		1j - 25 C	8 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	2	50	
Load regulation		Pogelood	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	25	100	mV
		Reg·load		1j = 25 C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	50	
Output voltage		Vout	1	T _j = 25°C	7 V ≤ V _{IN} ≤ 20 V, 5 mA ≤ I _{OUT} ≤ 350 mA	4.75	_	5.25	V
Quiescent current		Ι _Β	1	T _j = 25°C		—	4.5	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	8.5 V ≤ V _{IN} ≤ 25.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1	,	5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	50	200	μV _{rms}
Ripple rejection		R.R.	3	f = 120 Hz 8 V ≤ V _{IN} :	f = 120 Hz, I _{OUT} = 100 mA, 8 V ≤ V _{IN} ≤ 18 V, T _I = 25°C		67	_	dB
Short circuit current limit I_{SC} 1 $T_j = 25^{\circ}C$			—	960	—	mA			
Dropout voltage V_D 1 $T_j = 25^{\circ}C$			—	1.7	_	V			
Average temperature coefficient of output ve	oltage	T _{CVO}	1	I _{OUT} = 5 n	nA	-	-0.6	_	mV/°C

TA78M06F Electrical Characteristics (Unless otherwise specified, V_{IN} = 11 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		5.75	6.0	6.25	V
Line regulation		Reg·line	1	T _i = 25°C	8 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	4	100	mV
		Regime		1j - 25 C	9 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	2	50	
Load regulation		Pogload	1	T. = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	25	120	mV
Load regulation		Reg·load		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	60	
Output voltage		V _{OUT}	1	T _j = 25°C	8 V ≤ V _{IN} ≤ 21 V, 5 mA ≤ I _{OUT} ≤ 350 mA	N ≤ 21 V, OUT ≤ 350 mA 5.7 — 6		6.3	V
Quiescent current		Ι _Β	1	T _j = 25°C			4.5	8.0	mA
Quiescent current change	Line	ΔI _{BI}	1	T _i = 25°C	9.5 V ≤ V _{IN} ≤ 25.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1		5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz		55	220	μV_{rms}
Ripple rejection		R.R.	3	f = 120 Hz 9 V ≤ V _{IN} :	f = 120 Hz, I _{OUT} = 100 mA, 9 V ≤ V _{IN} ≤ 19 V, T _j = 25°C		65	_	dB
Short circuit current limit I _{SC}		1	T _j = 25°C		—	960	—	mA	
Dropout voltage		VD	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output vertices of the second	oltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-0.7	_	mV/°C

TA78M08F Electrical Characteristics (Unless otherwise specified, V_{IN} = 14 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	s	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		7.7	8.0	8.3	V
Line regulation		Regiline	1	T _i = 25°C	10.5 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	5	100	mV
		Regnine		1j - 25 C	11 V ≤ V _{IN} ≤ 25 V, I _{OUT} = 200 mA	_	3	50	
Load regulation		Reg·load	1	T 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	26	160	mV
		Regillau		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	80	IIIV
Output voltage		Vout	1	T _j = 25°C	10.5 V ≤ V _{IN} ≤ 23 V, 5 mA ≤ I _{OUT} ≤ 350 mA	7.6	_	8.4	V
Quiescent current		Ι _Β	1	T _j = 25°C			4.6	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	11 V ≤ V _{IN} ≤ 25.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1	,	5 mA ≤ I _{OUT} ≤ 350 mA		_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz		60	250	μV_{rms}
Ripple rejection		R.R.	3		f = 120 Hz, I _{OUT} = 100 mA, 11.5 V ≤ V _{IN} ≤ 21.5 V, T _I = 25°C		62	_	dB
Short circuit current limit I _{SC}		1	T _j = 25°C			960	_	mA	
Dropout voltage V _D		1	T _j = 25°C		—	1.7	—	V	
Average temperature coefficient of output ve	oltage	T _{CVO}	1	I _{OUT} = 5 r	nA	_	-1.0	_	mV/°C

TA78M09F Electrical Characteristics (Unless otherwise specified, V_{IN} = 15 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	S	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C	T _j = 25°C		9.0	9.36	V
Line regulation		Reg·line	1	T _i = 25°C	11.5 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 200 mA	_	5	100	mV
		Regiline		1j - 25 C	13 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 200 mA	_	3	50	IIIV
Load regulation		Reg·load	1	T 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	—	26	180	mV
		Regiloau	1	T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	90	
Output voltage		Vout	1	T _j = 25°C	11.5 V ≤ V _{IN} ≤ 24 V, 5 mA ≤ I _{OUT} ≤ 350 mA	8.55	_	9.45	V
Quiescent current		Ι _Β	1	T _j = 25°C		_	4.6	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	12 V ≤ V _{IN} ≤ 26.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	Δl _{BO}	1	,	5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage	•	V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	60	270	μV_{rms}
Ripple rejection		R.R.	3		f = 120 Hz, I _{OUT} = 100 mA, 12.5 V ≤ V _{IN} ≤ 22.5 V, T _I = 25°C		61	_	dB
Short circuit current limit I_{SC} 1 $T_j = 25^{\circ}C$			_	960	_	mA			
Dropout voltage V_D 1 $T_j = 25^{\circ}C$			—	1.7	—	V			
Average temperature coefficient of output ve	oltage	T _{CVO}	1	I _{OUT} = 5 r	nA	-	-1.1	_	mV/°C

TA78M10F Electrical Characteristics (Unless otherwise specified, V_{IN} = 16 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C	T _j = 25°C		10.0	10.4	V
Line regulation		Reg·line	1		12.5 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 200 mA	_	6	100	mV
		Regime		T _j = 25°C	14 V ≤ V _{IN} ≤ 26 V, I _{OUT} = 200 mA	_	3	50	
Lood regulation		Declard	1	$T_{\rm c} = 25^{\circ}$ C	5 mA ≤ I _{OUT} ≤ 500 mA	_	26	200	mV
Load regulation		Reg·load		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	100	mv
Output voltage		Vout	1	T _j = 25°C	12.5 V ≤ V _{IN} ≤ 25 V, 5 mA ≤ I _{OUT} ≤ 350 mA	9.5	— 10.5		V
Quiescent current		Ι _Β	1	T _j = 25°C		_	4.7	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	13 V ≤ V _{IN} ≤ 26.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1		5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	65	280	μV_{rms}
Ripple rejection		R.R.	3	f = 120 Hz 13.5 V ≤ V	f = 120 Hz, I _{OUT} = 100 mA, 13.5 V ≤ V _{IN} ≤ 23.5 V, T _I = 25°C		59	_	dB
Short circuit current li	nit	I _{SC}	1	T _j = 25°C		_	960		mA
Dropout voltage V_D 1 $T_j = 25^{\circ}C$			—	1.7	_	V			
Average temperature coefficient of output v	oltage	T _{CVO}	1	I _{OUT} = 5 r	nA	_	-1.3	_	mV/°C

TA78M12F Electrical Characteristics (Unless otherwise specified, V_{IN} = 19 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C	T _j = 25°C		12.0	12.5	V
Line regulation		Reg·line	1	T _i = 25°C	14.5 V ≤ V _{IN} ≤ 30 V, I _{OUT} = 200 mA	_	7	100	mV
		Regnine		1j - 25 C	16 V ≤ V _{IN} ≤ 30 V, I _{OUT} = 200 mA	_	3	50	IIIV
Load regulation		Reg·load	1	T. = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	27	240	mV
		Regillau		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	120	IIIV
Output voltage		Vout	1	T _j = 25°C	14.5 V ≤ V _{IN} ≤ 27 V, 5 mA ≤ I _{OUT} ≤ 350 mA	27 V, 350 mA 11.4 — 12.6		12.6	V
Quiescent current		Ι _Β	1	T _j = 25°C		_	4.8	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	15 V ≤ V _{IN} ≤ 30.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1		5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	70	300	μV_{rms}
Ripple rejection		R.R.	3		f = 120 Hz, I _{OUT} = 100 mA, 15 V ≤ V _{IN} ≤ 25 V, T _I = 25°C		57	_	dB
Short circuit current limit I _{SC} 1 T _j = 3		T _j = 25°C		_	960	_	mA		
Dropout voltage V_D 1 $T_j = 2$		T _j = 25°C		_	1.7	_	V		
Average temperature coefficient of output vertex	oltage	T _{CVO}	1	I _{OUT} = 5 r	nA	-	-1.6	_	mV/°C

TA78M15F Electrical Characteristics (Unless otherwise specified, V_{IN} = 23 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	s	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C	T _j = 25°C		15.0	15.6	V
Line regulation		Reg·line	1		17.5 V ≤ V _{IN} ≤ 30 V, I _{OUT} = 200 mA	_	8	100	mV
		Regnine		T _j = 25°C	20 V ≤ V _{IN} ≤ 30 V, I _{OUT} = 200 mA	_	4	50	
Load regulation		Reg·load	1	T. = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	—	27	300	mV
Load regulation		Regillau		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	150	IIIV
Output voltage		Vout	1	T _j = 25°C	25°C $ 17.5 V \le V_{IN} \le 30 V, \\ 5 \text{ mA} \le I_{OUT} \le 350 \text{ mA} $ 14.25 —		_	15.75	V
Quiescent current		Ι _Β	1	T _j = 25°C		_	4.8	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	18 V ≤ V _{IN} ≤ 30.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1	. ,	5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	80	450	μV_{rms}
Ripple rejection		R.R.	3	f = 120 Hz 18.5 V ≤ V	f = 120 Hz, I _{OUT} = 100 mA, 18.5 V ≤ V _{IN} ≤ 28.5 V, T _I = 25°C		55	_	dB
Short circuit current limit I_{SC} 1 $T_j = 25^{\circ}C$			_	960	_	mA			
Dropout voltage V_D 1 T_j =		T _j = 25°C		—	1.7	—	V		
Average temperature coefficient of output ve	oltage	T _{CVO}	1	I _{OUT} = 5 r	nA	_	-2.0	_	mV/°C

TA78M18F Electrical Characteristics (Unless otherwise specified, V_{IN} = 27 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		17.3	18.0	18.7	V
Line regulation		Reg·line	1	T _i = 25°C	21 V ≤ V _{IN} ≤ 33 V, I _{OUT} = 200 mA	_	9	100	mV
		Regime		1j - 25 C	24 V ≤ V _{IN} ≤ 33 V, I _{OUT} = 200 mA	_	5	50	
Load regulation		Pogload	1	T. = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	28	360	mV
Load regulation		Reg·load		T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	180	IIIV
Output voltage		Vout	1	T _j = 25°C	21 V ≤ V _{IN} ≤ 33 V, 5 mA ≤ I _{OUT} ≤ 350 mA	17.1	_	18.9	V
Quiescent current		Ι _Β	1	T _j = 25°C		—	4.8	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	21.5 V ≤ V _{IN} ≤ 33.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1		5 mA ≤ I _{OUT} ≤ 350 mA		_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz		90	490	μV_{rms}
Ripple rejection		R.R.	3	f = 120 Hz 22 V ≤ V _{IN}	f = 120 Hz, I _{OUT} = 100 mA, 22 V ≤ V _{IN} ≤ 32 V, T _I = 25°C		53	_	dB
Short circuit current li	mit	I _{SC}	1	T _j = 25°C			960	_	mA
Dropout voltage		VD	1	T _j = 25°C		_	1.7	_	V
Average temperature coefficient of output v	oltage	T _{CVO}	1	I _{OUT} = 5 n	nA	_	-2.5	_	mV/°C

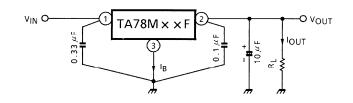
TA78M20F Electrical Characteristics (Unless otherwise specified, V_{IN} = 29 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		19.2	20.0	20.8	V
Line regulation		Reg·line	1	T _i = 25°C	23 V ≤ V _{IN} ≤ 35 V, I _{OUT} = 200 mA	_	10	100	mV
		Regime		1j - 25 C	24 V ≤ V _{IN} ≤ 35 V, I _{OUT} = 200 mA	_	6	50	IIIV
Load regulation		Pogload	1	T _i = 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	28	400	mV
Load regulation		Reg·load		1j = 25 C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	200	
Output voltage		Vout	1	T _j = 25°C	23 V ≤ V _{IN} ≤ 35 V, 5 mA ≤ I _{OUT} ≤ 350 mA	19.0	_	— 21.0	
Quiescent current		Ι _Β	1	T _j = 25°C		—	4.9	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	23.5 V ≤ V _{IN} ≤ 35.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1	,	5 mA ≤ I _{OUT} ≤ 350 mA		_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	—	95	540	μV_{rms}
Ripple rejection		R.R.	3	f = 120 Hz 24 V ≤ V _{IN}	f = 120 Hz, I _{OUT} = 100 mA, 24 V ≤ V _{IN} ≤ 34 V, T _I = 25°C		53	_	dB
Short circuit current limit I _{SC}		I _{SC}	1	T _j = 25°C			960	_	mA
Dropout voltage V _D		1	T _j = 25°C		—	1.7	—	V	
Average temperature coefficient of output vertices of the second	oltage	T _{CVO}	1	l _{OUT} = 5 r	nA	_	-3.0	_	mV/°C

TA78M24F Electrical Characteristics (Unless otherwise specified, V_{IN} = 33 V, I_{OUT} = 350 mA, 0°C \leq T_j \leq 125°C, C_{IN} = 0.33 µF, C_{OUT} = 0.1 µF)

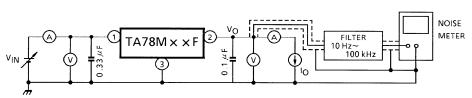
Characteristic	cs	Symbol	Test Circuit		Test Condition	Min	Тур.	Max	Unit
Output voltage		V _{OUT}	1	T _j = 25°C		23.0	24.0	25.0	V
Line regulation		Reg·line	1	T _i = 25°C	27 V ≤ V _{IN} ≤ 38 V, I _{OUT} = 200 mA	_	12	100	mV
		Regnine		1j - 25 C	28 V ≤ V _{IN} ≤ 38 V, I _{OUT} = 200 mA	_	7	50	
Load regulation		Pogload	1	T 25°C	5 mA ≤ I _{OUT} ≤ 500 mA	_	30	480	mV
Load regulation		Reg·load	1	T _j = 25°C	5 mA ≤ I _{OUT} ≤ 200 mA	_	10	240	IIIV
Output voltage		Vout	1	T _j = 25°C	27 V ≤ V _{IN} ≤ 38 V, 5 mA ≤ I _{OUT} ≤ 350 mA	$N \le 38 \text{ V},$ 22.8 — 25 DUT $\le 350 \text{ mA}$		25.2	V
Quiescent current		Ι _Β	1	T _j = 25°C		_	5.0	8.0	mA
Quiescent current	Line	ΔI _{BI}	1	T _i = 25°C	27.5 V ≤ V _{IN} ≤ 38.5 V, I _{OUT} = 200 mA	_	_	0.8	mA
change	Load	ΔI _{BO}	1	,	5 mA ≤ I _{OUT} ≤ 350 mA	_	_	0.5	
Output noise voltage		V _{NO}	2	Tj = 25°C,	10 Hz ≤ f ≤ 100 kHz	_	115	650	μV_{rms}
Ripple rejection		R.R.	3		f = 120 Hz, I _{OUT} = 100 mA, 28 V ≤ V _{IN} ≤ 38 V, T _I = 25°C		53	_	dB
Short circuit current limit I _{SC}		I _{SC}	1	T _j = 25°C		_	960	—	mA
Dropout voltage V _D		1	T _j = 25°C		_	1.7	—	V	
Average temperature coefficient of output v	oltage	T _{CVO}	1	l _{OUT} = 5 r	nA	-	-3.5	_	mV/°C

Test Circuit 1 / Standard Application



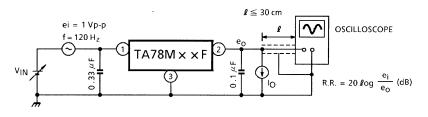
Test Circuit 2

V_{NO}

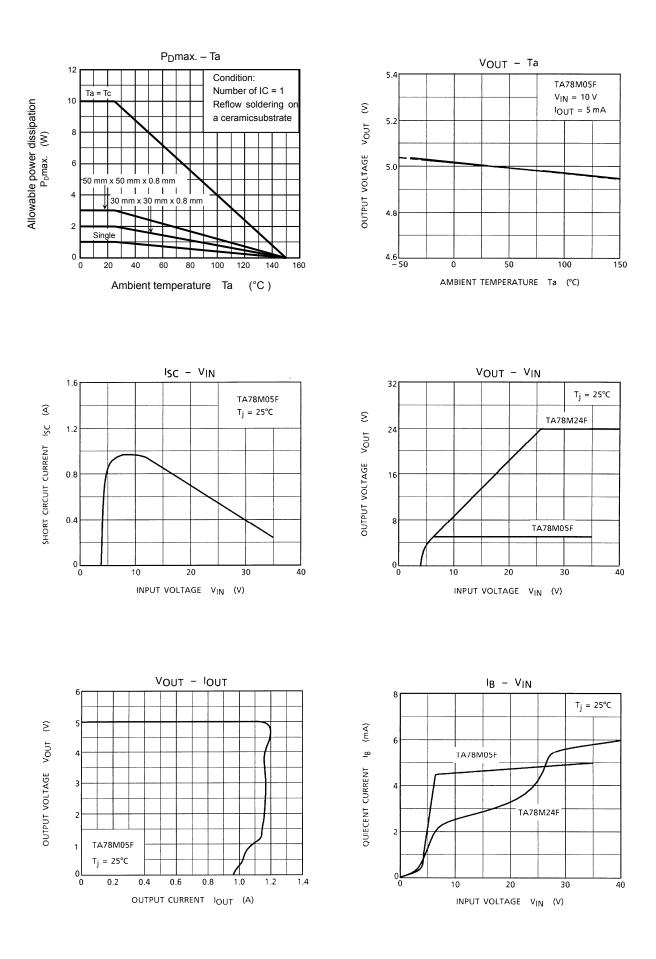


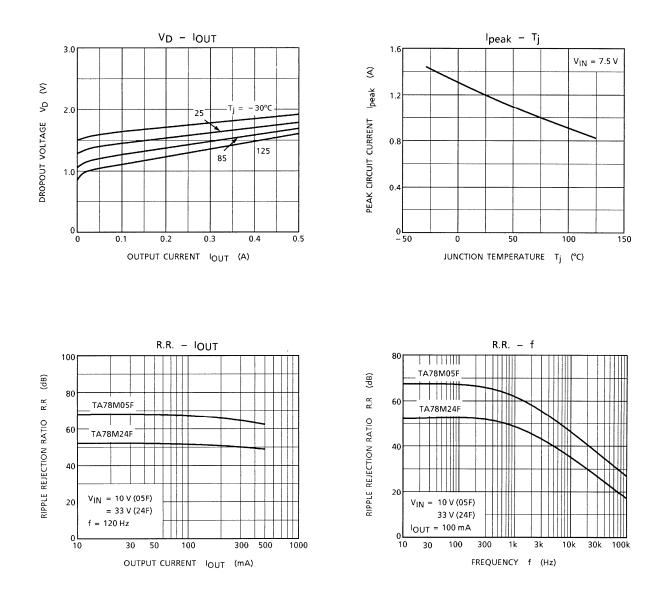
Test Circuit 3





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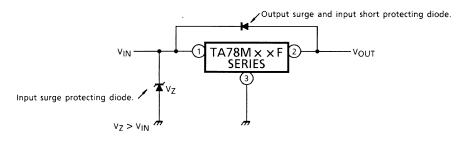
Usage Precautions

- (1) In regard to GND, be careful not to apply a negative voltage to the input/output terminal.
- (2) If a surge voltage exceeding the absolute maximum rating is applied to the input terminal or if a voltage in excess of the input terminal voltage is applied to the output terminal, the circuit may be destroyed.

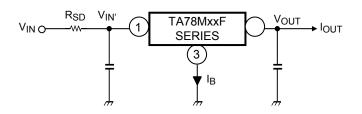
Particular care is necessary in the case of the latter.

Circuit destruction may also occur if the input terminal shorts to GND in a state of normal operation, causing the output terminal voltage to exceed the input voltage (GND potential) and the electrical charge of the chemical capacitor connected to the output terminal to flow into the input side.

Where these risks exist, take steps such as connecting Zener and general silicon diodes to the circuit, as shown in the figure below.



(3) When the input voltage is too high, the power dissipation of the three-terminal regulator, which is a series regulator, increases, causing the junction temperature to rise. In such a case, it is recommended to reduce the power dissipation, and hence the junction temperature, by inserting a power-limiting resistor R_{SD} in the input terminal.



The power dissipation PD of the IC is expressed in the following equation.

 $P_{D} = (V_{IN'} - V_{OUT}) \cdot I_{OUT} + V_{IN'} \cdot I_{B}$

Reducing $V_{IN'}$ below the lowest voltage necessary for the IC will cause ripple, deterioration in output regulation and, in certain circumstances, parasitic oscillation.

To determine the resistance value of RSD , design with a margin, referring to the following equation.

$$R_{SD} < \frac{V_{IN} - V_{IN'}}{I_{OUT} + I_B}$$

- (4) Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The capacitances should be determined experimentally because they depend on printed circuit board patterns. In particular, adequate investigation should be made to ensure there is no problem even in high or low temperatures.
- (5) The molded plastic portion of this unit, measuring 5.5 mm (L) by 6.5 mm (W) by 2.3 mm (T), is more compact compared to its equivalent TO-220.
 The collector fin extends directly out of the main body and can be soldered directly to the ceramic circuit board for significant increase in collector power dissipation.
 To obtain high reliability on the heat sink design of a regulator IC, it is generally required to derate more than 20% of maximum junction temperature (T_j max).
 Further, full consideration should be given to the installation of the IC on a heat sink.

• Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

Overcurrent Protection

The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

Overheating Protection

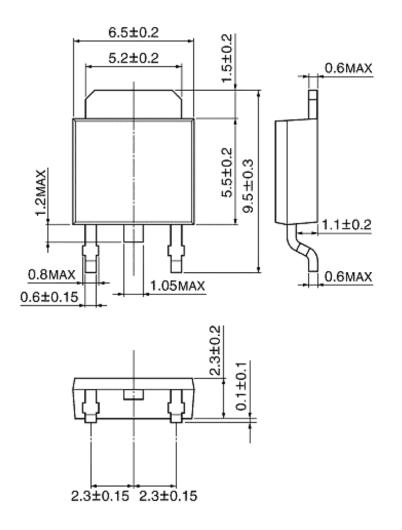
The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the overheating protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the overheating protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

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Package Dimensions

HSOP3-P-2.30D

Unit: mm



Weight: 0.36 g (typ.)

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