TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4051BP, TC4051BF, TC4051BFN, TC4051BFT TC4052BP, TC4052BF, TC4052BFN, TC4052BFT TC4053BP, TC4053BF, TC4053BFN, TC4053BFT

TC4051B

Single 8-Channel Multiplexer/Demultiplexer

TC4052B

Differential 4-Channel Multiplexer/Demultiplexer

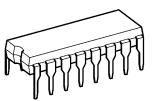
TC4053B

Triple 2-Channel Multiplexer/Demultiplexer

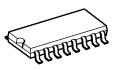
TC4051B, TC4052B and TC4053B are multiplexers with capabilities of selection and mixture of analog signal and digital signal. TC4051B has 8 channels configuration. TC4052B has 4 channel \times 2 configuration and TC4053B has 2 channel \times 3 configuration. The digital signal to the control terminal turns "ON" the corresponding switch of each channel, with large amplitude (VDD – VEE) can be switched by the control signal with small logical amplitude (VDD – VSS). For example, in the case of VDD = 5 V VSS = 0 V and VEE = –5 V, signals between –5 V and +5 V can be switched from the logical circuit with single power supply of 5 volts. As the ON-resistance of each switch is low, these can be connected to the circuits with low input impedance.

Note: xxxFN (JEDEC SOP) is not available in Japan.

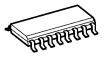
TC4051BP, TC4052BP, TC4053BP



DIP16-P-300-2.54A TC4051BF, TC4052BF, TC4053BF



SOP16-P-300-1.27A TC4051BFN, TC4052BFN, TC4053BFN



SOL16-P-150-1.27 TC4051BFT, TC4052BFT, TC4053BFT



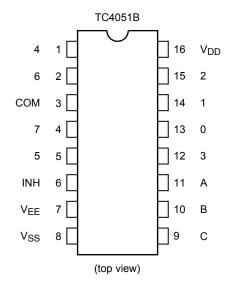
TSSOP16-P-0044-0.65A

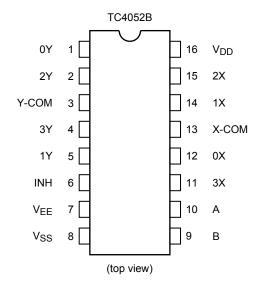
Weight

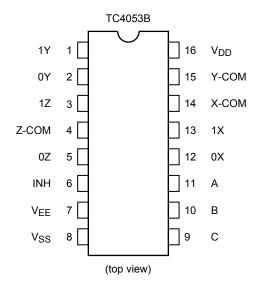
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.) TSSOP16-P-0044-0.65A : 0.06 g (typ.)



Pin Assignment







Truth Table

	Contro	Inputs	-	"ON" Channel					
Inhibit	СΔ	В	Α	TC4051B	TC4052B	TC4053B			
L	L	L	L	0	0X, 0Y	0X, 0Y, 0Z			
L	L	L	Н	1	1X, 1Y	1X, 0Y, 0Z			
L	L	Н	L	2	2X, 2Y	0X, 1Y, 0Z			
L	L	Н	Н	3	3X, 3Y	1X, 1Y, 0Z			
L	Н	L	L	4	_	0X, 0Y, 1Z			
L	Н	L	Н	5	_	1X, 0Y, 1Z			
L	Н	Н	L	6	_	0X, 1Y, 1Z			
L	Н	Н	Н	7	_	1X, 1Y, 1Z			
Н	Х	Х	Х	None	None	None			

2

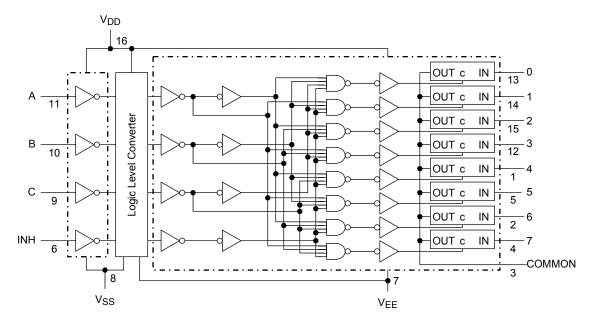
X: Don't care

Δ: Except TC4052B

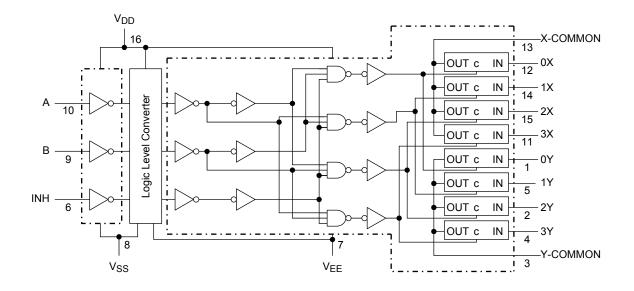


Logic Diagram

TC4051B

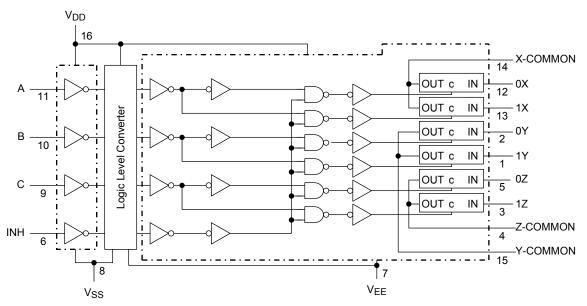


TC4052B



3

TC4053B



Truth Table

Control C	Impedance between IN-OUT	(Note)
Н	0.5 to 5 \times 10 ² Ω	
L	>10 ⁹ Ω	

Note: See electrical characteristics

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD} - V_{SS}	−0.5 to 20	V
DC supply voltage	V _{DD} -V _{EE}	-0.5 to 20	V
Control input voltage	V _{CIN}	V _{SS} – 0.5 to V _{DD} + 0.5	V
Switch I/O voltage	V _I /V _O	V _{EE} – 0.5 to V _{DD} + 0.5	V
Control input current	I _{CIN}	±10	mA
Potential difference across I/O during ON	V _I -V _O	-0.5 to 0.5	٧
Power dissipation	P _D	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
DC supply voltage	V_{DD} - V_{SS}	_	3	_	18	V	
Do supply voltage	V_{DD} - V_{EE}	_	3	_	18	ı v	
Control input voltage	V _{IN}	_	V _{SS}	_	V_{DD}	V	
Input/output voltage	V _{IN} /V _{OUT}	_	VEE		V_{DD}	V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused Control inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics

		Test Condition				-40°C			25°C		85°C		
Characteristics	Symbol		V _{SS} (V)	V _{EE} (V)	V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
			\/	- \/oo	5	3.5	_	3.5	2.75	_	3.5	_	
Control input high voltage	V_{IH}		V _{EE} =		10	7.0		7.0	5.50	_	7.0	_	V
		$V_{IS} = V_{DD}$	to V _S	S	15	11.0	_	11.0	8.25	_	11.0	_	
Control input law		thru 1 kΩ	I _{IS} < 2	2 μΑ	5	_	1.5	_	2.25	1.5	_	1.5	
Control input low voltage	V_{IL}		on all chanr		10	_	3.0	_	4.5	3.0	_	3.0	V
					15	_	4.0	_	6.75	4.0	_	4.0	
On atota		$0 \le V_{IS} \le V_{DD}$	0	0	5	_	850	_	240	950	_	1200	
On-state resistance	R _{ON}	$R_L = 10 \text{ k}\Omega$	0	0	10	_	210	_	110	250	_	300	Ω
			0	0	15		140	_	80	160	_	200	
ΔOn-state	R _{ON} ∆		0	0	5	_		_	10	_	_	_	
resistance between any 2		_	0	0	10	_		_	6	_	_	_	Ω
switches			0	0	15	_		_	4	_	_	_	
Input/output	l _{OFF}	$V_{IN} = 18 \text{ V}, V_{OUT} = 0 \text{ V}$ $V_{IN} = 0 \text{ V}, V_{OUT} = 18 \text{ V}$			18	_	±100	_	±0.01	±100	_	±1000	nA
leakage current					18	_	±100	_	±0.01	±100	_	±1000	
	I _{DD}				5	_	5.0	_	0.005	5.0	_	150	
Quiescent supply current		$V_{IN} = V_{SS}, V_{DD}$	(Note)		10	_	10	_	0.010	10	_	300	μА
				15	_	20	_	0.015	20	_	600		
Input current	I _{IN}	V _{IH} = 18 V V _{IL} = 0 V			18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	μΑ
input current					18		-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	μΑ
Input capacitance	C _{IN}	_						_	5	7.5	_	_	pF
Switch input capacitance	C _{IN}				_	_	_	_	10	_	_	_	pF
	C _{OUT}	TC4051B			10	_	_	_	58	_	_	_	
Output capacitance		TC4052B TC4053B		10	_	_	_	30	_	_	_	pF	
				10	_	_	_	17	_	_	_		
		TC4051B - TC4052B			10		_	_	0.2	_	_	_	
Feedthrough capacitance	C _{IN} - C _{-OUT}			10	_		_	0.2	_	_	_	pF	
Sapaonarios	0-001	TC4053B			10	_	_	_	0.2	_	_	_	

Note: All valid input combinations.



Dynamic Electrical Characteristics (Ta = 25°C, C_L = 50 pF)

Characteristics	Symbol	Te	on V _{SS}			Min	Тур.	Max	Unit	
Characteristics	Symbol				V _{EE}	V _{DD} (V)	IVIIII	тур.		IVIGA
		_		0	0	5	_	15	45	
Phase difference between input to output	φι-О			0	0	10	_	8	20	ns
. ,				0	0	15	_	6	15	
	t. 71			0	0	5	_	170	550	
Propagation delay time	t _{pZL} t _{pZH}			0	0	10	_	90	240	
(A, B, C, -OUT)	ф2н t _{pLZ}	$R_L = 1 \text{ k}\Omega$		0	0	15	_	70	160	ns
(11, 2, 3, 331)	t _{pHZ}			0	-5	5	_	100	240	
	ΨΠΖ			0	-7.5	7.5	_	80	160	
				0	0	5	_	120	380	
Propagation delay time	t _n 71			0	0	10	_	60	200	
(INH-OUT)	^t pZL ^t pZH	$R_L = 1 k\Omega$		0	0	15	_	50	160	ns
(0	-5	5	_	80	200	
				0	-7.5	7.5	_	60	160	
	^t pLZ ^t pHZ			0	0	5	_	170	450	
Propagation delay time				0	0	10	_	90	210	
(INH-OUT)		$R_L = 1 \text{ k}\Omega$		0	0	15	_	70	160	ns
(0	-5	5	_	100	210	
				0	-7.5	7.5		80	160	
-3dB cutoff frequency TC4051B				-5	-5	5	_	20	_	
TC4051B	f _{max} (I-O)	$R_L = 1 \text{ k}\Omega$	(Note 1)	-5	-5	5	_	30	_	MHz
TC4053B				-5	-5	5	_	40	_	
		D 404 5		-2.5	-2.5	2.5	_	0.15	_	
Total harmonic distortion	_	$R_L = 10 \text{ k}\Omega$	(1) (0)	-5	-5	5	_	0.03	_	%
		f = 1 kHz	(Note 2)	-7.5	-7.5	7.5	_	0.02		
-50dB feedthrough		$R_L = 1 k\Omega$	(Note 3)	-5	-5	5	_	500		kHz
(switch off)			(Note 3)	_5	7	3		500		NI IZ
Crosstalk	_	$R_L = 1 \text{ k}\Omega$	(Note 4)	-5	-5	5	_	1.5		MHz
Crosstalk		$R_{IN} = 1 k\Omega$		0	0	5	_	200	_	
(control-OUT)	_	$R_{OUT} = 10 \text{ k}\Omega$		0	0	10	_	400	_	mV
(55.1851 551)		C _L = 15 pF		0	0	15	_	600	_	

Note 1: Sine wave of $\pm 2.5 \ V_{p-p}$ shall be used for V_{is} and the frequency of 20 log 10 $\frac{V_{OS}}{V_{is}} = -3 \text{dB}$ shall be f_{max} .

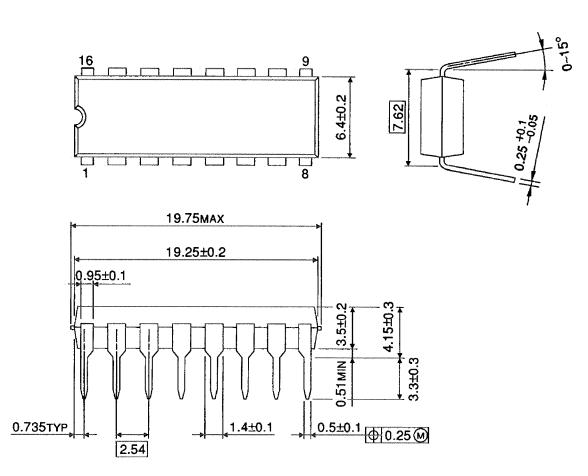
Note 2: V_{is} shall be sine wave of $\pm \left(\frac{V_{DD} - V_{EE}}{4}\right) \ p\text{-p}.$

Note 3: Sine wave of $\pm 2.5 \text{ V}_{\text{p-p}}$ shall be used for V_{is} and the frequency of 20 log 10 $\frac{\text{V}_{\text{OS}}}{\text{V}_{\text{is}}} = -50 \text{dB}$ shall be feed-through.

Note 4: Sine wave of $\pm 2.5 \text{ V}_{\text{p-p}}$ shall be used for V_{is} and the frequency of 20 log 10 $\frac{\text{V}_{\text{OS}}}{\text{V}_{\text{is}}} = -50 \text{dB}$ shall be crosstalk.

Package Dimensions

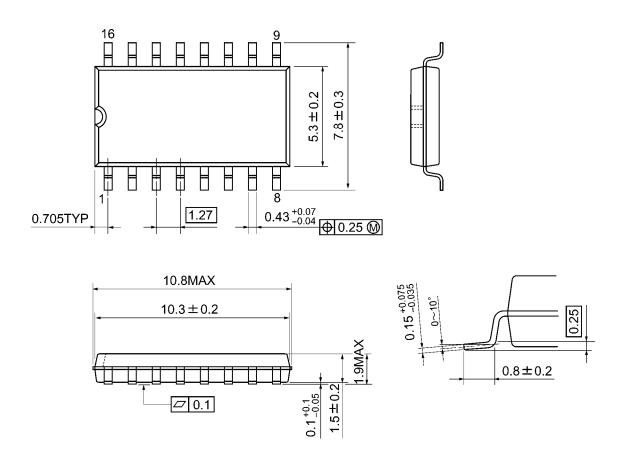
DIP16-P-300-2.54A Unit: mm



Weight: 1.00 g (typ.)

Package Dimensions

SOP16-P-300-1.27A Unit: mm



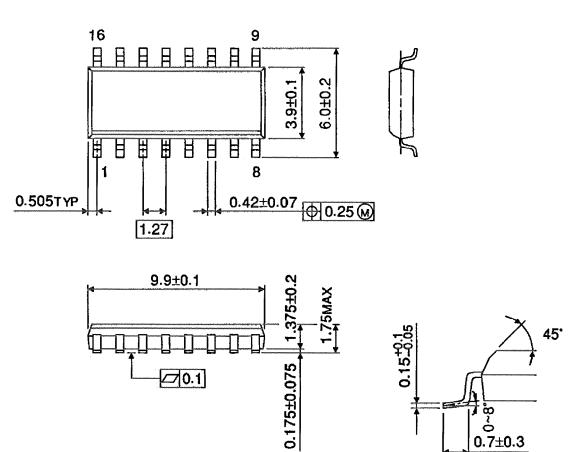
8

Weight: 0.18 g (typ.)



Package Dimensions (Note)

SOL16-P-150-1.27 Unit: mm



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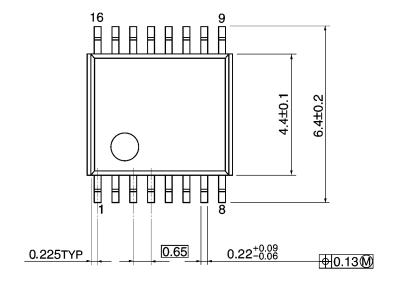
Note: This package is not available in Japan.

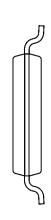
Weight: 0.13 g (typ.)

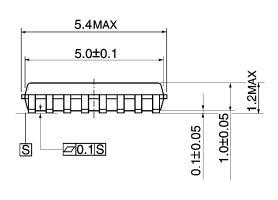
Package Dimensions

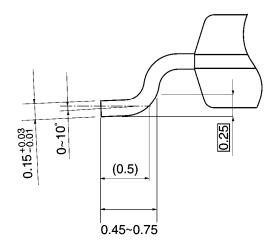
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)



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