TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC14P,TC74AC14F,TC74AC14FN,TC74AC14FT

Hex Schmitt Inverter

The TC74AC14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low pwer dissipation.

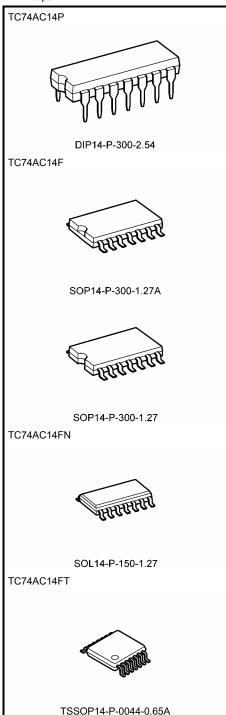
Pin configuration and function are the same as the TC74AC04 but the inputs have hysteresis and with its schmitt trigger function, the TC74AC14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 5.3 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 4 \mu A \text{ (max)}$ at $Ta = 25^{\circ}C$
- Symmetrical output impedance: |IOH| = IOL = 24 mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 5.5 V
- Pin and function compatible with 74F14

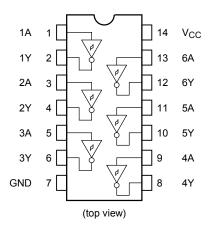
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

Pin Assignment



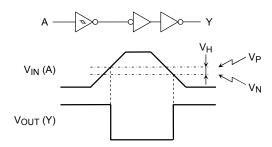
IEC Logic Symbol

1A (1)	_	(2) 1Y
(0)	Б	
2A(3)		(4) 2Y
3A(5)		(6) 3Y
4A (9)		(8) 4Y
5A (11)		(10) 5Y
6A (13)		(12) 6Y

Truth Table

Α	Υ
L	Н
Н	L

System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±150	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

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Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		- Unit	
Characteristics	Symbol				V _C C (V)	Min	Тур.	Max	Min	Max	Offic
					3.0	_	_	2.2	_	2.2	
Positive threshold voltage	V_{P}	_		4.5	_	_	3.2	_	3.2	V	
95					5.5	_	_	3.9	_	3.9	
					3.0	0.5	_	_	0.5	1	
Negative threshold voltage	V_N	_		4.5	0.9	_	_	0.9	_	V	
3.					5.5	1.1	_	-	1.1	1	
		_		3.0	0.3	_	1.2	0.3	1.2		
Hysteresis voltage	V_{H}			4.5	0.4	_	1.4	0.4	1.4	V	
					5.5	0.5	_	1.6	0.5	1.6	
	V _{ОН}	V _{IN} = V _{IL}			2.0	1.9	2.0	_	1.9	_	
			I _{OH} = -50 μA		3.0	2.9	3.0	_	2.9	_	
High-level output					4.5	4.4	4.5	-	4.4	1	- V
voltage			I _{OH} = -4 mA		3.0	2.58	_	_	2.48	_	
			I _{OH} = −24 mA		4.5	3.94	_	_	3.80	_	
			I _{OH} = -75 mA	(Note)	5.5	_	_	_	3.85	_	
		V _{IN} = V _{IH}			2.0	_	0.0	0.1	_	0.1	
Low-level output voltage			I _{OL} = 50 μA		3.0	-	0.0	0.1	_	0.1	
	V _{OL}				4.5	1	0.0	0.1	-	0.1	V
	VOL		I _{OL} = 12 mA		3.0	_	_	0.36	_	0.44	V
			I _{OL} = 24 mA		4.5	_	_	0.36	_	0.44	
			I _{OL} = 75 mA	(Note)	5.5	1	_	-	-	1.65	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	_		±0.1	I	±1.0	μΑ	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND			5.5	_	_	4.0	_	40.0	μΑ

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	•		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay	t _{pLH}		3.3 ± 0.3	_	8.1	13.2	1.0	15.0	ns
time	t _{pHL}	_	5.0 ± 0.5	_	6.0	9.7	1.0	11.0	115
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_	29	_		_	pF

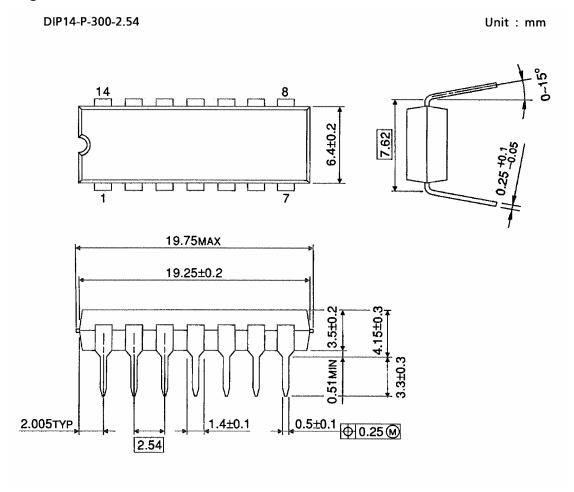
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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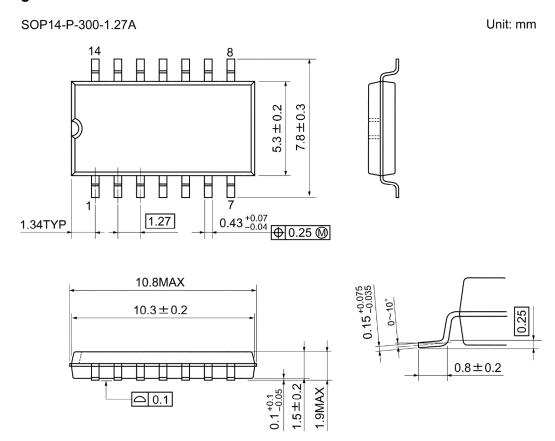
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 (per gate)$



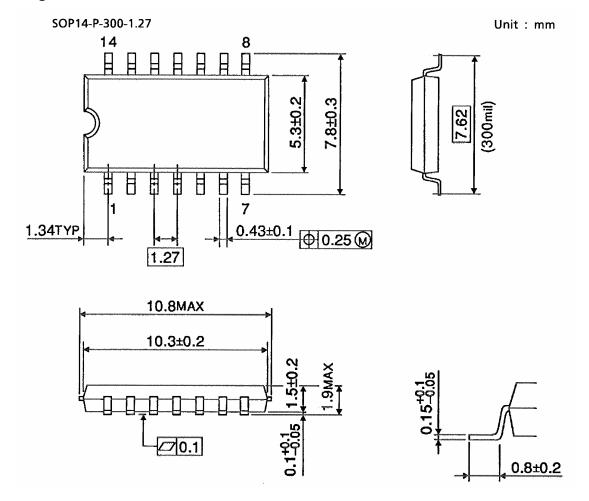


Weight: 0.96 g (typ.)



Weight: 0.18 g (typ.)



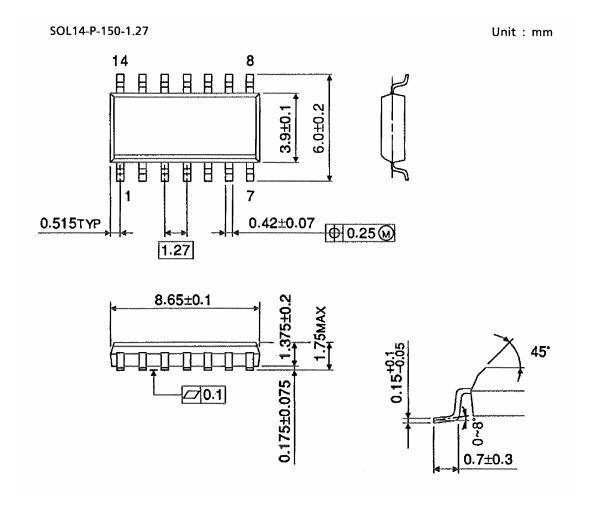


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Weight: 0.18 g (typ.)



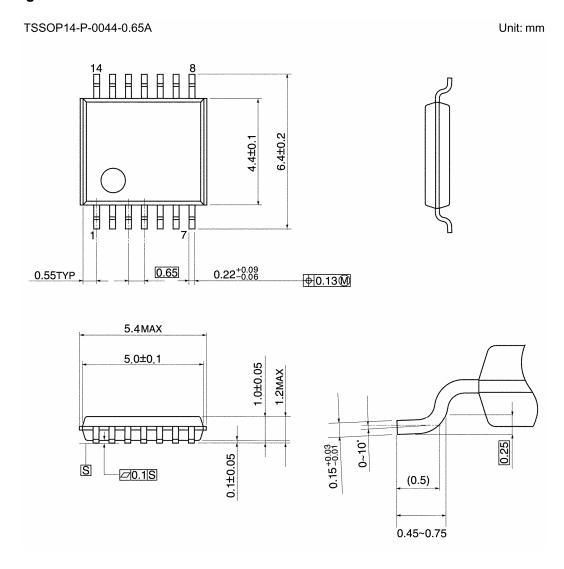
Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)





Weight: 0.06 g (typ.)

Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A

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