TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74ACT14P,TC74ACT14F,TC74ACT14FN,TC74ACT14FT

Hex Schmitt Inverter

The TC74ACT14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

Pin configuration and function are the same as the TC74ACT04 but the inputs have hysteresis and with its schmitt trigger function, the TC74ACT14 can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

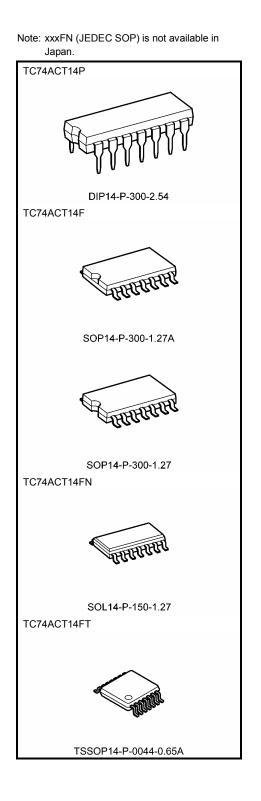
Features

- High speed: $t_{pd} = 6.5 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu A \pmod{at Ta} = 25^{\circ}C$
- Compatible with TTL outputs: VIL = 0.8 V (max)

 $V_{IH} = 2.0 V (min)$

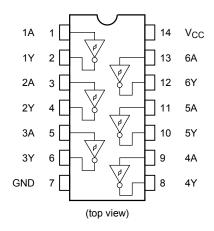
- Symmetrical output impedance: |IOH| = IOL = 24 mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 V to 5.5 V
- Pin and function compatible with 74F14

| : 0.96 g (typ.) |
|-----------------|
| : 0.18 g (typ.) |
| : 0.18 g (typ.) |
| : 0.12 g (typ.) |
| 0.06 g (typ.) |
| |



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Pin Assignment



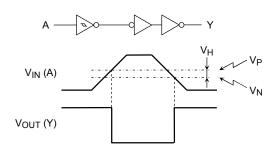
IEC Logic Symbol

| 1A(1) | Б | (2) 1Y |
|--------------------------------|---|------------------|
| 2A - (3) - (5) | | (4) 2Y (6) 3Y |
| 3A <u>(5)</u> 4A <u>(9)</u> | | (8) 4Y |
| 5A (11) | | (10) 5Y |
| 6A (13) | | (12) 6Y |

Truth Table

| А | Y |
|---|---|
| L | Н |
| Н | L |

System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|------------------------------------|------------------|------------------------------------|------|
| Supply voltage range | V _{CC} | -0.5 to 7.0 | V |
| DC input voltage | V _{IN} | -0.5 to V _{CC} + 0.5 | V |
| DC output voltage | V _{OUT} | -0.5 to V _{CC} + 0.5 | V |
| Input diode current | IIK | ±20 | mA |
| Output diode current | I _{ОК} | ±50 | mA |
| DC output current | lout | ±50 | mA |
| DC V _{CC} /ground current | Icc | ±150 | mA |
| Power dissipation | PD | 500 (DIP) (Note 2)/180 (SOP/TSSOP) | mW |
| Storage temperature | T _{stg} | -65 to 150 | °C |

Note1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note2: 500 mW in the range of Ta = -40° C to 65° C. From Ta = 65° C to 85° C a derating factor of -10 mW/° C should be applied up to 300 mW.

Recommended Operating Conditions (Note)

| Characteristics | Symbol | Rating | Unit |
|-----------------------|------------------|----------------------|------|
| Supply voltage | V _{CC} | 4.5 to 5.5 | V |
| Input voltage | V _{IN} | 0 to V _{CC} | V |
| Output voltage | V _{OUT} | 0 to V _{CC} | V |
| Operating temperature | T _{opr} | -40 to 85 | °C |

Note: The recommended operating conditions are required to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

| Characteristics Symbol | | Test Condition | | | Ta = 25°C | | | Ta = −40 to 85°C | | Unit | | | | | | | | | | | | | | |
|------------------------------|-----------------|--|---|----------------------|-----------|------|------|---------------------|------|------|-------|--------------------------------------|--|--|--|-------------------------|-----|---|---|---|------|---|------|---|
| | | | | V _C (V | | Min | Тур. | Max | Min | Max | Offic | | | | | | | | | | | | | |
| Positive threshold voltage | VP | _ | | | 5 | _ | _ | 2.0 | I | 2.0 | V | | | | | | | | | | | | | |
| Negative threshold voltage | V _N | _ | | | 5 | 0.8 | | | 0.8 | | V | | | | | | | | | | | | | |
| Hysteresis voltage | V _H | - | | | 5 | 0.4 | | 1.2 | 0.4 | 1.2 | V | | | | | | | | | | | | | |
| | | | I _{OH} = -50 μA | 4.5 | 5 | 4.4 | 4.5 | — | 4.4 | — | | | | | | | | | | | | | | |
| High-level output voltage | VOH | V _{IN} = V _{IL} | I _{OH} = −24 mA | 4.5 | 5 | 3.94 | — | _ | 3.80 | — | V | | | | | | | | | | | | | |
| | | | I _{OH} = −75 mA (No | ote) 5.8 | 5 | — | — | _ | 3.85 | — | | | | | | | | | | | | | | |
| | | | I _{OL} = 50 μA | 4.5 | 5 | | 0.0 | 0.1 | _ | 0.1 | | | | | | | | | | | | | | |
| Low-level output voltage | V _{OL} | V _{IN} = VILI | | | | | | | | | | V _{IN} = V _{IH} | | | | I _{OL} = 24 mA | 4.5 | 5 | — | — | 0.36 | — | 0.44 | V |
| · o | | | I _{OL} = 75 mA (No | ote) 5.8 | 5 | — | _ | | - | 1.65 | | | | | | | | | | | | | | |
| Input leakage current | I _{IN} | V _{IN} = V _{CC} or GND | | | 5 | Ι | Ι | ±0.1 | - | ±1.0 | μA | | | | | | | | | | | | | |
| Icc | | V _{IN} = V _{CC} or GND | | 5.8 | 5 | _ | _ | 4.0 | _ | 40.0 | μA | | | | | | | | | | | | | |
| Quiescent supply current | ΙC | | t: V _{IN} = 3.4 V put: V _{CC} or GND | 5.5 | 5 | | | 1.35 | _ | 1.5 | mA | | | | | | | | | | | | | |

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

| Characteristics | Symbol Test Condition | | | Ta = 25°C | | Ta = −40 to 85°C | | Unit | |
|-------------------------------|--------------------------------------|---|---------------------|-----------|------|---------------------|-----|------|----|
| | -, | | V _{CC} (V) | Min | Тур. | Max | Min | Max | |
| Propagation delay time | t _{pLH} t _{pHL} | - | 5.0 ± 0.5 | _ | 7.2 | 11.4 | 1.0 | 13.0 | ns |
| Input capacitance | CIN | _ | | _ | 5 | 10 | _ | 10 | pF |
| Power dissipation capacitance | C _{PD} | | (Note) | _ | 30 | _ | _ | _ | pF |

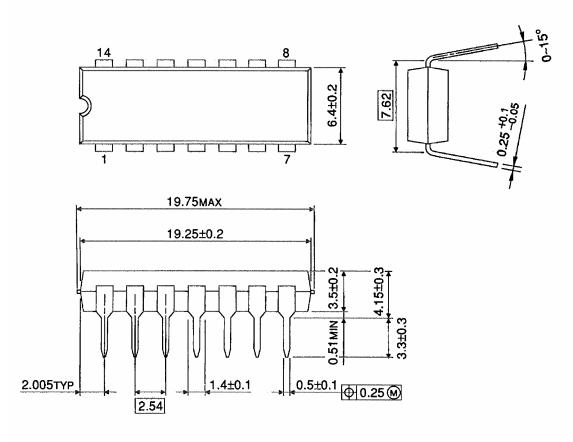
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

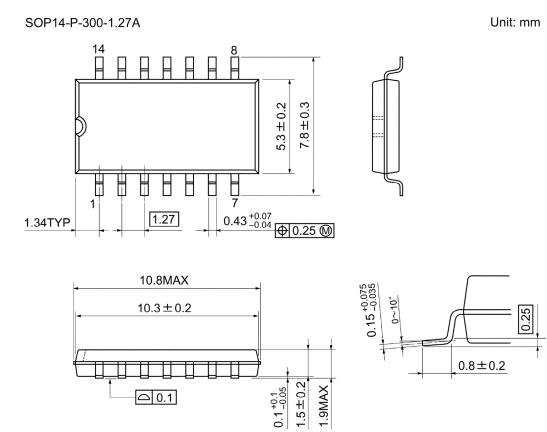
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

DIP14-P-300-2.54

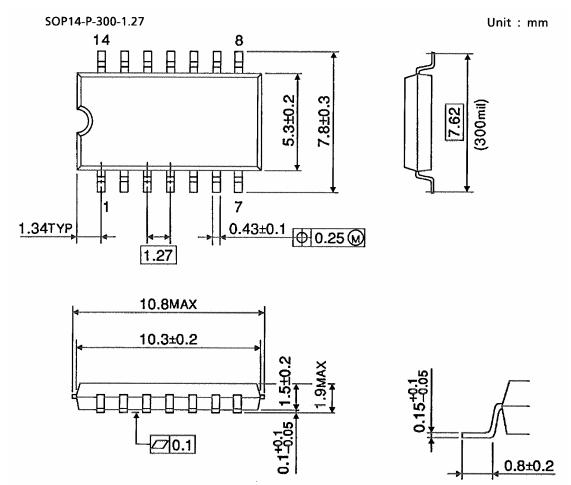
Unit : mm



Weight: 0.96 g (typ.)

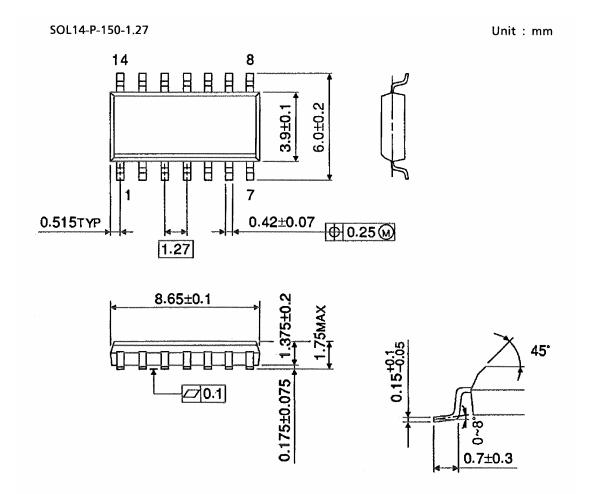


Weight: 0.18 g (typ.)



Weight: 0.18 g (typ.)

Package Dimensions (Note)

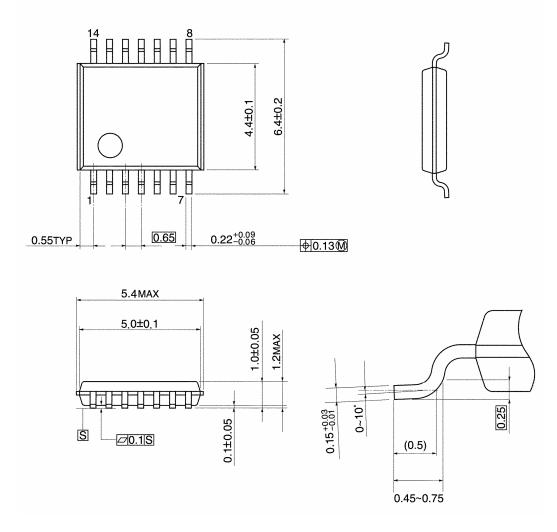


Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

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Note: Lead (Pb)-Free Packages

DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A

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