

TC74HC7240AP,TC74HC7240AF,TC74HC7244AP,TC74HC7244AF

Octal Bus Buffer (with schmitt trigger inputs)

TC74HC7240AP/AF Inverted, 3-State Outputs

TC74HC7244AP/AF Non-Inverted, 3-State Outputs

The TC74HC7240A/7244A are high speed CMOS OCTAL BUS BUFFERS with silicon gate C²MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

The TC74HC7240A/7244A have same pin configuration and function as the TC74HC240A/244A. And they have a hysteresis characteristics with each input, so TC74HC7240A/7244A can be used as a line receiver, etc.

They have two active low output enables.

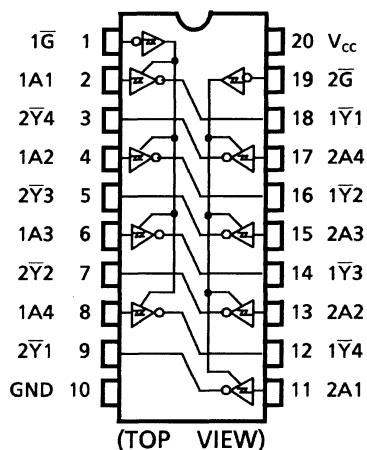
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

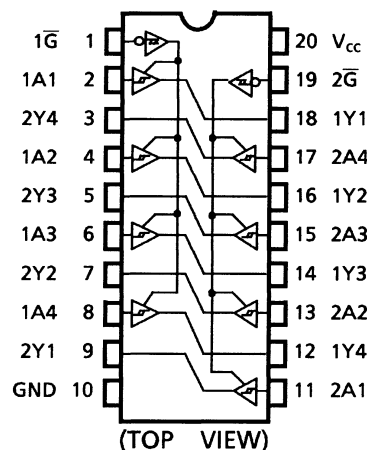
- High speed: $t_{pd} = 15 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 4 \mu\text{A}$ (max) at $T_a = 25^\circ\text{C}$
- High noise immunity: $V_H = 1.1 \text{ V}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 6 \text{ mA}$ (min)
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: $V_{CC} (\text{opr}) = 2 \text{ to } 6 \text{ V}$
- Pin and function compatible with 74LS240/244

Pin Assignment

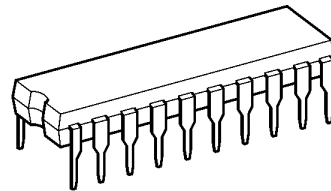
TC74HC7240A



TC74HC7244A

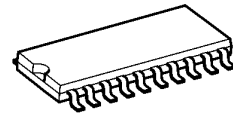


TC74HC7240AP, TC74HC7244AP

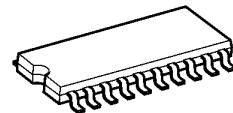


DIP20-P-300-2.54A

TC74HC7240AF, TC74HC7244AF



SOP20-P-300-1.27A



SOP20-P-300-1.27

Weight

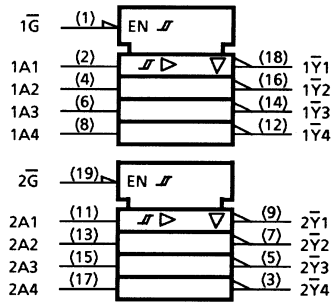
DIP20-P-300-2.54A : 1.30 g (typ.)

SOP20-P-300-1.27A : 0.22 g (typ.)

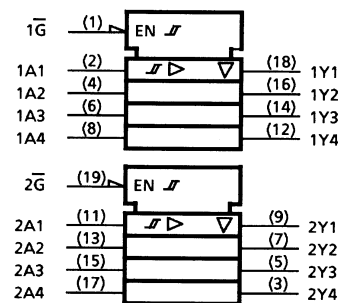
SOP20-P-300-1.27 : 0.22 g (typ.)

IEC Logic Symbol

TC74HC7240A



TC74HC7244A



Truth Table

Inputs		Outputs	
\bar{G}	A_n	Y_n	\bar{Y}_n^{Δ}
L	L	L	H
L	H	H	L
H	X	Z	Z

Δ : For TC74HC7240A only

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7	V
DC input voltage	V_{IN}	-0.5 to $V_{CC} + 0.5$	V
DC output voltage	V_{OUT}	-0.5 to $V_{CC} + 0.5$	V
Input diode current	I_{IK}	± 20	mA
Output diode current	I_{OK}	± 20	mA
DC output current	I_{OUT}	± 35	mA
DC V_{CC} /ground current	I_{CC}	± 75	mA
Power dissipation	P_D	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T_{stg}	-65 to 150	$^{\circ}C$

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of $T_a = -40$ to $65^{\circ}C$. From $T_a = 65$ to $85^{\circ}C$ a derating factor of -10 mW/ $^{\circ}C$ shall be applied until 300 mW.

Recommended Operating Conditions (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2 to 6	V
Input voltage	V_{IN}	0 to V_{CC}	V
Output voltage	V_{OUT}	0 to V_{CC}	V
Operating temperature	T_{opr}	-40 to 85	°C

Note: The recommended operating conditions are required to ensure the normal operation of the device.
Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics
DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
				V _{CC} (V)	Min	Typ.	Max	Min		Max
Positive threshold voltage	V _P	—		2.0 4.5 6.0	1.0 2.3 3.0	1.25 2.7 3.5	1.5 3.15 4.2	1.0 2.3 3.0	1.5 3.15 4.2	V
Negative threshold voltage	V _N	—		2.0 4.5 6.0	0.3 1.13 1.5	0.65 1.6 2.3	0.9 2.0 2.6	0.3 1.13 1.5	0.9 2.0 2.6	V
Hysteresis voltage	V _H	—		2.0 4.5 6.0	0.3 0.6 0.8	0.6 1.1 1.2	1.0 1.4 1.7	0.3 0.6 0.8	1.0 1.4 1.7	V
High-level output voltage	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = −20 μA	2.0	1.9	2.0	—	1.9	—	V
				4.5	4.4	4.5	—	4.4	—	
				6.0	5.9	6.0	—	5.9	—	
			I _{OH} = −6 mA I _{OH} = −7.8 mA	4.5 6.0	4.18 5.68	4.31 5.80	— —	4.13 5.63	— —	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH} or V _{IL}		I _{OL} = 20 μA	2.0	—	0.0	0.1	—	0.1
			4.5		—	0.0	0.1	—	0.1	
			6.0		—	0.0	0.1	—	0.1	
			I _{OL} = 6 mA I _{OL} = 7.8 mA	4.5 6.0	— —	0.17 0.18	0.26 0.26	— —	0.33 0.33	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		6.0	—	—	±0.5	—	±5.0	μA
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	—	—	4.0	—	40.0	μA

AC Characteristics (input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = −40 to 85°C		Unit
			CL (pF)	VCC (V)	Min	Typ.	Max	Min	Max	
Output transition time	tTLH	—	50	2.0	—	25	60	—	75	ns
	tTHL			4.5	—	7	12	—	15	
				6.0	—	6	10	—	13	
Propagation delay time	tPLH tPHL	—	50	2.0	—	50	125	—	155	ns
				4.5	—	15	25	—	31	
				6.0	—	13	21	—	26	
			150	2.0	—	67	165	—	205	
				4.5	—	20	33	—	41	
				6.0	—	17	28	—	35	
Output enable time	tPZL tPZH	RL = 1 kΩ	50	2.0	—	68	150	—	190	ns
				4.5	—	21	30	—	38	
				6.0	—	16	26	—	32	
			150	2.0	—	84	165	—	230	
				4.5	—	26	37	—	46	
				6.0	—	20	31	—	39	
Output disable time	tPLZ tPHZ	RL = 1 kΩ	50	2.0	—	48	150	—	190	ns
				4.5	—	21	30	—	38	
				6.0	—	19	26	—	32	
Input capacitance	CIN	—			—	5	10	—	10	pF
Output capacitance	COUT	—			—	10	—	—	—	pF
Power dissipation capacitance	CPD (Note)	TC74HC7240A			—	33	—	—	—	pF
		TC74HC7244A			—	34	—	—	—	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

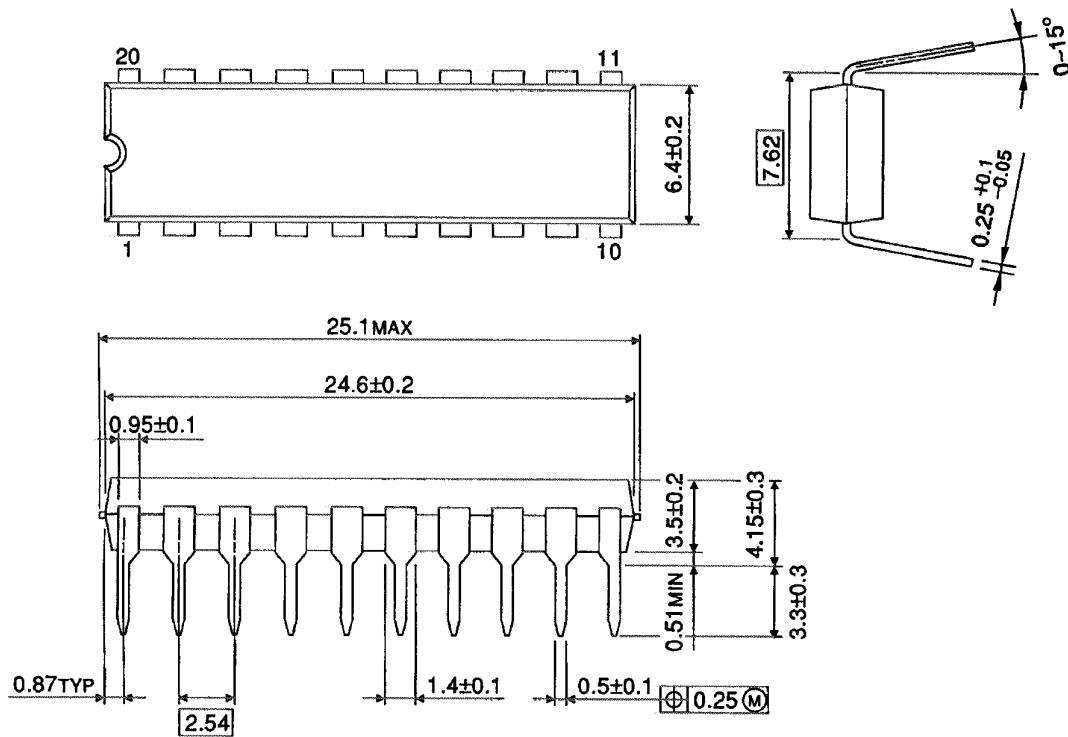
Average operating current can be obtained by the equation:

$$I_{CC}(\text{opr}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$$

Package Dimensions

DIP20-P-300-2.54A

Unit : mm

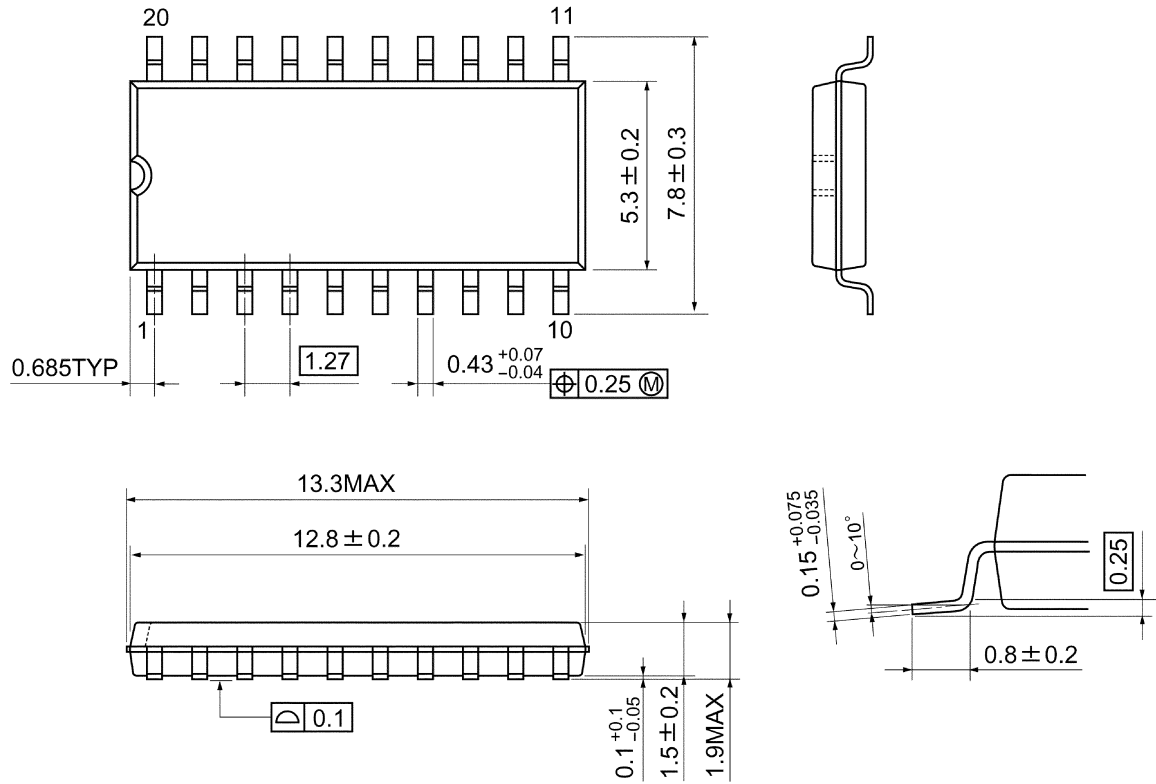


Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A

Unit: mm

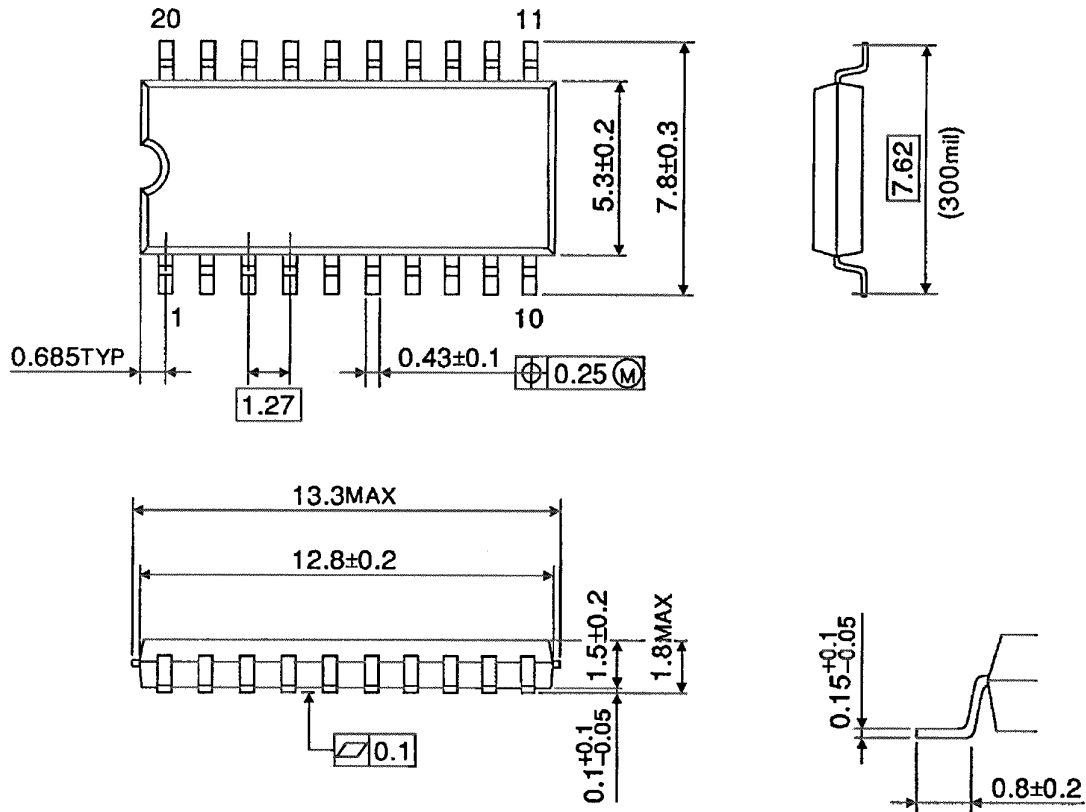


Weight: 0.22 g (typ.)

Package Dimensions

SOP20-P-300-1.27

Unit : mm



Weight: 0.22 g (typ.)

Note: Lead (Pb)-Free Packages

DIP20-P-300-2.54A SOP20-P-300-1.27A

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