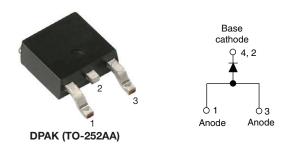
Vishay Semiconductors

High Performance Schottky Rectifier, 5.5 A



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PRIMARY CHARACTERISTICS								
I _{F(AV)}	5.5 A							
V _R	100 V							
V _F at I _F	See Electrical table							
I _{RM}	4 mA at 125 °C							
T _J max.	150 °C							
E _{AS}	6 mJ							
Package	DPAK (TO-252AA)							
Circuit configuration	Single							

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
 Deputer DDA(c authors)
 Halogen
- Popular DPAK outline
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\mathrm{C}$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-50WQ10FN-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	5.5	A					
V _{RRM}		100	V					
I _{FSM}	t _p = 5 μs sine	330	А					
V _F	5 A _{pk} , T _J = 125 °C	0.63	V					
TJ	Range	-40 to +150	°C					

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-50WQ10FN-M3	UNITS						
Maximum DC reverse voltage	V _R	100	V						
Maximum working peak reverse voltage	V _{RWM}	100	v						

ABSOLUTE MAXIMUM RATINGS										
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS						
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 135 °C	5.5							
Maximum peak one cycle non-repetitive surge current			Following any rated load condition and with rated	330	А					
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	110						
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \ ^\circ C, \ I_{AS} = 0.5 \ A, \ L = 40 \ m$	6.0	mJ						
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T_J maxim	0.5	А						

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FREE



VS-50WQ10FN-M3

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS						
Maximum forward voltage drop See fig. 1		5 A	T _{.1} = 25 °C	0.77					
	V _{FM} ⁽¹⁾	10 A	1j=23 0	0.91	V				
	VFM (*)	5 A	T _{.1} = 125 °C	0.63					
		10 A	1j = 125 C	0.74					
Maximum reverse leakage current	I _{BM} ⁽¹⁾	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	mA				
See fig. 2	IRM (**	T _J = 125 °C	VR = naleu VR	4	mA				
Threshold voltage	V _{F(TO)}			0.47	V				
Forward slope resistance	r _t	$T_J = T_J$ maximum		21.46	mΩ				
Typical junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal range	183	pF					
Typical series inductance	L _S	Measured lead to lead 5 mm	from package body	5.0	nH				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		-40 to +150	°C					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W					
Approximate weight			0.3	g					
Approximate weight			0.01	oz.					
Marking device		Case style DPAK (TO-252AA)	50WQ10FN						

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



VS-50WQ10FN-M3

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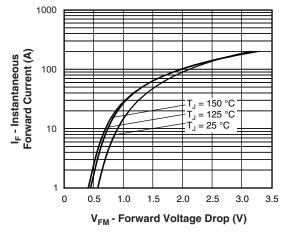


Fig. 1 - Maximum Forward Voltage Drop Characteristics

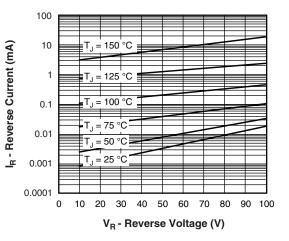


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

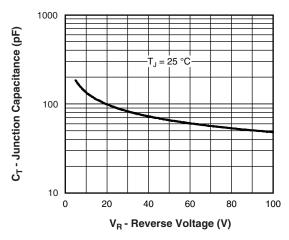
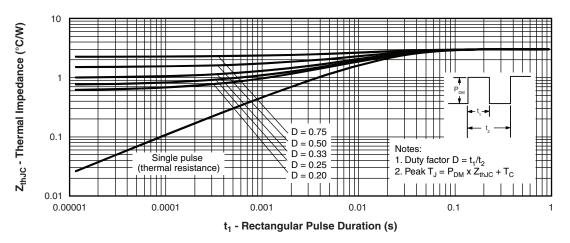


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





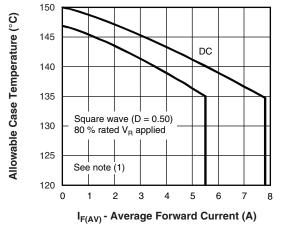
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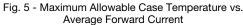
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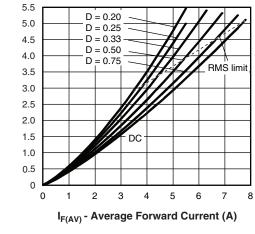
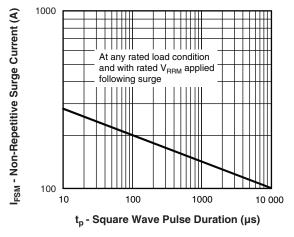


Fig. 6 - Forward Power Loss Characteristics



Average Power Loss (W)

Fig. 7 - Maximum Non-Repetitive Surge Current

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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ORDERING INFORMATION TABLE

				1			<u> </u>		
Device code	VS-	50	w	Q	10	FN	TRL	-M3	
	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	
		Viel		aioonduk	toro pr	duct			
	1 - Vishay Semiconductors product								
	2 - Current rating (5.5 A)								
	3 -	- Package identifier:							
		VV =	DPAK						
	4 -	Sch	ottky "C	" series					
	5 -	Volt	age rati	ng (10 =	= 100 V)				
	6 -	FN	= TO-25	52AA (D	PAK)				
	7.	• N	one = tu	be					
		• TR = tape and reel							
		• TRL = tape and reel (left oriented)							
		• TRR = tape and reel (right oriented)							
	8 -			ntal digit			,		

-M3 = halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-50WQ10FN-M3	75	3000	Antistatic plastic tube						
VS-50WQ10FNTR-M3	2000	2000	13" diameter reel						
VS-50WQ10FNTRL-M3	3000	3000	13" diameter reel						
VS-50WQ10FNTRR-M3	3000	3000	13" diameter reel						

LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?95627							
Part marking information	www.vishay.com/doc?95176						
Packaging information	www.vishay.com/doc?95033						





D-PAK (TO-252AA) "M"

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STNIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	2.18	2.39	0.086	0.094			е	2.29	BSC	0.090	BSC	
A1	-	0.13	-	0.005			Н	9.40	10.41	0.370	0.410	
b	0.64	0.89	0.025	0.035			L	1.40	1.78	0.055	0.070	
b2	0.76	1.14	0.030	0.045			L1	2.74	BSC	0.108	REF.	
b3	4.95	5.46	0.195	0.215	3		L2	0.51	BSC	0.020	BSC	
с	0.46	0.61	0.018	0.024			L3	0.89	1.27	0.035	0.050	3
c2	0.46	0.89	0.018	0.035			L4	-	1.02	-	0.040	
D	5.97	6.22	0.235	0.245	5		L5	1.14	1.52	0.045	0.060	2
D1	5.21	-	0.205	-	3		Ø	0°	10°	0°	10°	
E	6.35	6.73	0.250	0.265	5		Ø1	0°	15°	0°	15°	
E1	4.32	-	0.170	-	3		Ø2	25°	35°	25°	35°	

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

(4) Section C - C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip

(5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁶⁾ Dimension b1 and c1 applied to base metal only

⁽⁷⁾ Datum A and B to be determined at datum plane H

⁽⁸⁾ Outline conforms to JEDEC[®] outline TO-252AA



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