

# Device Modeling Report

COMPONENTS : OPERATIONAL AMPLIFIER (CMOS)  
PART NUMBER : NJU7061D  
MANUFACTURER : NEW JAPAN RADIO



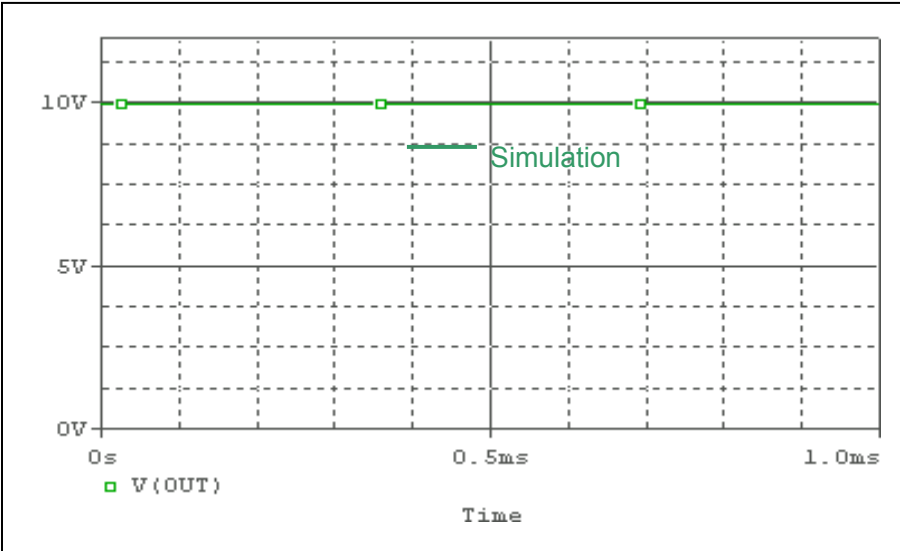
**Bee Technologies Inc.**

## MOSFET MODEL

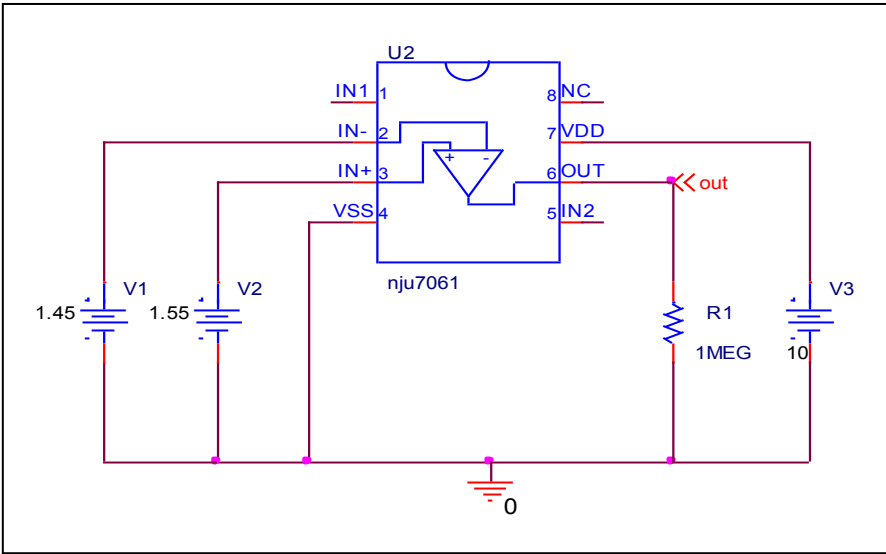
| Pspice model parameter | Model description                                  |
|------------------------|----------------------------------------------------|
| LEVEL                  |                                                    |
| L                      | Channel Length                                     |
| W                      | Channel Width                                      |
| KP                     | Transconductance                                   |
| RS                     | Source Ohmic Resistance                            |
| RD                     | Ohmic Drain Resistance                             |
| VTO                    | Zero-bias Threshold Voltage                        |
| RDS                    | Drain-Source Shunt Resistance                      |
| TOX                    | Gate Oxide Thickness                               |
| CGSO                   | Zero-bias Gate-Source Capacitance                  |
| CGDO                   | Zero-bias Gate-Drain Capacitance                   |
| CBD                    | Zero-bias Bulk-Drain Junction Capacitance          |
| MJ                     | Bulk Junction Grading Coefficient                  |
| PB                     | Bulk Junction Potential                            |
| FC                     | Bulk Junction Forward-bias Capacitance Coefficient |
| RG                     | Gate Ohmic Resistance                              |
| IS                     | Bulk Junction Saturation Current                   |
| N                      | Bulk Junction Emission Coefficient                 |
| RB                     | Bulk Series Resistance                             |
| PHI                    | Surface Inversion Potential                        |
| GAMMA                  | Body-effect Parameter                              |
| DELTA                  | Width effect on Threshold Voltage                  |
| ETA                    | Static Feedback on Threshold Voltage               |
| THETA                  | Modility Modulation                                |
| KAPPA                  | Saturation Field Factor                            |
| VMAX                   | Maximum Drift Velocity of Carriers                 |
| XJ                     | Metallurgical Junction Depth                       |
| UO                     | Surface Mobility                                   |

# Output Voltage Swing

## Simulation result



## Evaluation Circuit

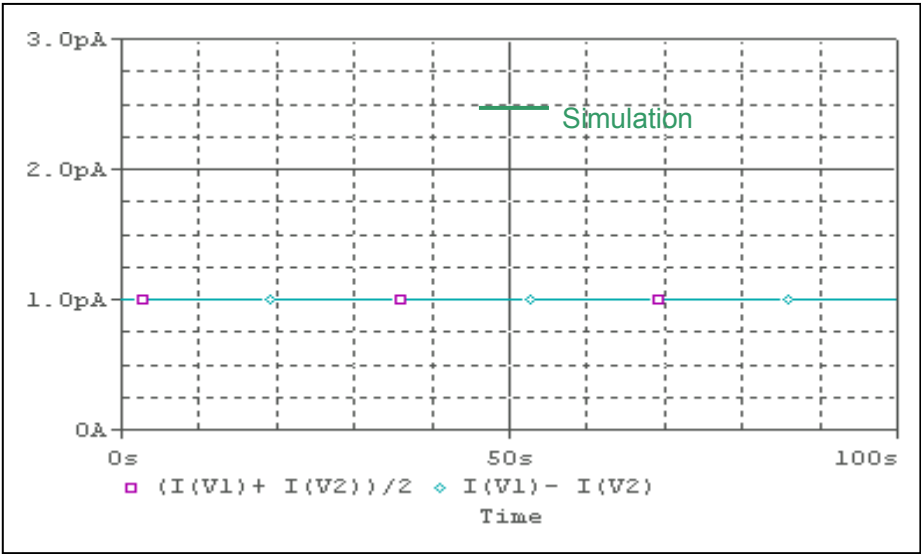


## Compasion Table

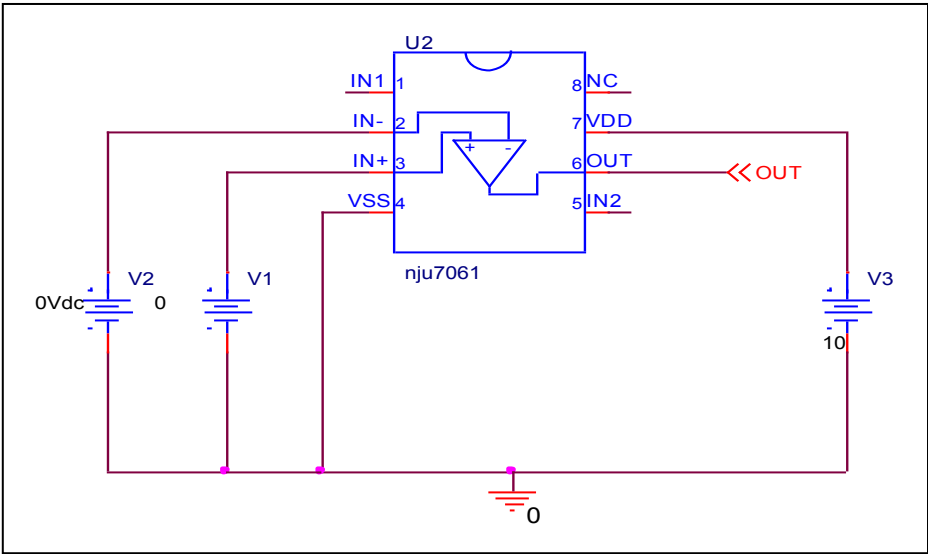
|              | Measurement | Simulation | %Error |
|--------------|-------------|------------|--------|
| $V_{OM}$ (V) | 9.98        | 9.979      | -0.01  |

# Input Current

## Simulation result



## Evaluation Circuit

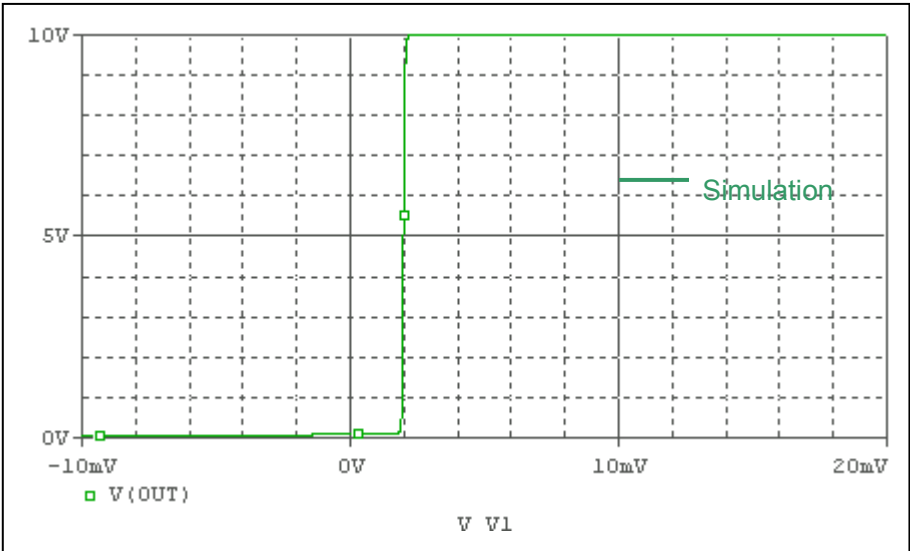


## Compasion Table

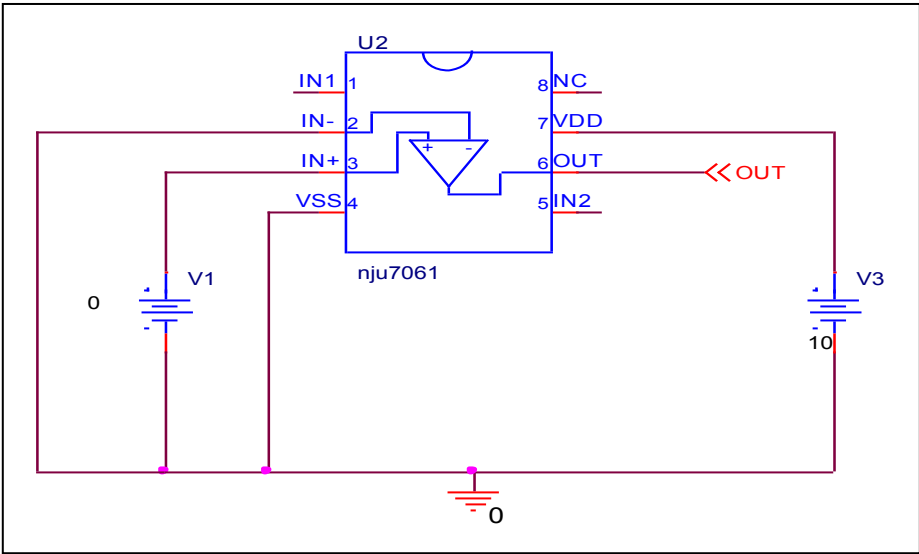
|               | Measurement | Simulation | % Error |
|---------------|-------------|------------|---------|
| $I_b$ (pA)    | 1           | 1.002      | 0.2     |
| $I_{os}$ (pA) | 1           | 0.995      | -0.5    |

# Input Offset Voltage

## Simulation result



## Evaluation Circuit

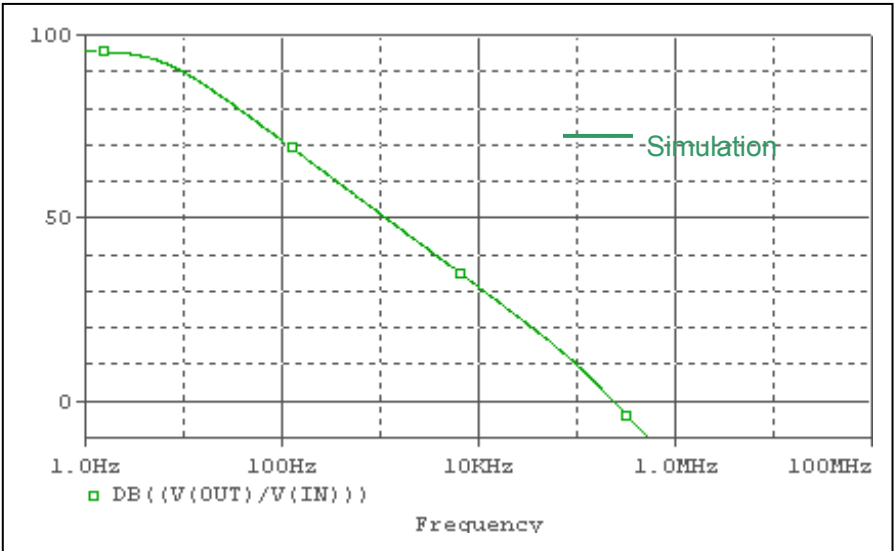


## Compasion Table

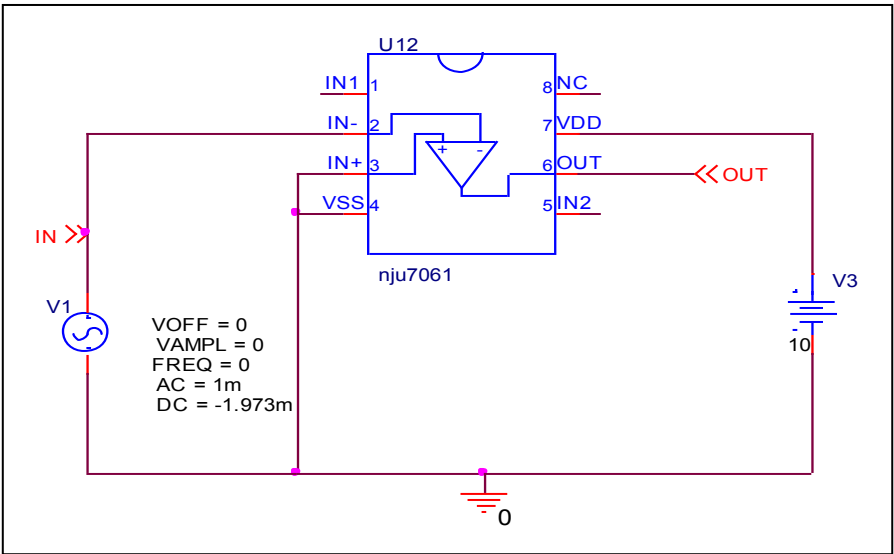
|                      | Measurement | Simulation | %Error |
|----------------------|-------------|------------|--------|
| V <sub>os</sub> (mV) | 2           | 1.973      | -1.35  |

# Open loop Voltage Gain

## Simulation result



## Evaluation Circuit

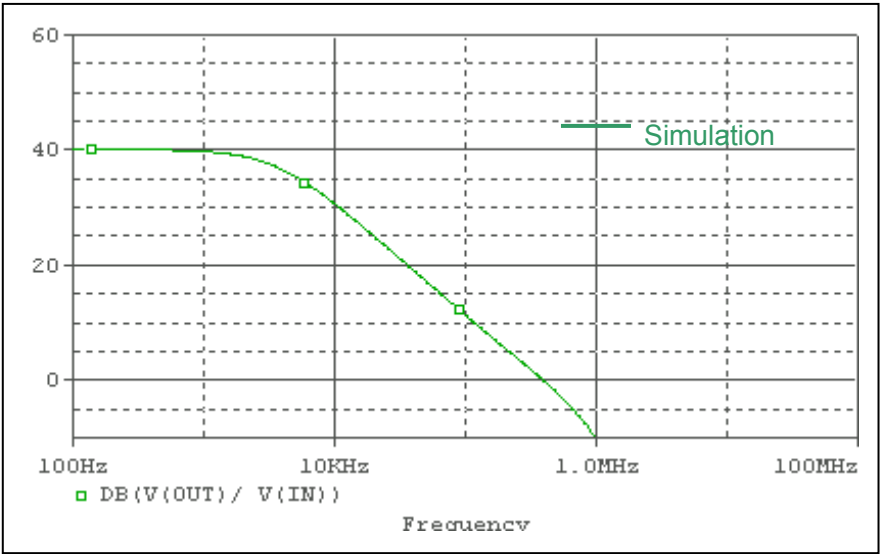


## Compasion Table

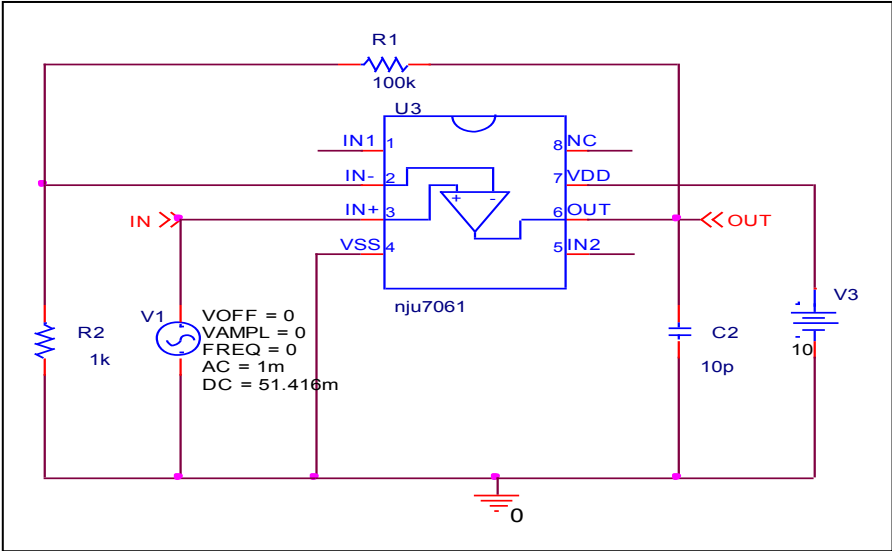
|                | Measurement | Simulation | %Error |
|----------------|-------------|------------|--------|
| <b>Av (dB)</b> | 95          | 95.571     | 0.601  |

# Unity Gain Frequency

## Simulation result



## Evaluation Circuit

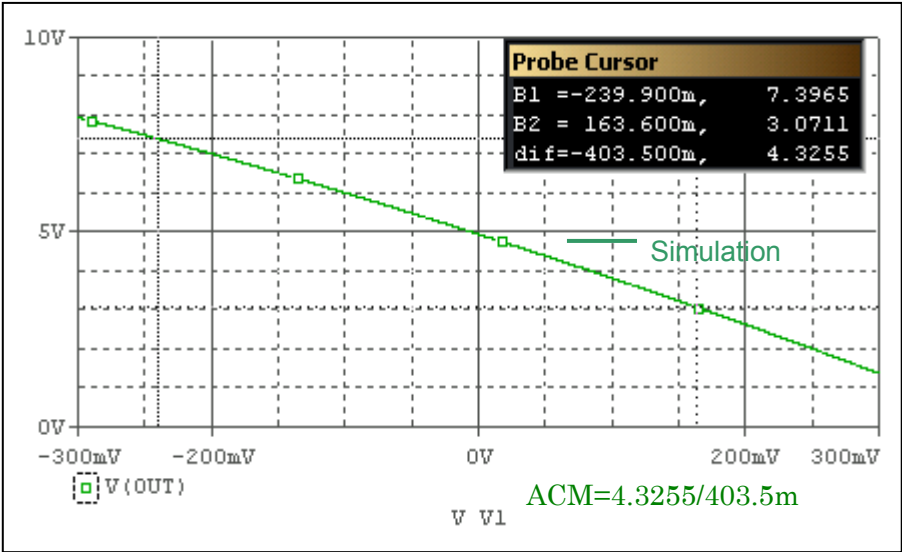


## Compasion Table

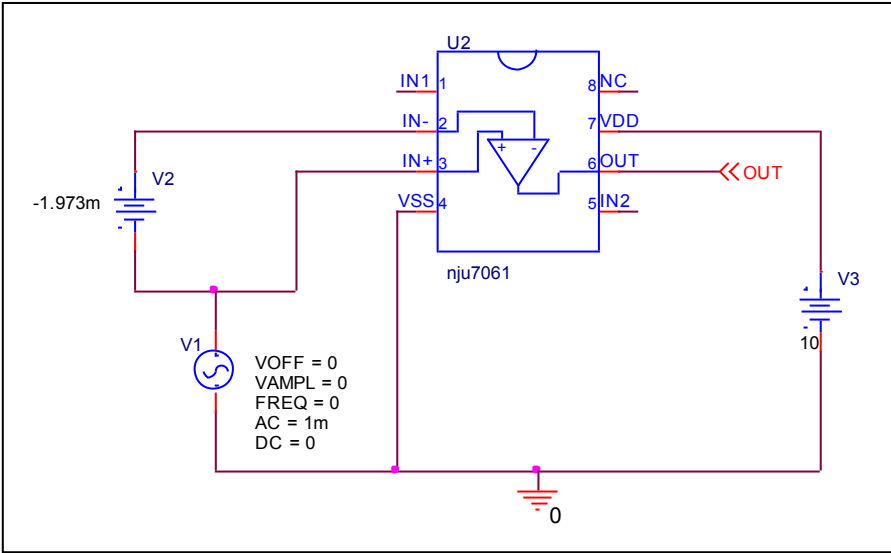
| $A_v=40\text{dB}, C_L=10\text{pF}$ | Measurement | Simulation | %Error |
|------------------------------------|-------------|------------|--------|
| <b>Ft(MHz)</b>                     | 0.4         | 0.393      | -1.75  |

# Common-Mode Rejection Ratio

## Simulation result



## Evaluation Circuit



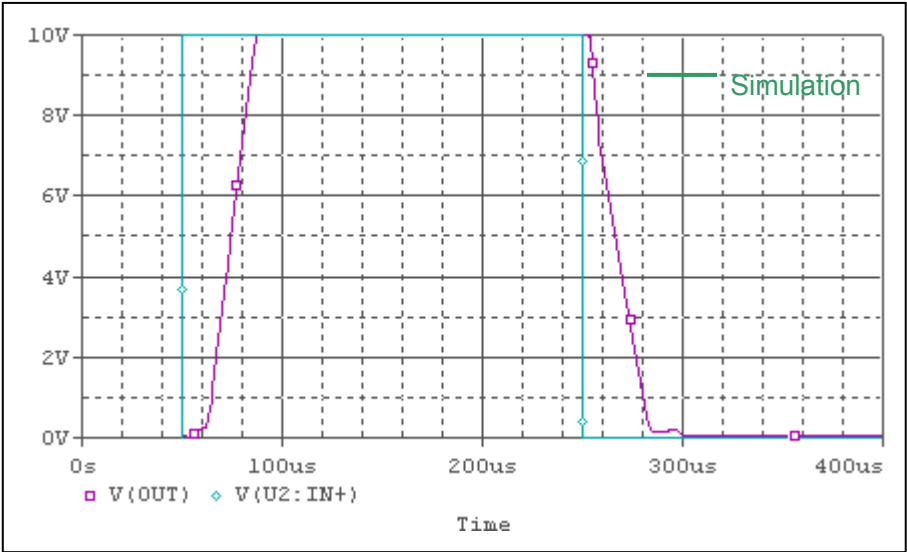
$$CMRR= AV / ACM$$

## Compason Table

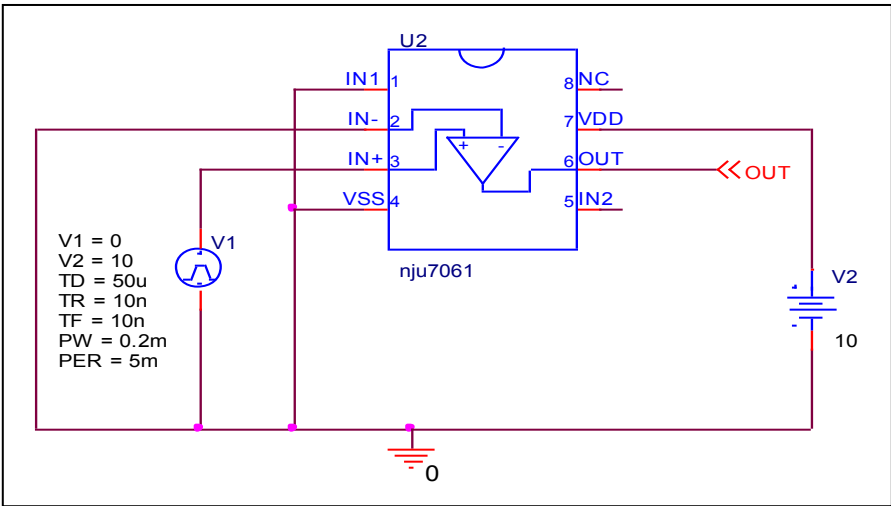
|           | Measurement | Simulation | %Error |
|-----------|-------------|------------|--------|
| CMRR (dB) | 75          | 74.967     | -0.044 |

# Slew Rate

## Simulation result



## Evaluation Circuit



## Compasion Table

|           | Measurement | Simulation | % Error |
|-----------|-------------|------------|---------|
| SR (V/us) | 0.4         | 0.404      | 1       |