

Device Modeling Report

COMPONENTS: Digital transistor (BRT)
PART NUMBER: RN1961FS
MANUFACTURER: TOSHIBA



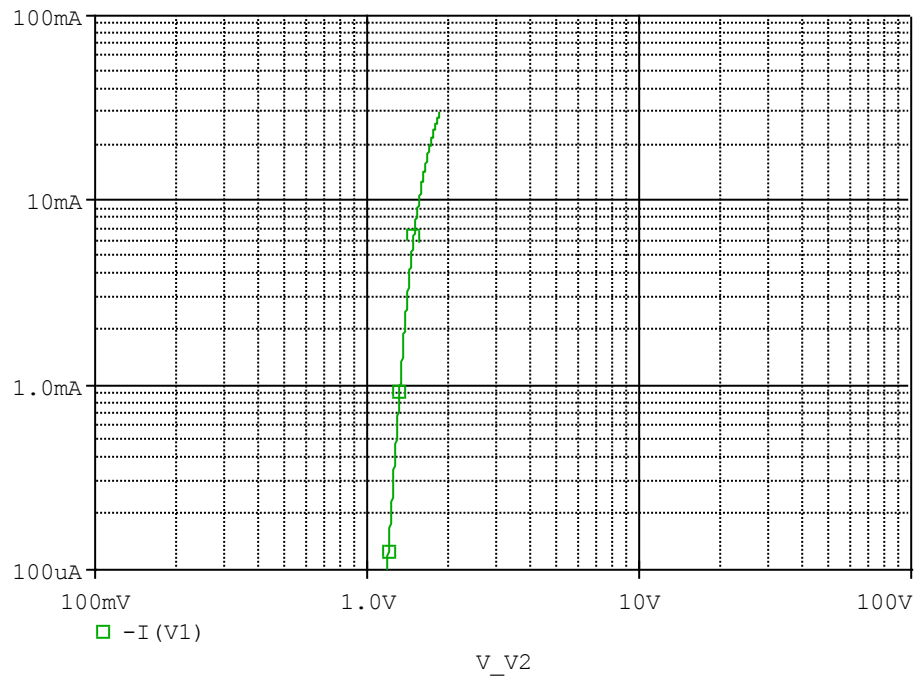
Bee Technologies Inc.

DIGITAL TRANSISTOR MODEL

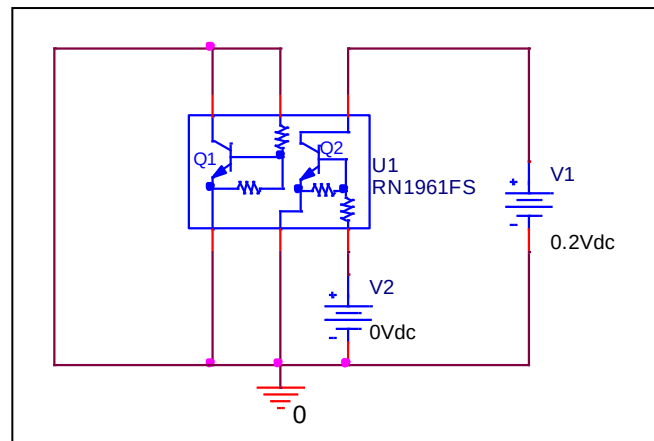
PSpice model parameter	Model description
IS	Saturation Current
BF	Ideal Maximum Forward Beta
NF	Forward Current Emission Coefficient
VAF	Forward Early Voltage
IKF	Forward Beta Roll-off Knee Current
ISE	Non-ideal Base-Emitter Diode Saturation Current
NE	Non-ideal Base-Emitter Diode Emission Coefficient
BR	Ideal Maximum Reverse Beta
NR	Reverse Emission Coefficient
VAR	Reverse Early Voltage
IKR	Reverse Beta Roll-off Knee Current
ISC	Non-ideal Base-Collector Diode Saturation Current
NC	Non-ideal Base-Collector Diode Emission Coefficient
NK	Forward Beta Roll-off Slope Exponent
RE	Emitter Resistance
RB	Base Resistance
RC	Series Collector Resistance
CJE	Zero-bias Emitter-Base Junction Capacitance
VJE	Emitter-Base Junction Potential
MJE	Emitter-Base Junction Grading Coefficient
CJC	Zero-bias Collector-Base Junction Capacitance
VJC	Collector-base Junction Potential
MJC	Collector-base Junction Grading Coefficient
FC	Coefficient for Onset of Forward-bias Depletion Capacitance
TF	Forward Transit Time
XTF	Coefficient for TF Dependency on Vce
VTF	Voltage for TF Dependency on Vce
ITF	Current for TF Dependency on Ic
PTF	Excess Phase at $f=1/2\pi*TF$
TR	Reverse Transit Time
EG	Activation Energy
XTB	Forward Beta Temperature Coefficient
XTI	Temperature Coefficient for IS

I_C-V_{I(ON)} Characteristics

Circuit simulation result

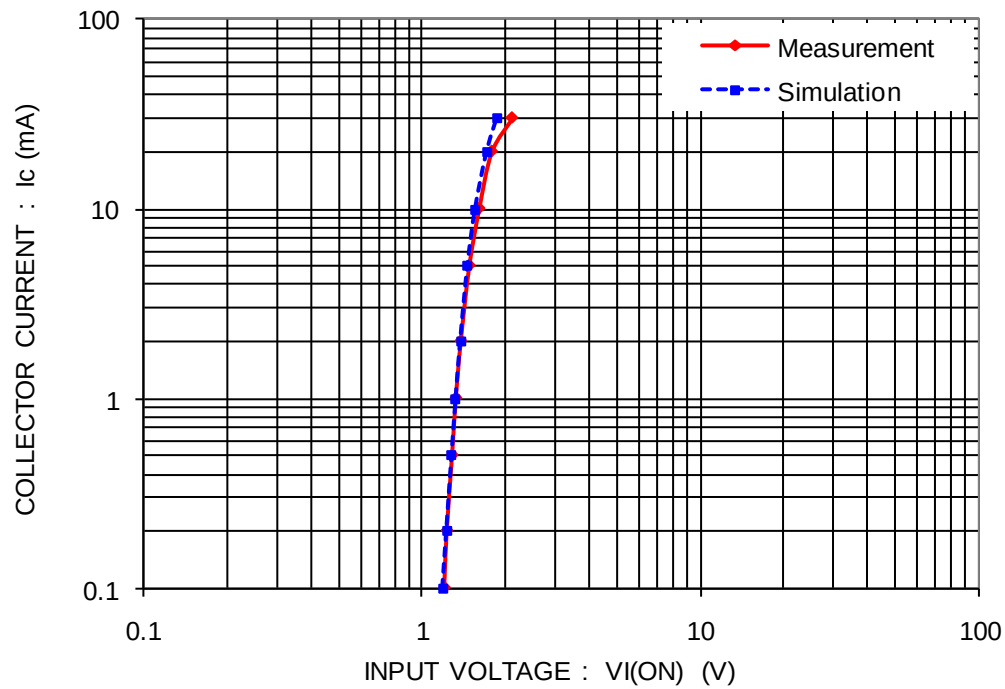


Evaluation circuit



Comparison Graph

Circuit Simulation Result



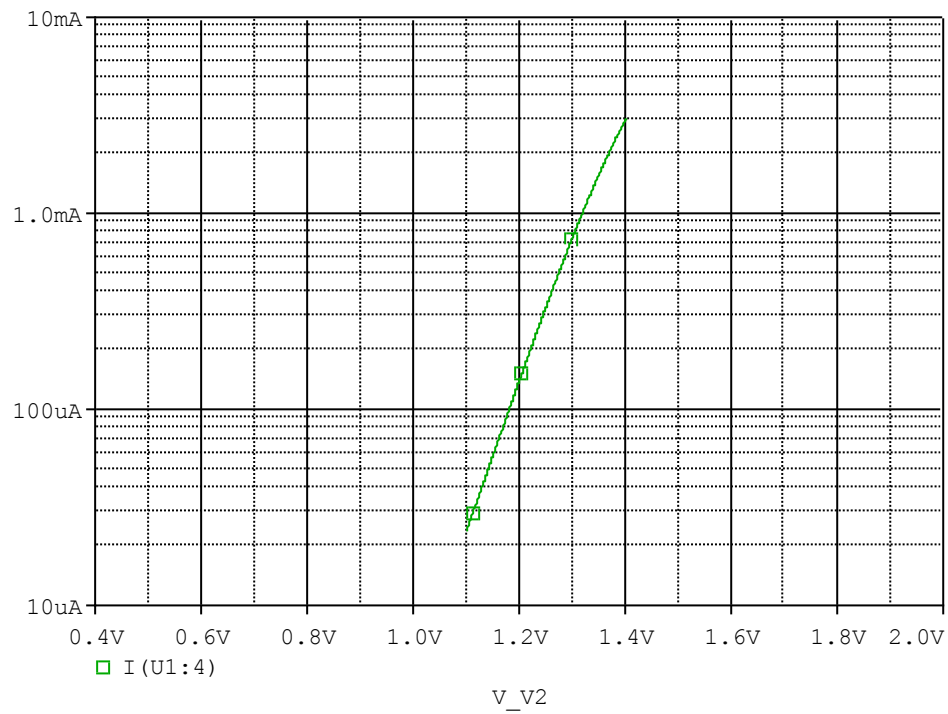
Simulation Result

Condition : $V_{CE} = 0.2 \text{ V}$

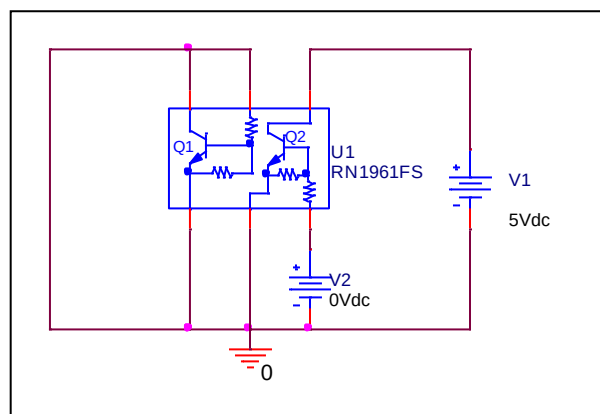
$I_c(\text{mA})$	$V_{i(ON)} \text{ (V)}$		Error (%)
	Measurement	Simulation	
0.1	1.200	1.184	-1.33
0.2	1.225	1.224	-0.11
0.5	1.280	1.278	-0.14
1	1.320	1.322	0.18
2	1.375	1.372	-0.20
5	1.475	1.457	-1.23
10	1.600	1.552	-3.02
20	1.775	1.706	-3.90

I_C - $V_{I(OFF)}$ Characteristics

Circuit simulation result

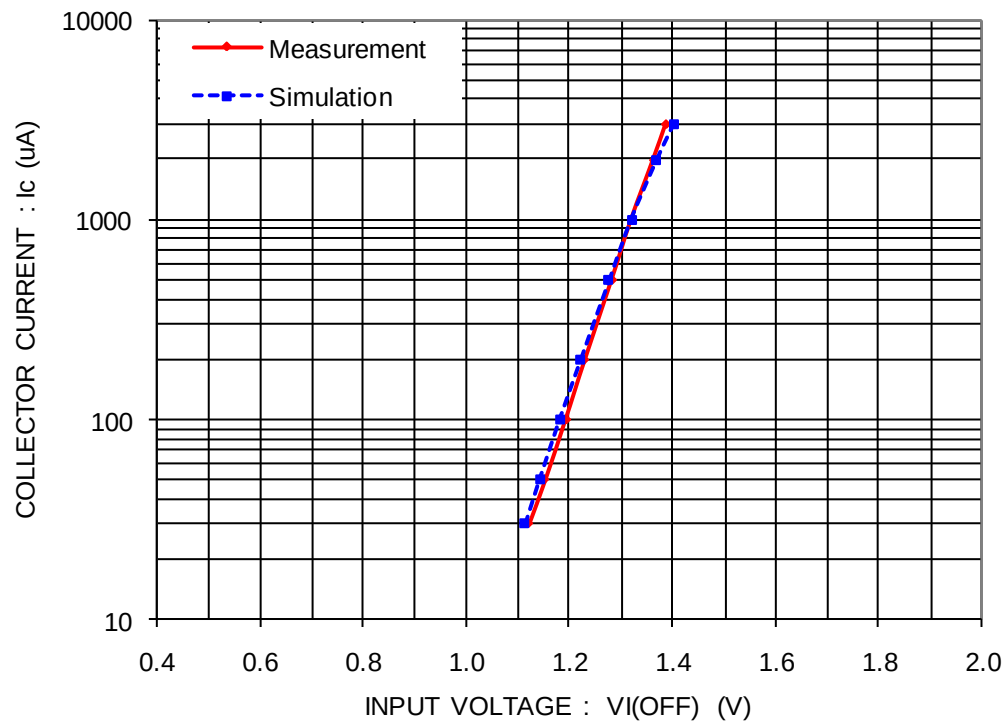


Evaluation circuit



Comparison Graph

Circuit Simulation Result



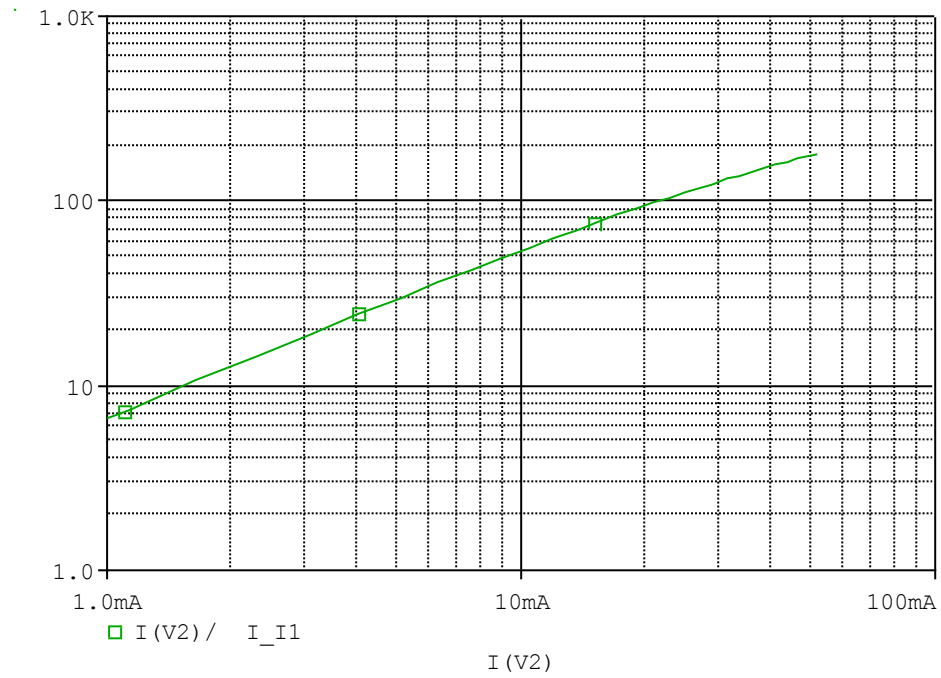
Simulation Result

Condition : $V_{CC} = 5\text{ V}$

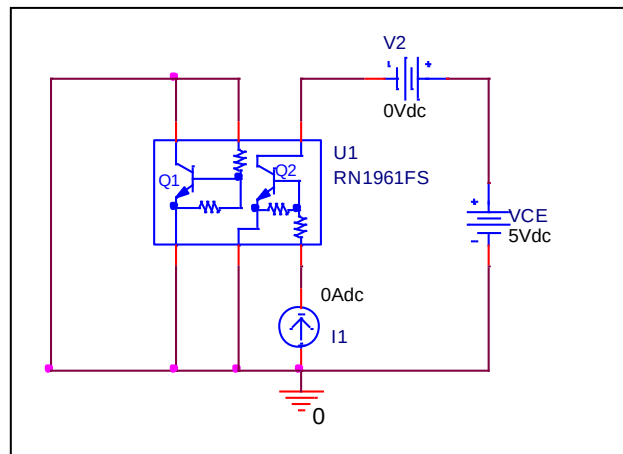
$I_c(\mu A)$	$V_{I(OFF)}\text{ (V)}$		Error (%)
	Measurement	Simulation	
30	1.120	1.113	-0.59
50	1.152	1.142	-0.86
100	1.192	1.181	-0.90
200	1.229	1.221	-0.65
500	1.282	1.275	-0.54
1000	1.319	1.319	0.00
2000	1.362	1.368	0.46
3000	1.385	1.401	1.18

h_{FE} - I_C Characteristics

Circuit simulation result

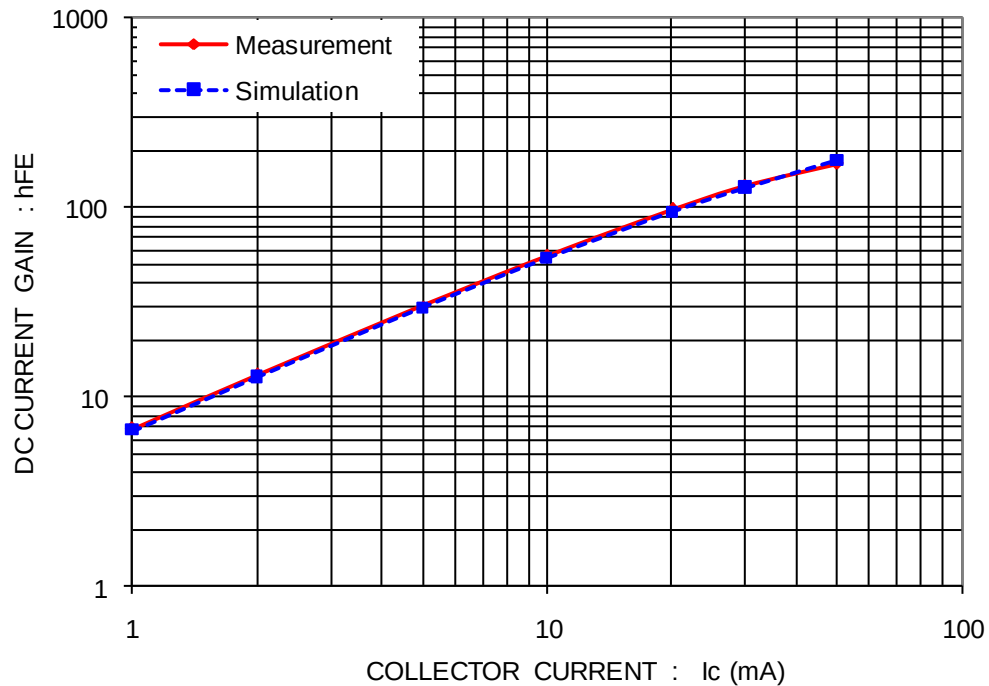


Evaluation circuit



Comparison Graph

Circuit Simulation Result



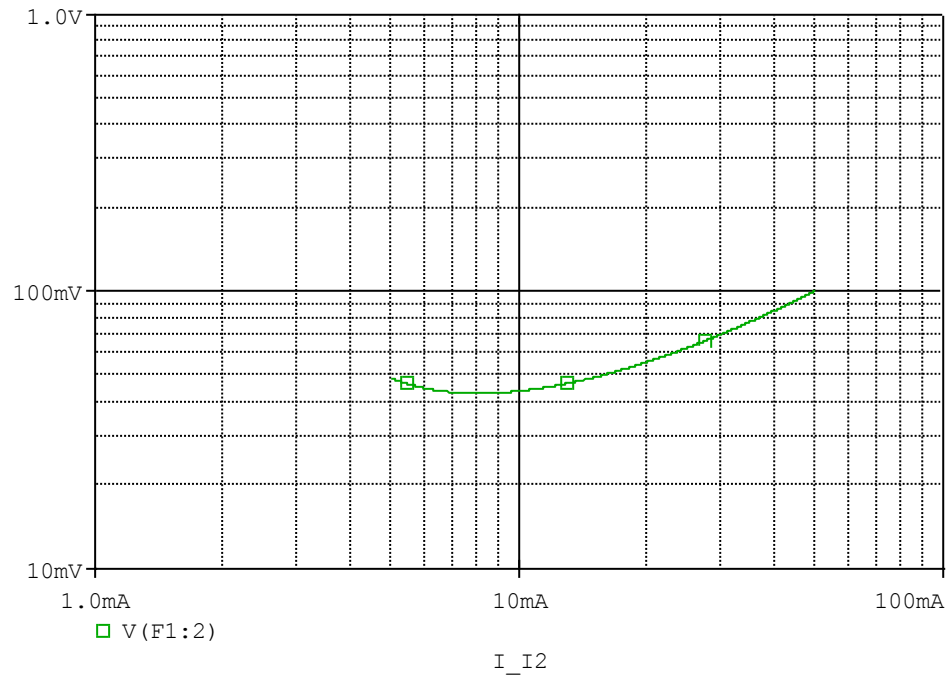
Simulation Result

Condition : $V_{CC} = 5\text{ V}$

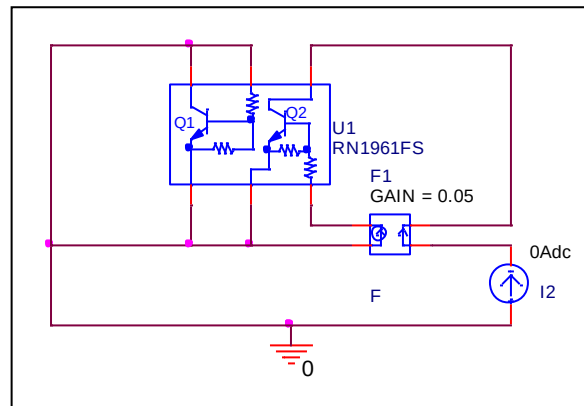
$I_c(\text{mA})$	h_{FE}		Error (%)
	Measurement	Simulation	
1	6.750	6.645	-1.56
2	13.000	12.726	-2.11
5	30.000	29.496	-1.68
10	56.000	54.034	-3.51
20	97.000	94.491	-2.59
30	131.000	126.884	-3.14
50	170.000	175.946	3.50

$V_{CE(Sat)}$ - I_C Characteristics

Circuit simulation result

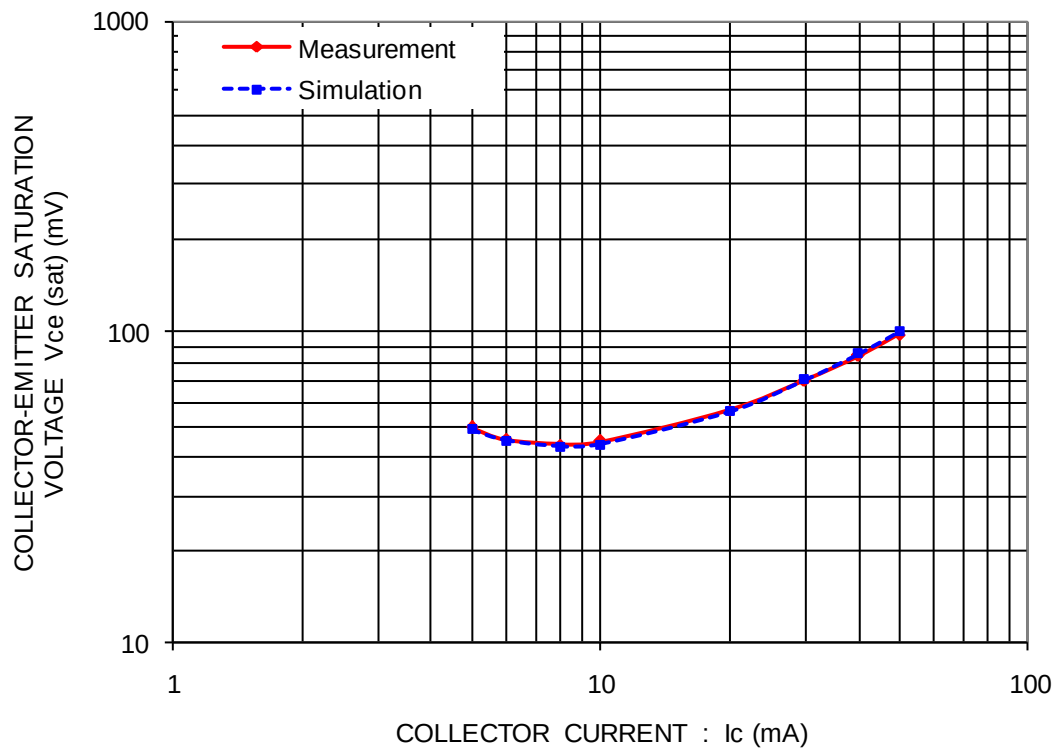


Evaluation circuit



Comparison Graph

Circuit Simulation Result



Simulation Result

Condition : $I_C/I_B=20$

Ic(mA)	VCE(sat) (mV)		Error (%)
	Measurement	Simulation	
5	49.500	48.820	-1.37
6	45.400	44.844	-1.22
8	43.800	43.012	-1.80
10	44.600	43.821	-1.75
20	56.000	55.758	-0.43
30	70.000	70.353	0.50
40	84.000	85.486	1.77
50	98.500	100.818	2.35