

Device Modeling Report

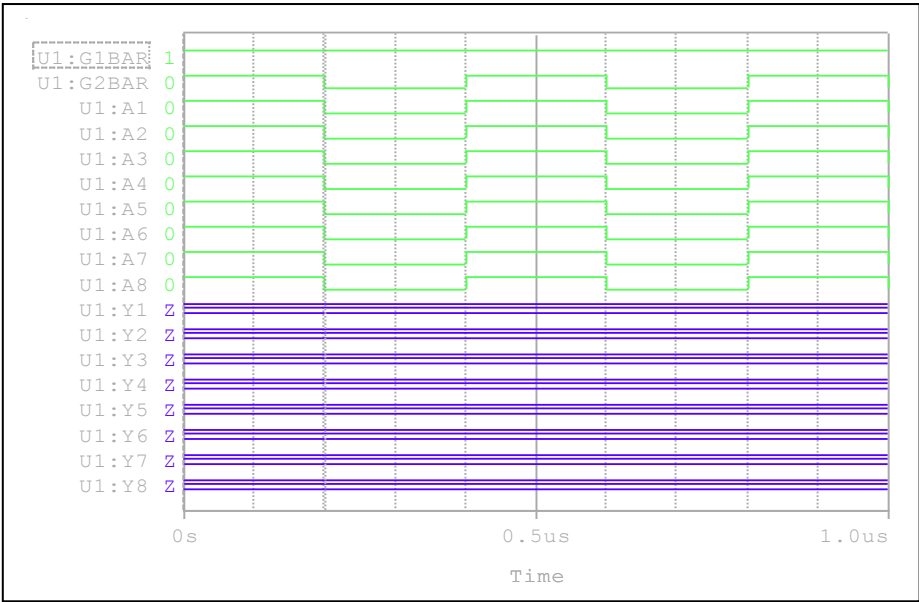
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT
PART NUMBER : TC74AC540FT
MANUFACTURER : TOSHIBA



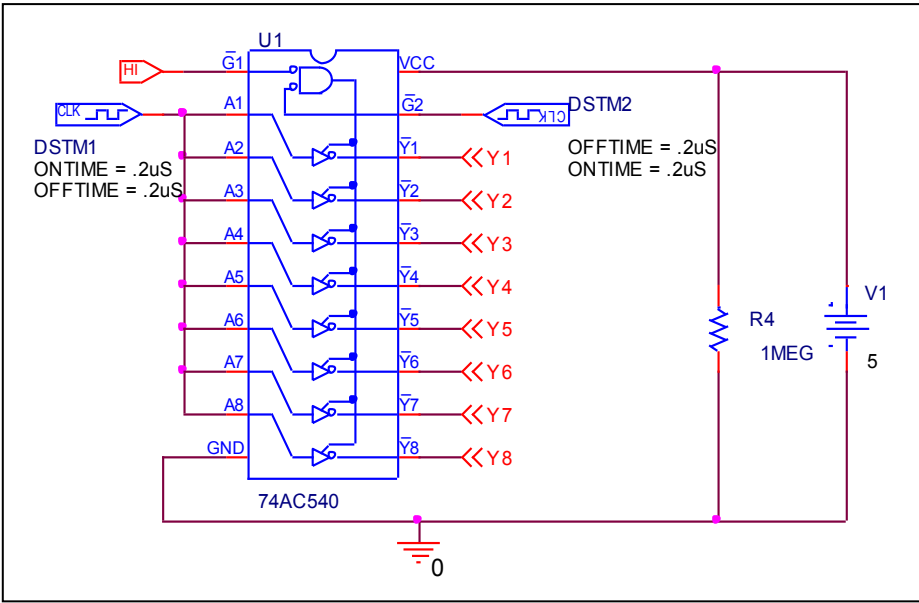
Bee Technologies Inc.

Truth Table

Circuit simulation result



Evaluation circuit

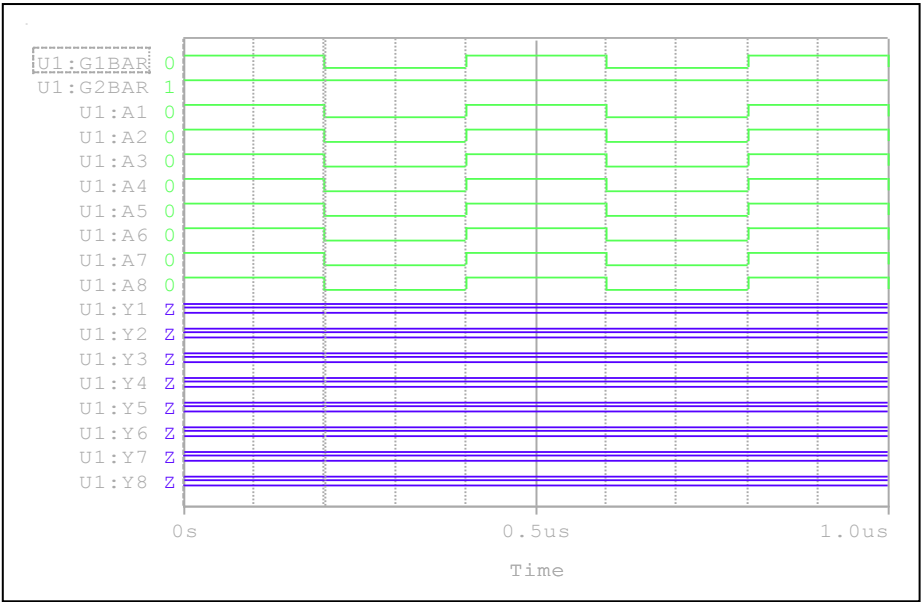


Comparison table

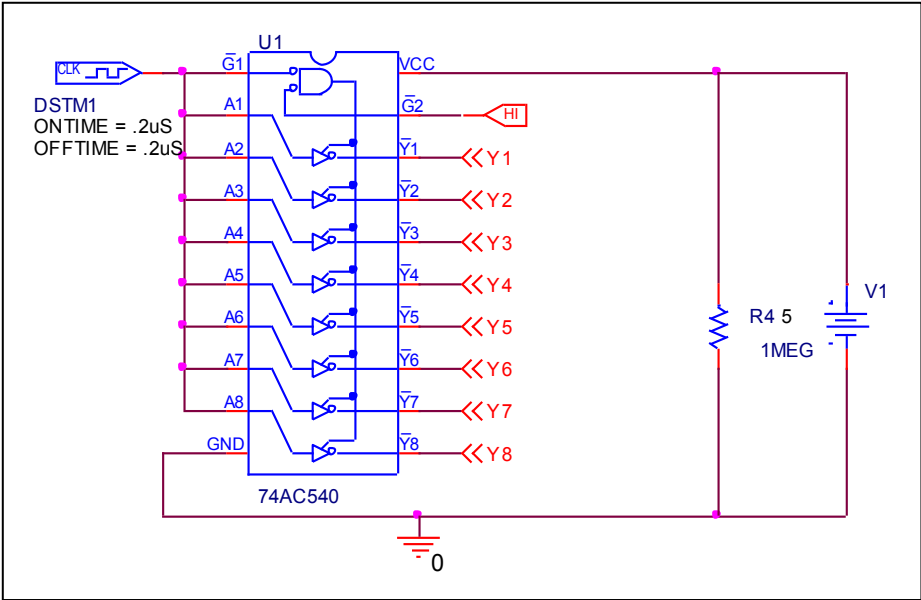
Input			Output		%Error
$\bar{G}1$	$\bar{G}2$	A_n	$\bar{Y}_n$ (Measurement)	$\bar{Y}_n$ (Simulation)	
H	X	X	Z	Z	0

Truth Table

Circuit simulation result



Evaluation circuit

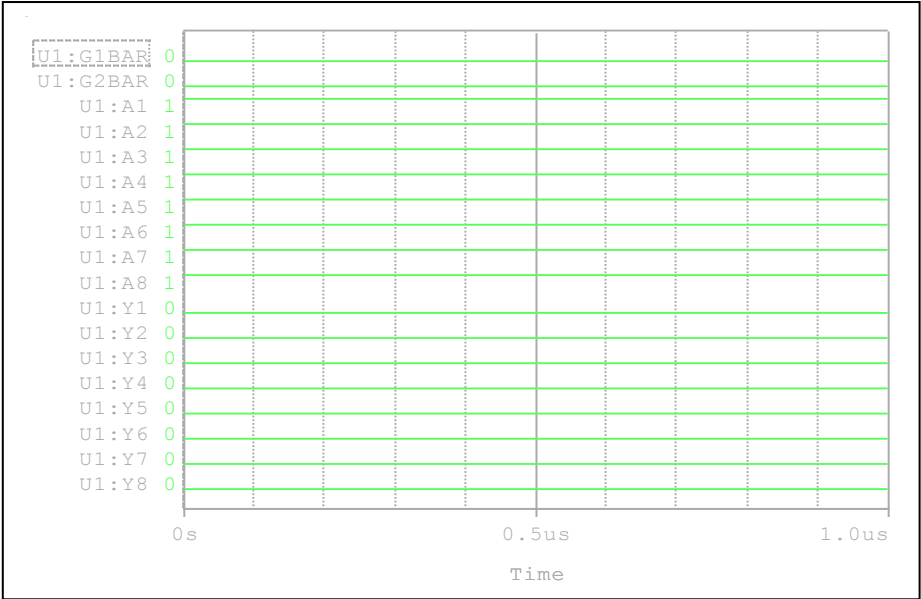


Comparison table

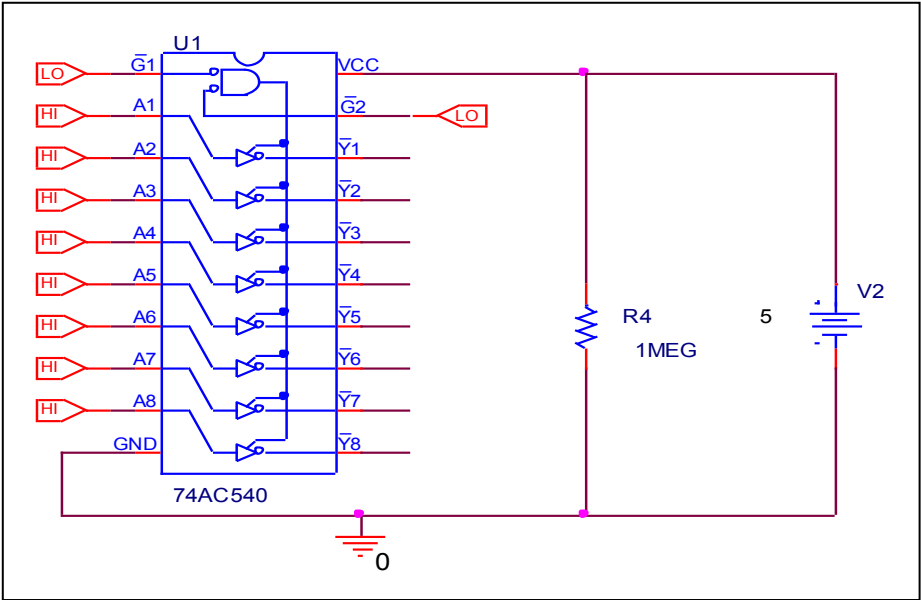
Input			Output		%Error
G1	G2	An	Yn (Measurement)	Yn (Simulation)	
X	H	X	Z	Z	0

Truth Table

Circuit simulation result



Evaluation circuit

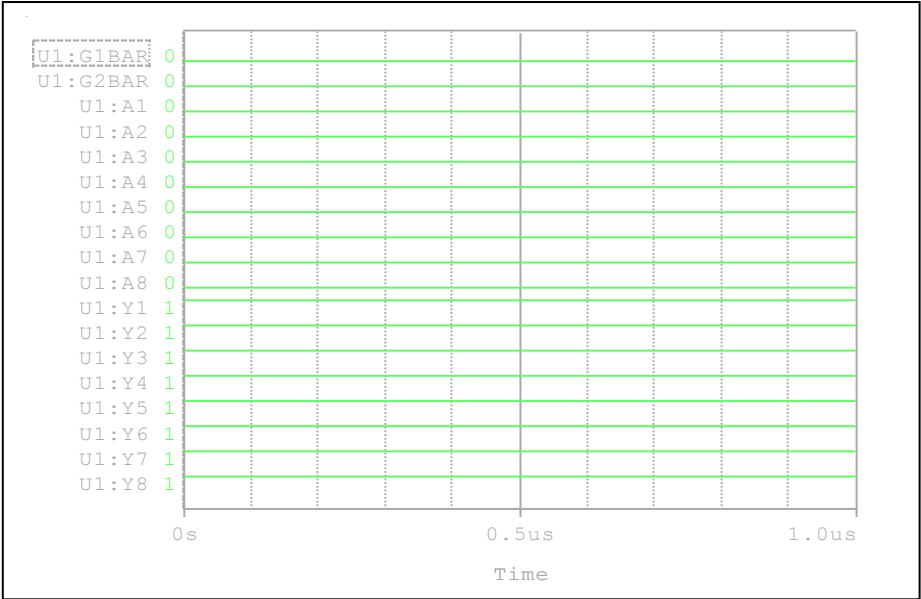


Comparison table

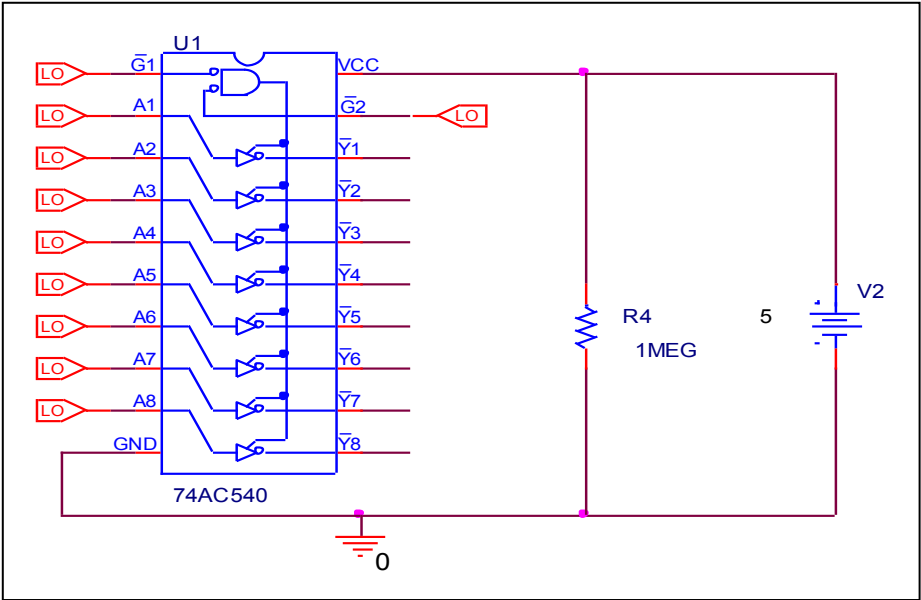
Input			Output		%Error
G1	G2	An	Yn (Measurement)	Yn (Simulation)	
L	L	H	L	L	0

Truth Table

Circuit simulation result



Evaluation circuit

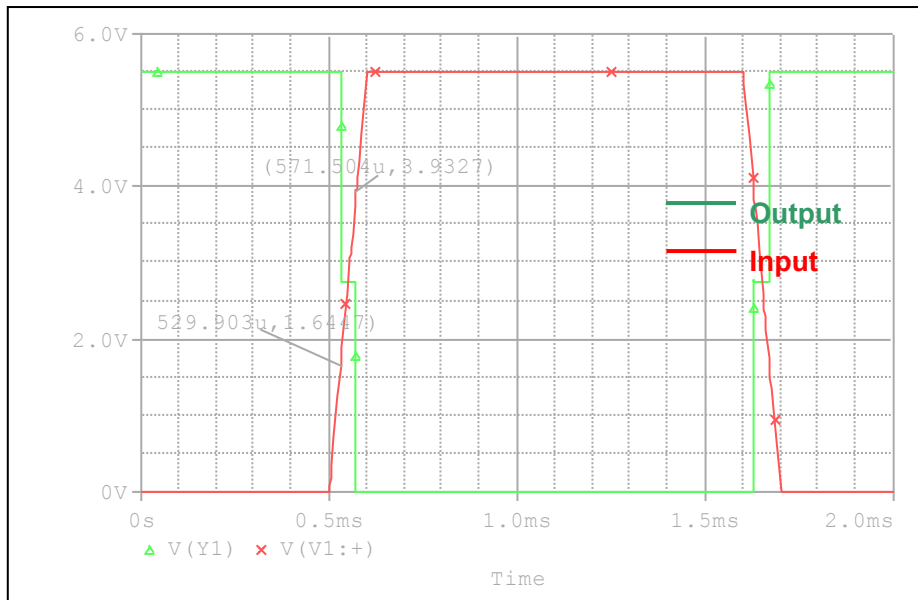


Comparison table

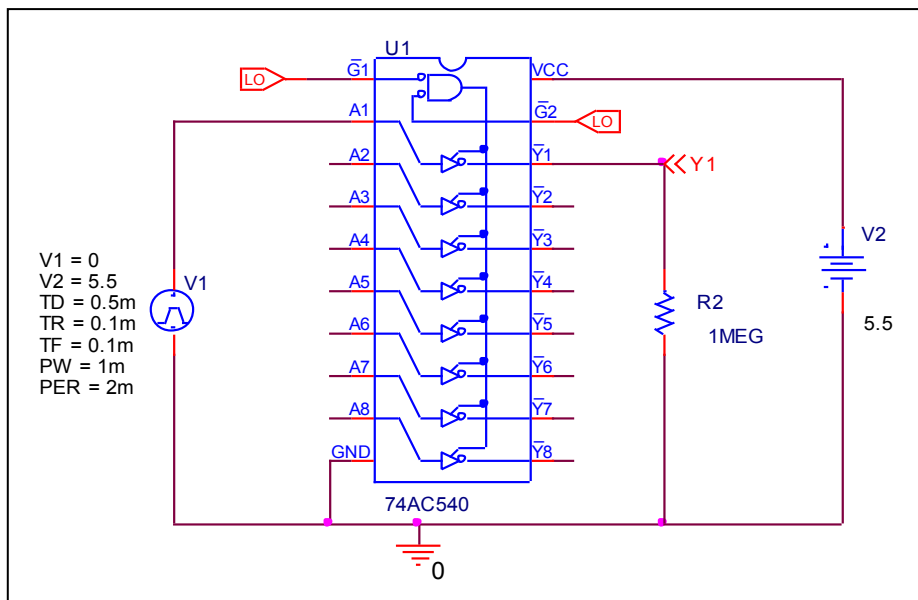
Input			Output		%Error
$\overline{G1}$	$\overline{G2}$	An	$\overline{Yn}$ (Measurement)	$\overline{Yn}$ (Simulation)	
L	L	L	H	H	0

High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

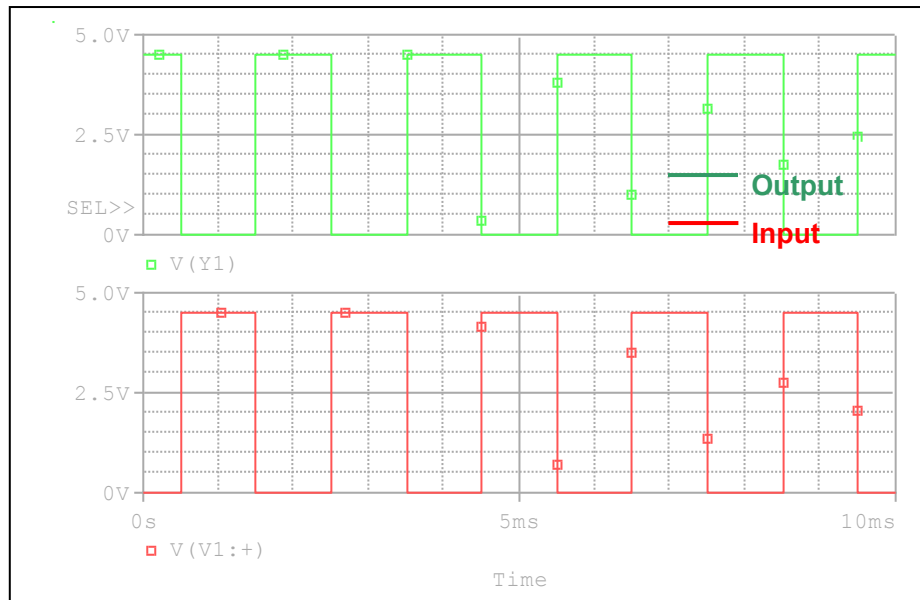


Comparison table

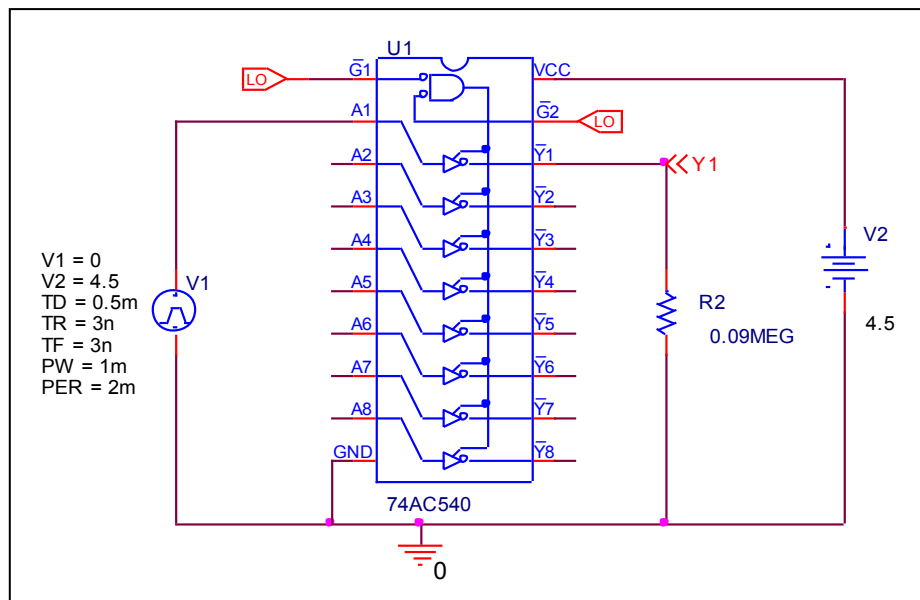
$V_{CC} = 5.5V$	Measurement	Simulation	%Error
$V_{IH} (V)$	3.85	3.9327	2.148
$V_{IL} (V)$	1.65	1.6447	-0.321

High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

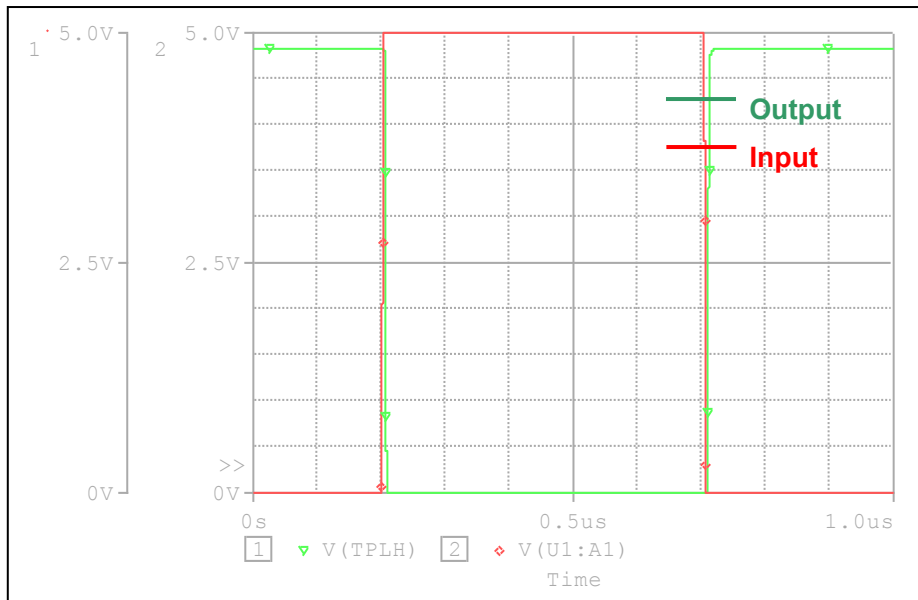


Comparison table

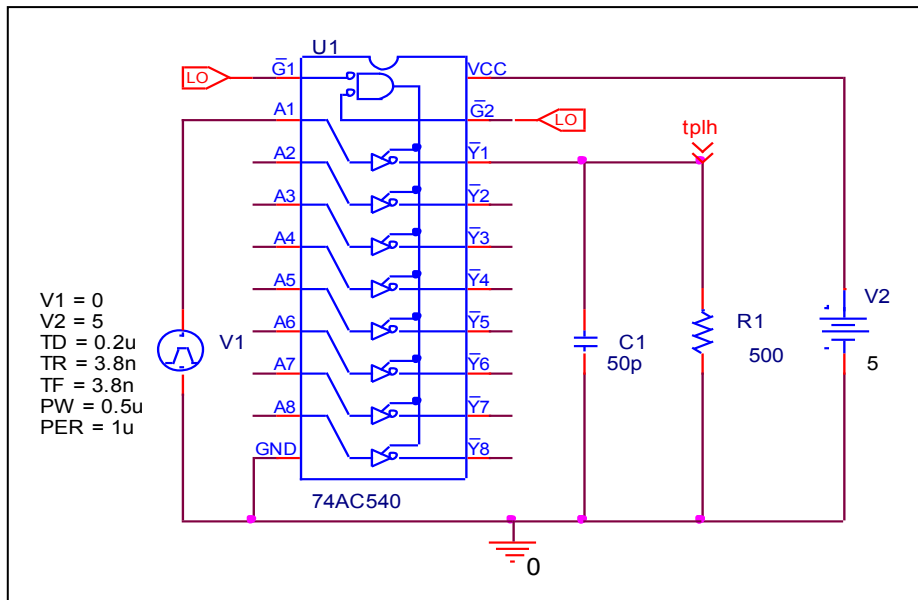
$V_{CC} = 4.5V$	Measurement	Simulation	%Error
$V_{OH} (V)$	4.5	4.499	-0.022
$V_{OL} (V)$	0	0	0

Propagation Delay Time

Circuit simulation result



Evaluation circuit

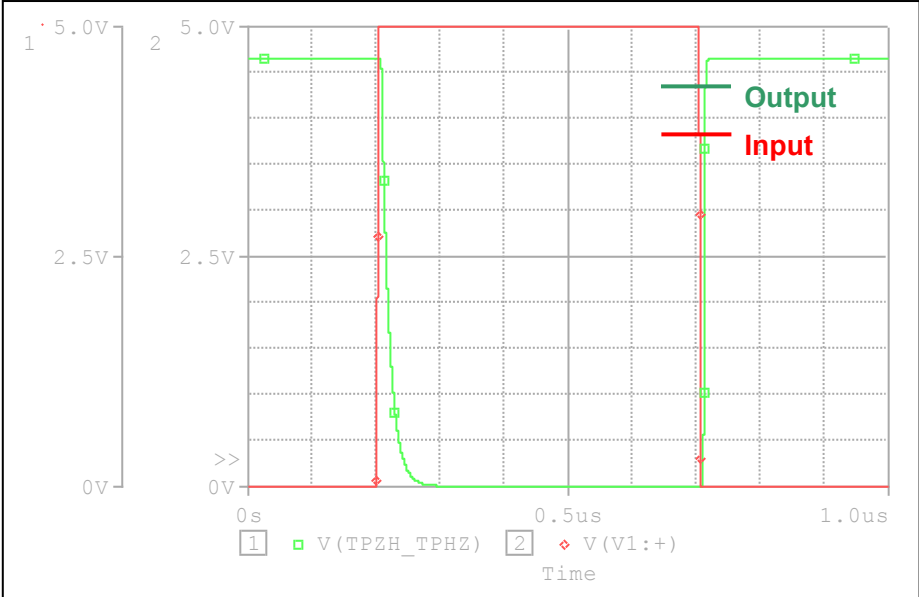


Comparison table $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$

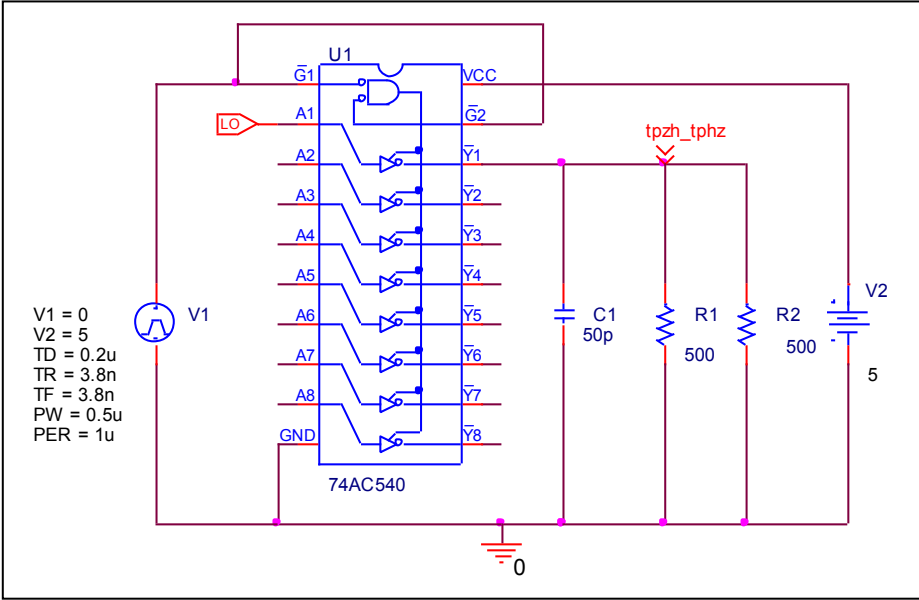
$V_{CC} = 5 \text{ V}$, $t_r = t_f = 3 \text{ ns}$	Measurement	Simulation	%Error
$t_{PLH} \text{ (ns)}$	4.7	4.7224	0.477
$t_{PHL} \text{ (ns)}$	4.7	4.7304	0.647

Output enable time, high impedance (off) to high output (t_{PZH})
Output disable time, high to high impedance (off) output (t_{PHZ})

Circuit simulation result



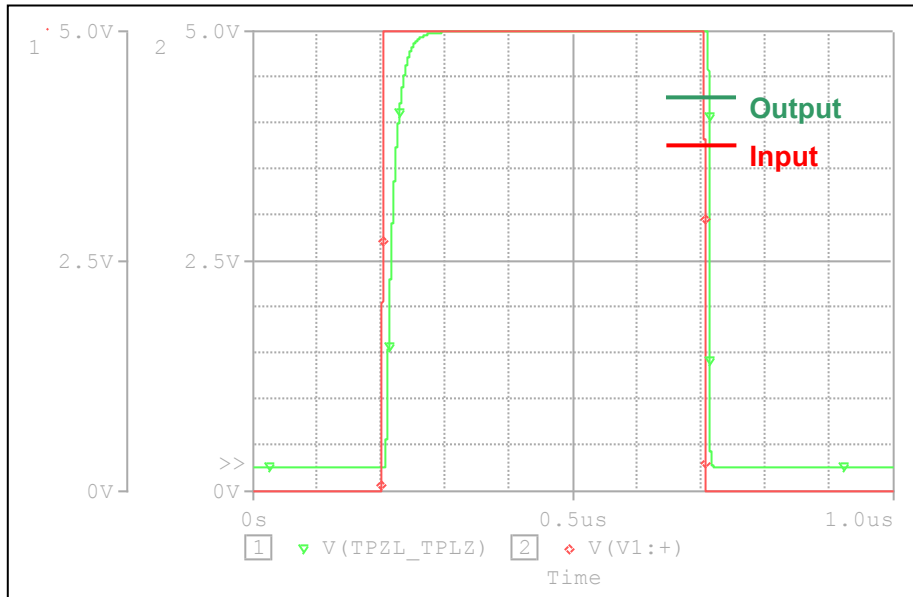
Evaluation circuit



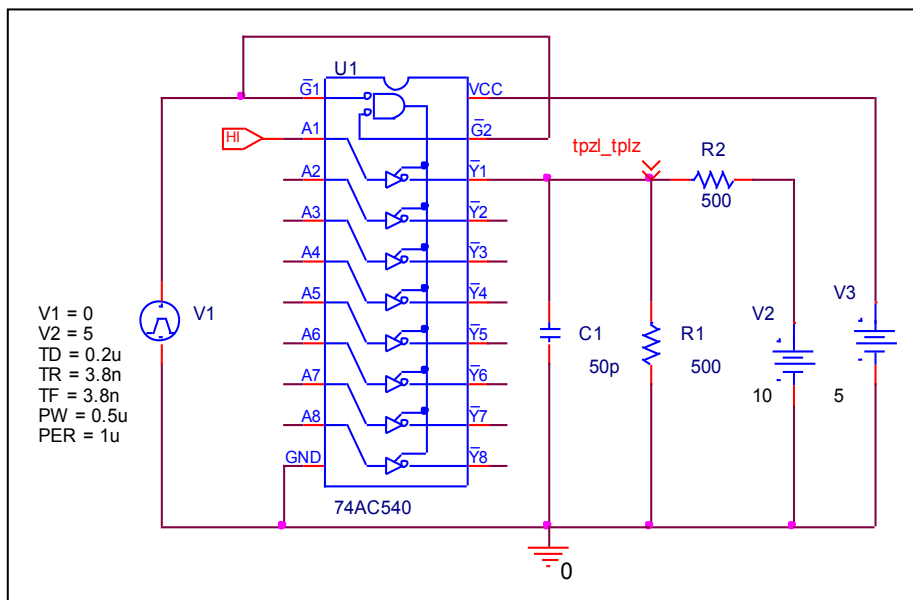
Comparison table $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$

$V_{CC} = 5 \text{ V}$, $t_r = t_f = 3 \text{ ns}$	Measurement	Simulation	%Error
$t_{PZH} \text{ (ns)}$	6.4	6.4434	0.678
$t_{PHZ} \text{ (ns)}$	6.4	6.4843	1.317

Circuit simulation result



Evaluation circuit



Comparison table $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$

V_{CC} = 5 V, tr = tf = 3 ns	Measurement	Simulation	%Error
t_{PZL} (ns)	6.4	6.4819	1.280
t_{PLZ} (ns)	6.4	6.4248	0.387