

Device Modeling Report

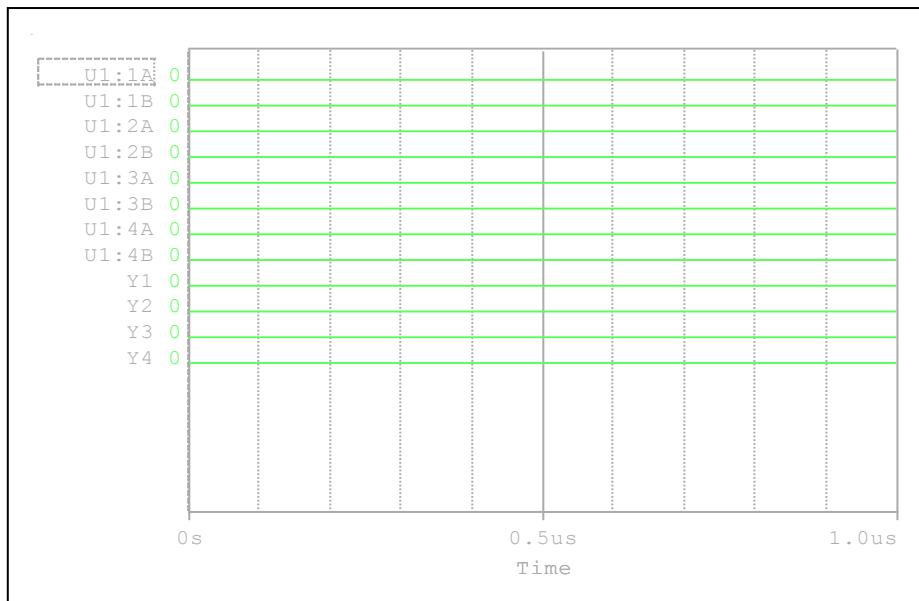
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT
PART NUMBER : TC74HCT08AF
MANUFACTURER : TOSHIBA



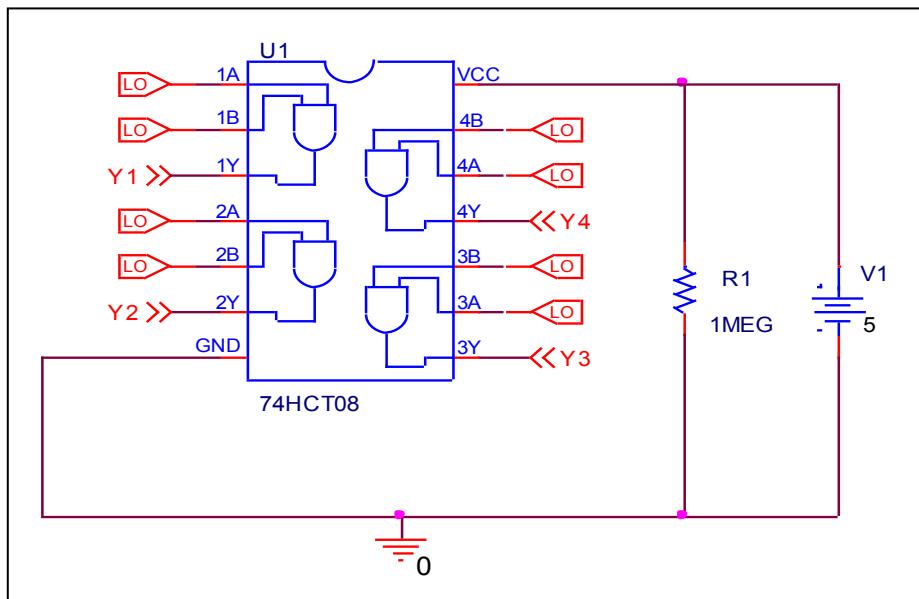
Bee Technologies Inc.

Truth Table

Circuit simulation result



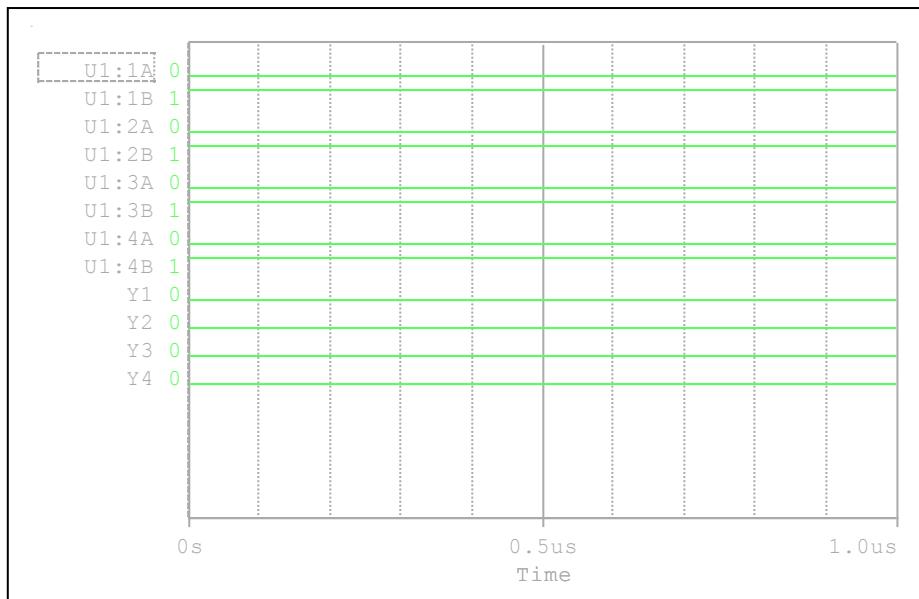
Evaluation circuit



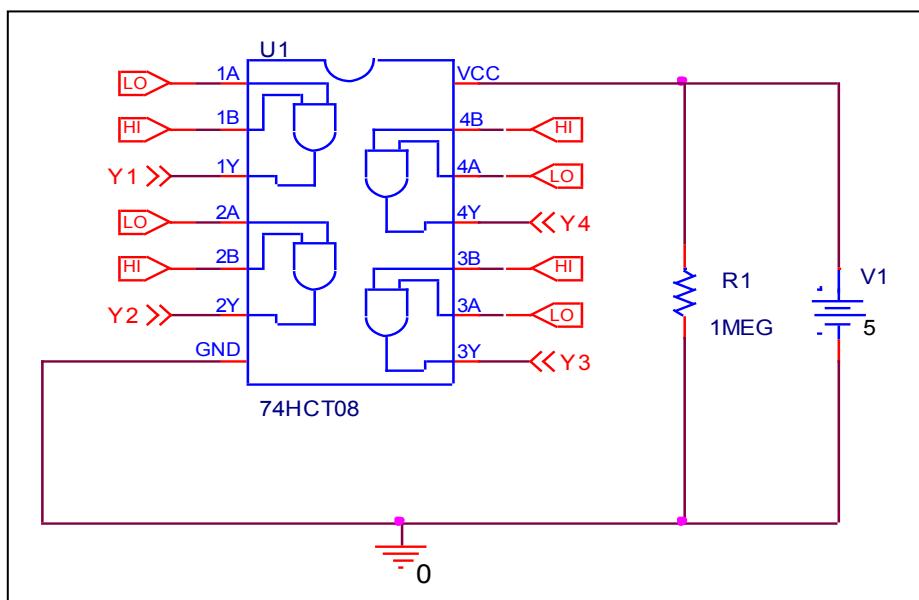
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
L	L	L	L	0

Truth Table

Circuit simulation result



Evaluation circuit

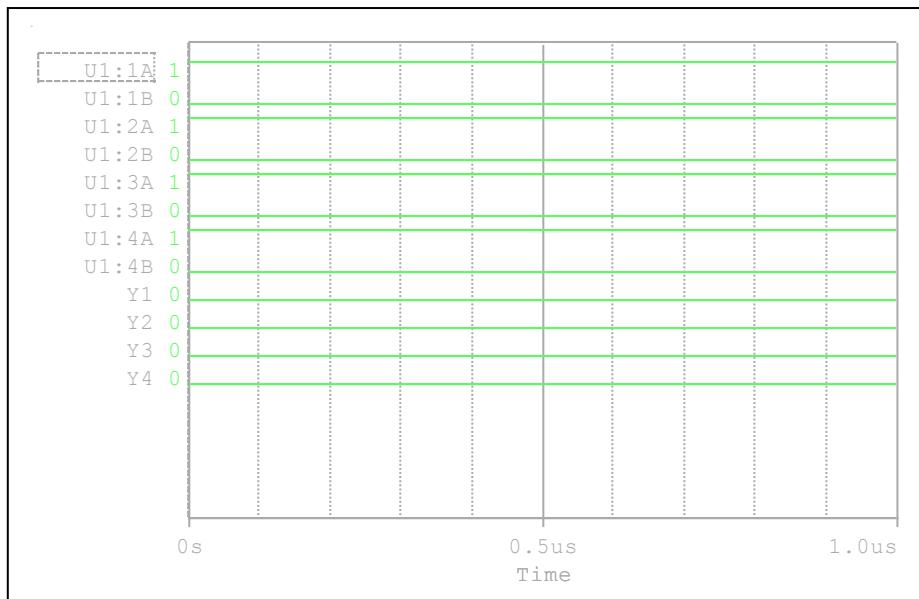


Comparison table

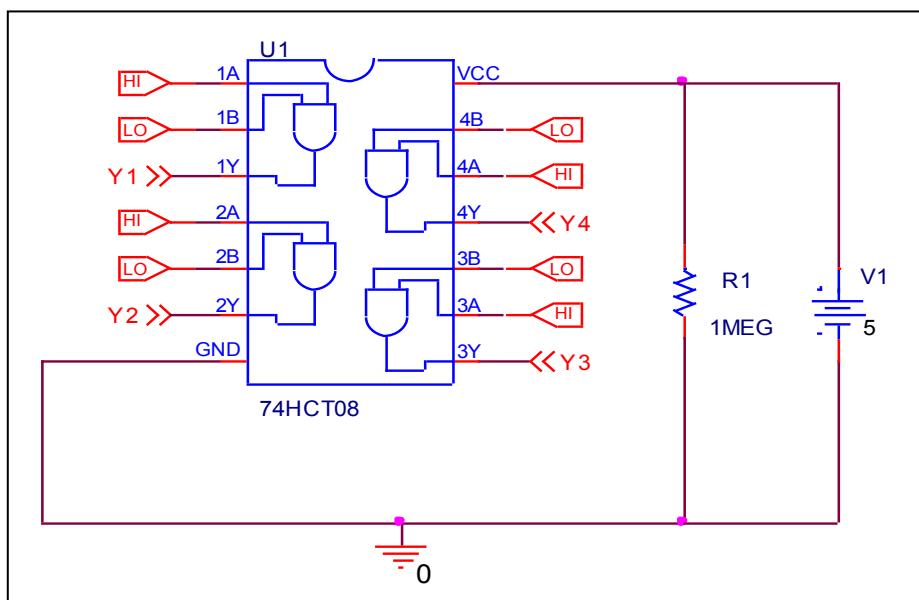
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
L	H	L	L	0

Truth Table

Circuit simulation result



Evaluation circuit

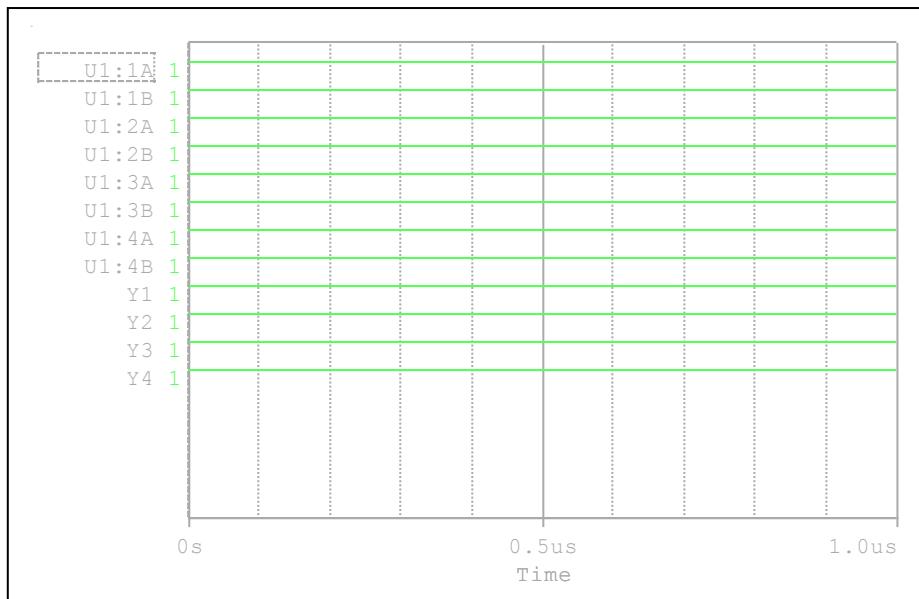


Comparison table

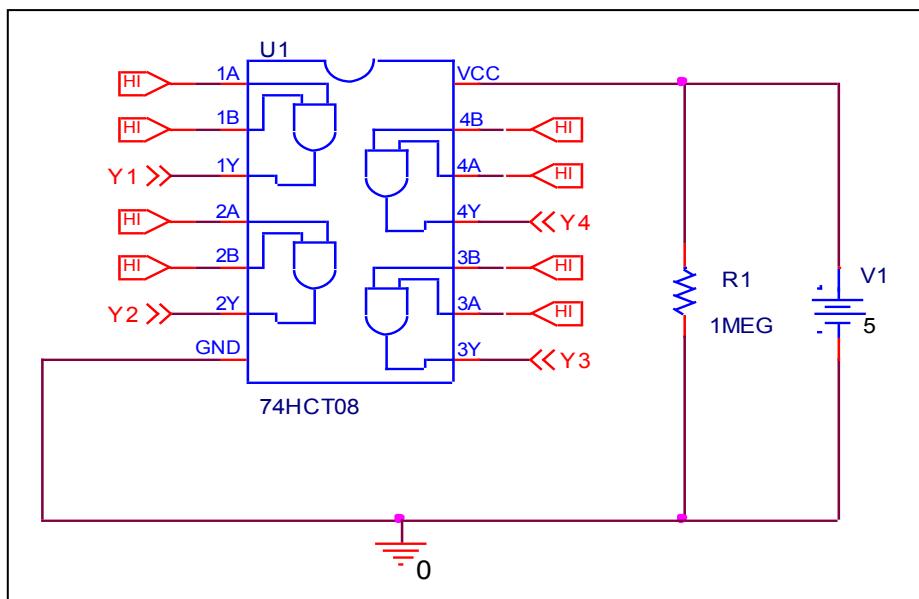
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
H	L	L	L	0

Truth Table

Circuit simulation result



Evaluation circuit

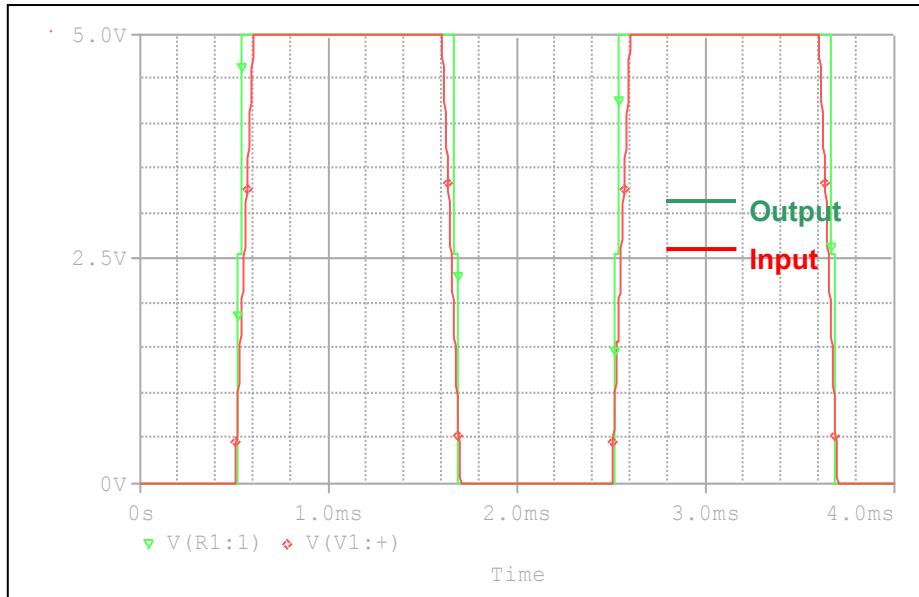


Comparison table

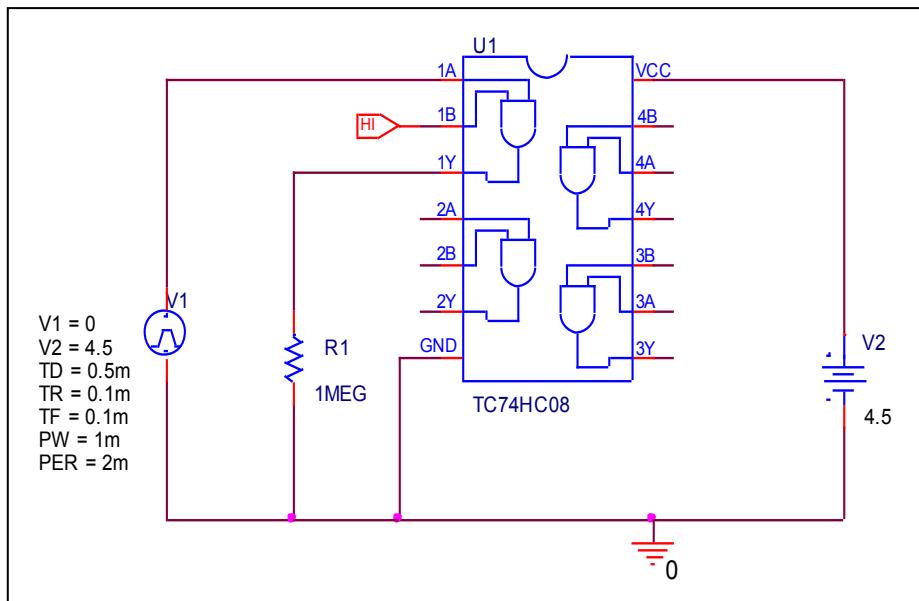
Input		Output		%Error
A _n	B _n	Y _n (Measurement)	Y _n (Simulation)	
H	H	H	H	0

High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

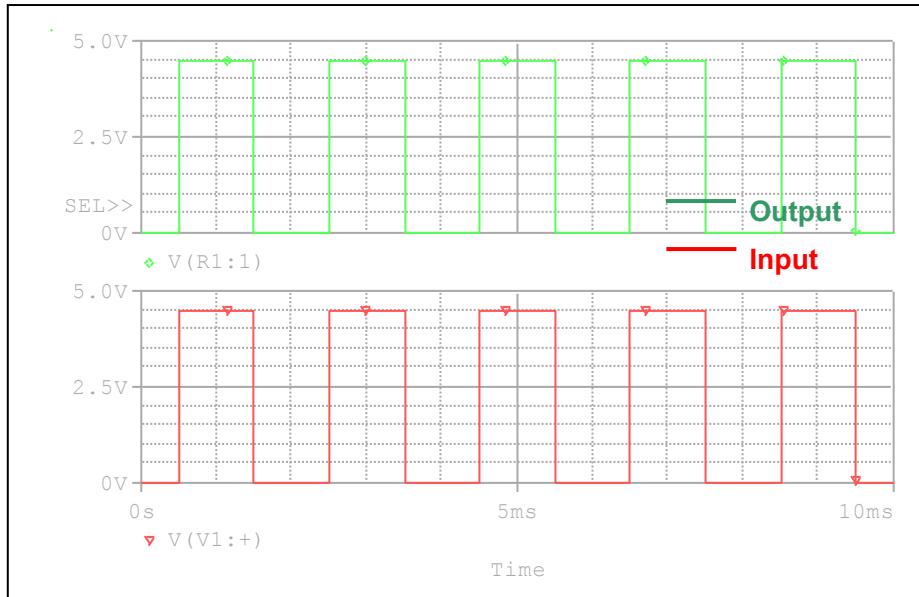


Comparison table

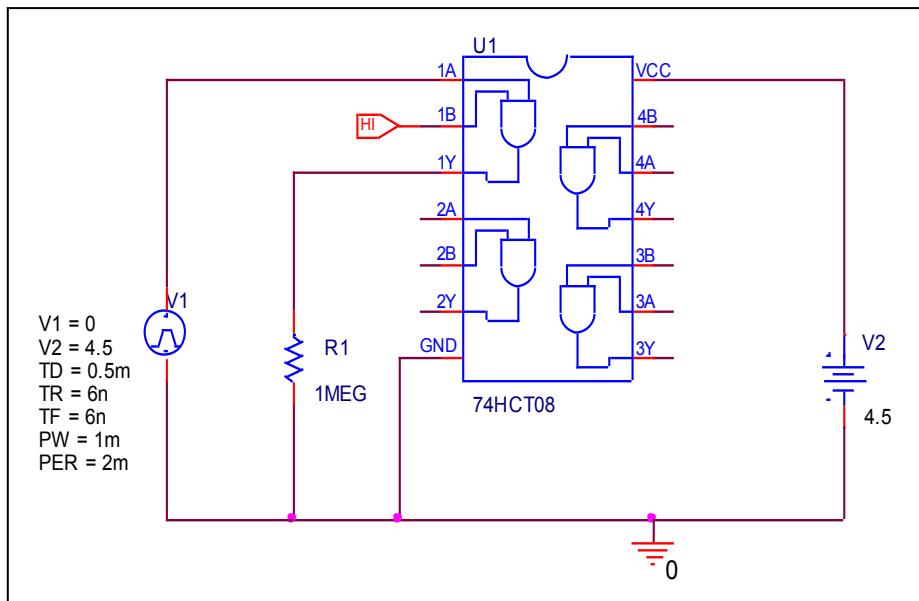
$V_{CC} = 5V$	Measurement	Simulation	%Error
$V_{IH} (V)$	2	2.001	0.050
$V_{IL} (V)$	0.8	0.799085	-0.114

High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

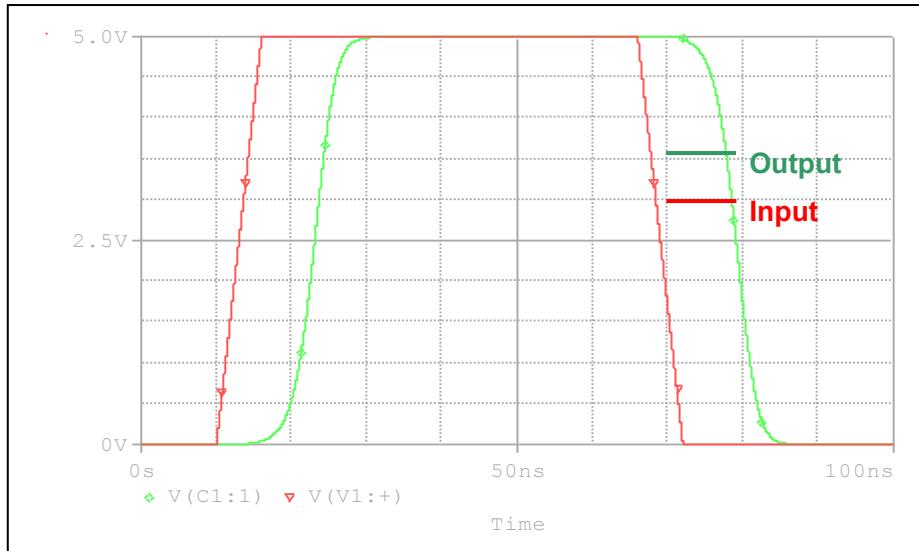


Comparison table

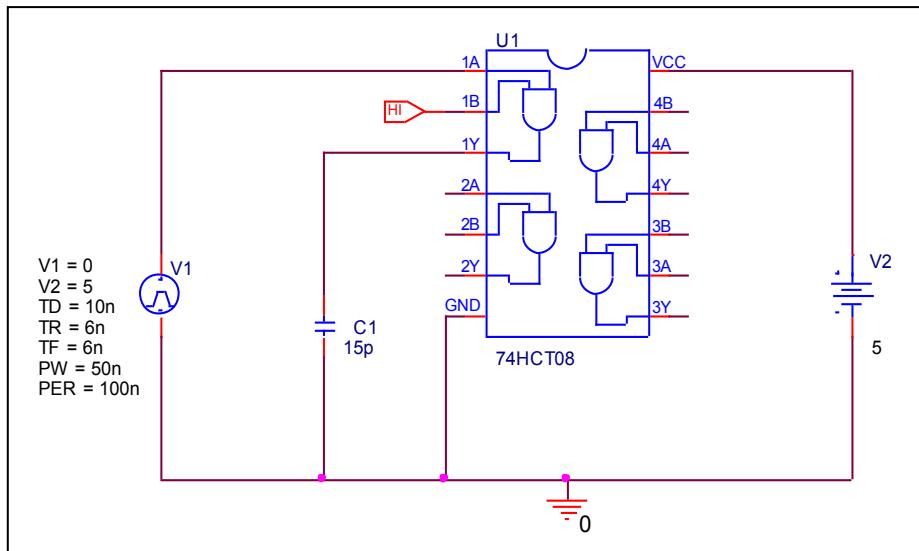
$V_{CC} = 4.5V$	Measurement	Simulation	%Error
$V_{OH}(V)$	4.5	4.4977	-0.051
$V_{OL}(V)$	0	0	0

Propagation Delay Time

Circuit simulation result



Evaluation circuit



Comparison table

$C_L = 15\text{pF}, V_{CC} = 5\text{V}, t_r = t_f = 6\text{ns}$	Measurement	Simulation	%Error
$t_{TLH} (\text{ns})$	6	6.02	0.333
$t_{THL} (\text{ns})$	6	6.0298	0.497
$T_{pLH} (\text{ns})$	10	10.069	0.690
$T_{pHL} (\text{ns})$	10	10.075	0.750