

Device Modeling Report

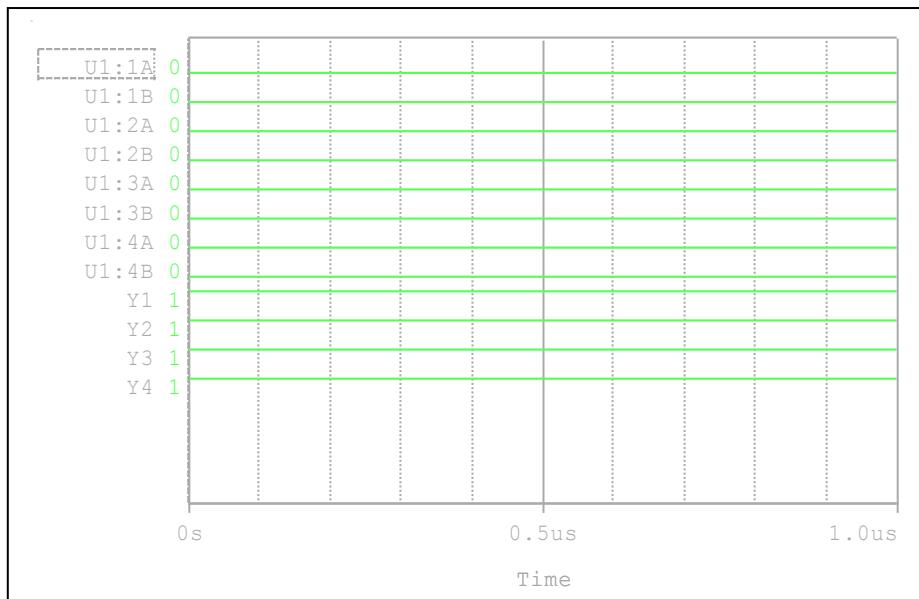
COMPONENTS : CMOS DIGITAL INTEGRATED CIRCUIT
PART NUMBER : TC74AC00P
MANUFACTURER : TOSHIBA



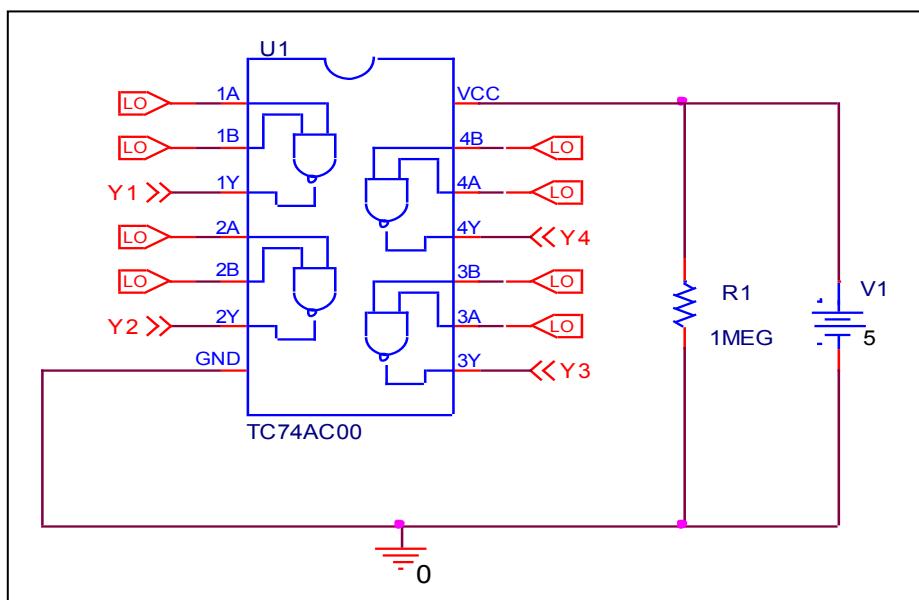
Bee Technologies Inc.

Truth Table

Circuit simulation result



Evaluation circuit

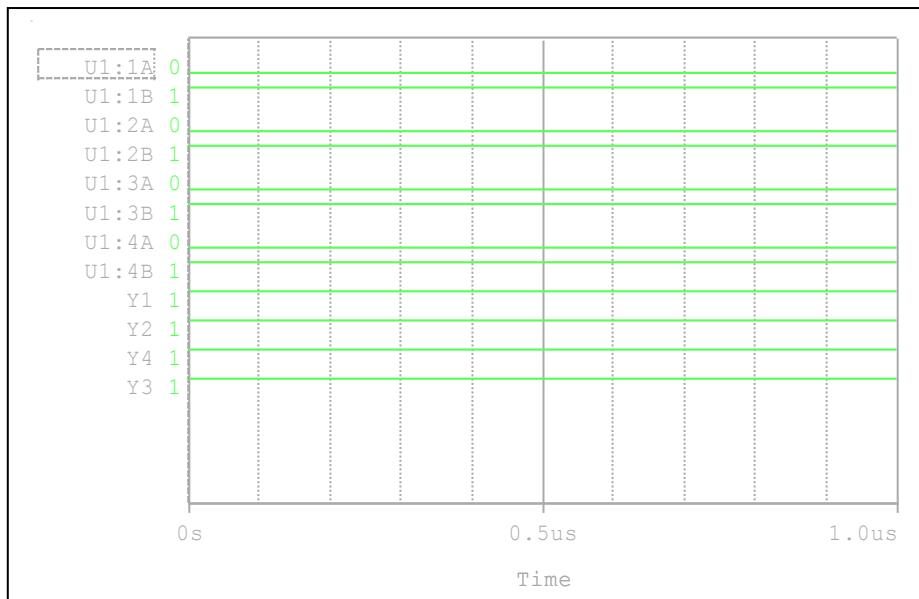


Comparison table

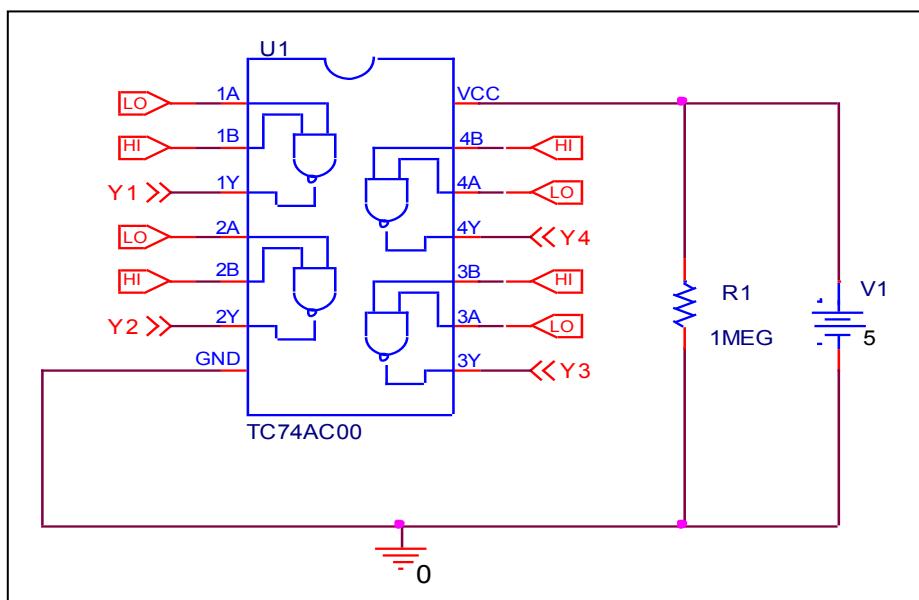
| Input | | Output | | %Error |
|----------------|----------------|------------------------------|-----------------------------|--------|
| A _n | B _n | Y _n (Measurement) | Y _n (Simulation) | |
| L | L | H | H | 0 |

Truth Table

Circuit simulation result



Evaluation circuit

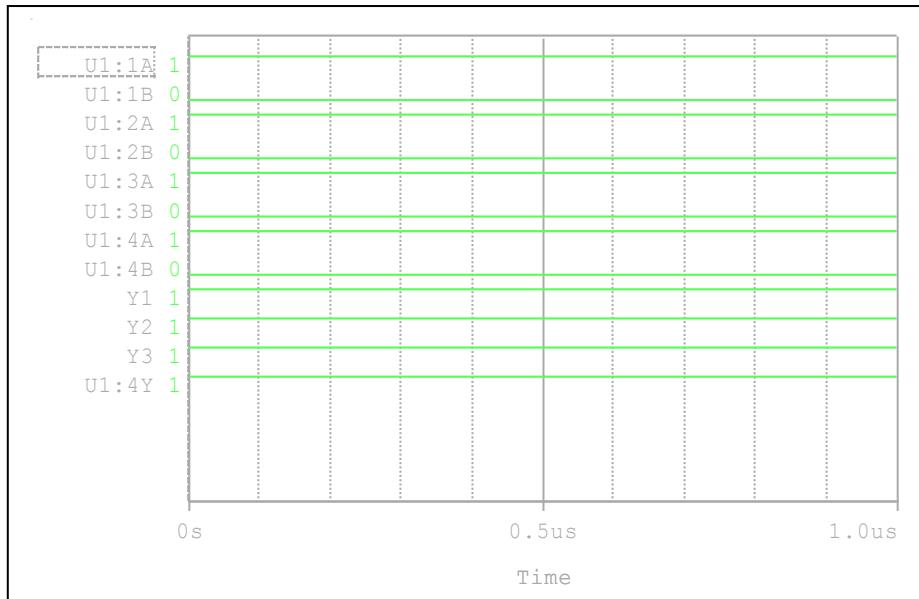


Comparison table

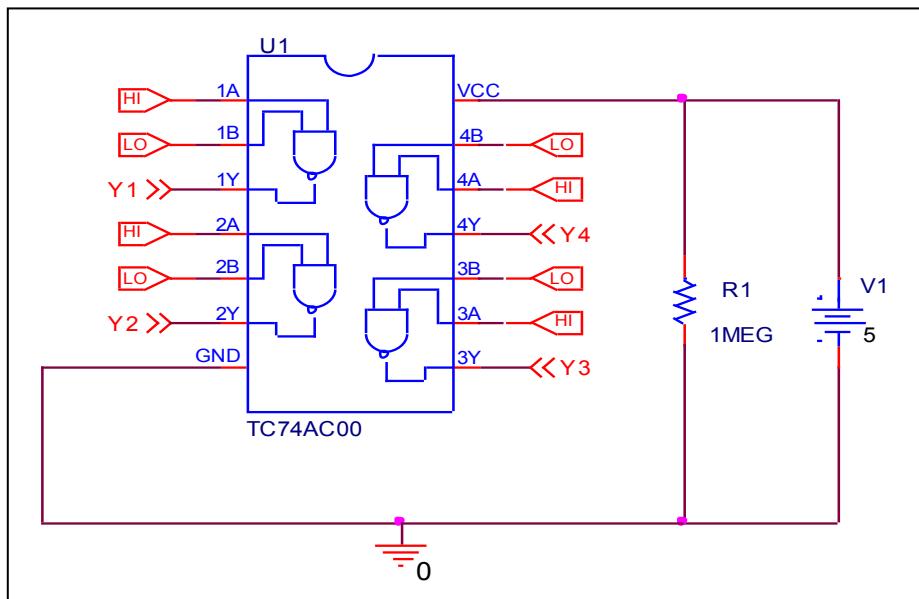
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Truth Table

Circuit simulation result



Evaluation circuit

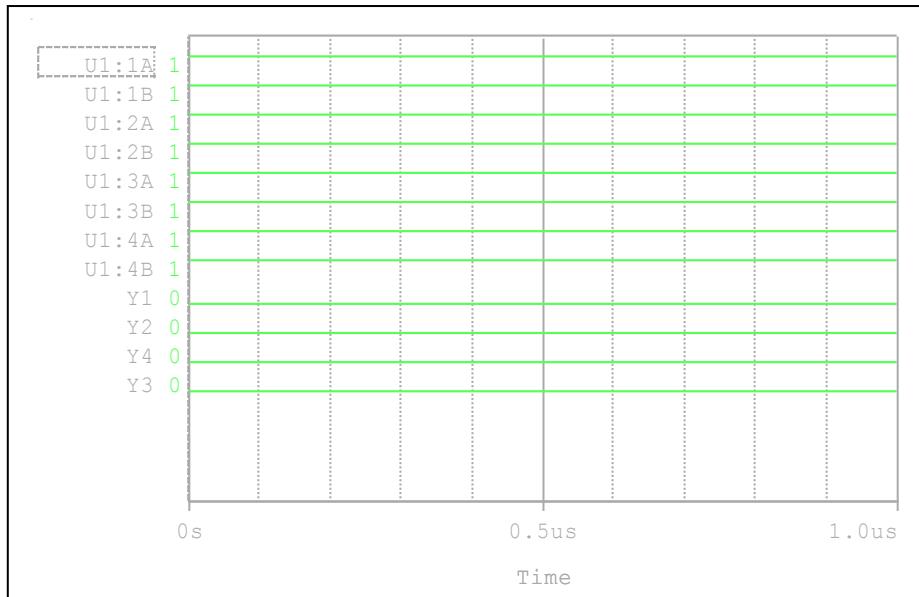


Comparison table

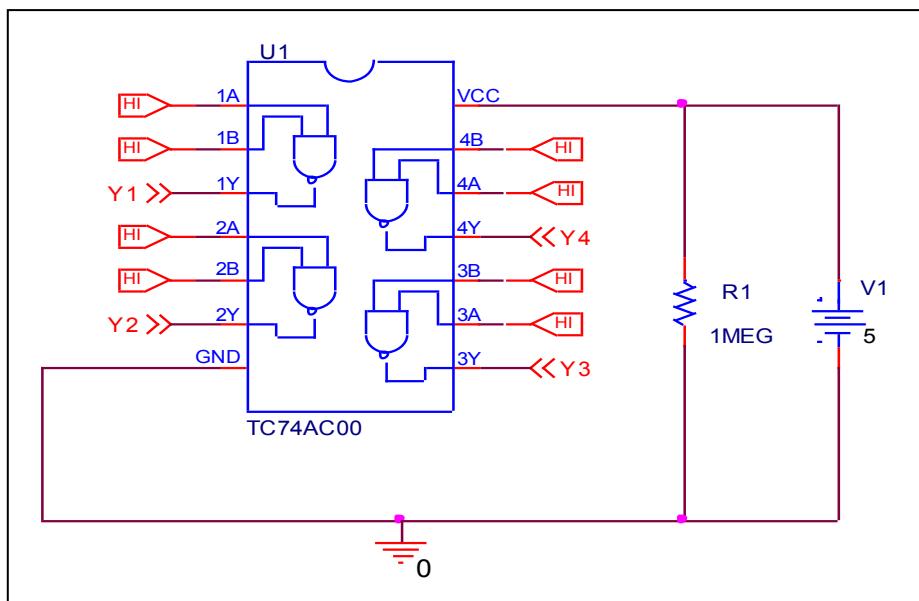
| Input | | Output | | %Error |
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Truth Table

Circuit simulation result



Evaluation circuit

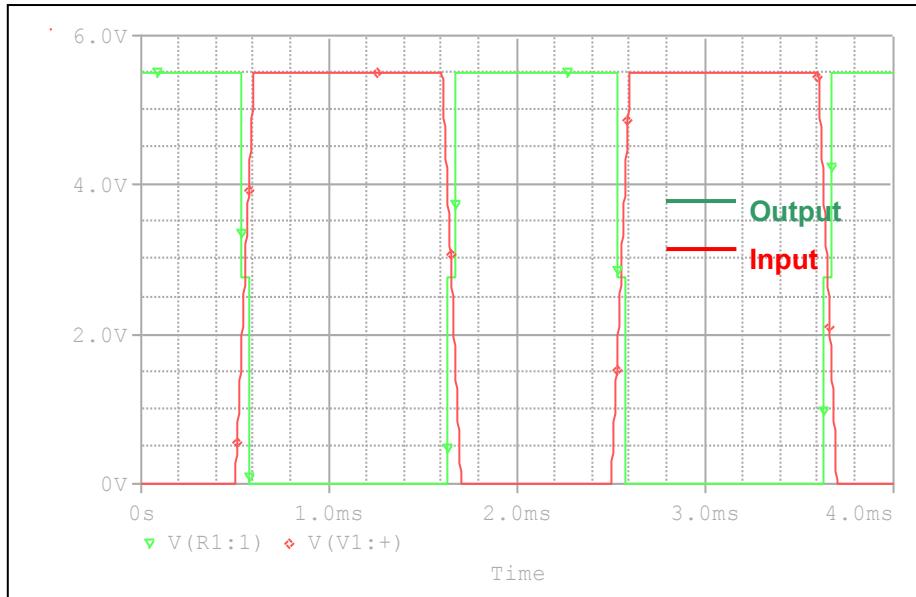


Comparison table

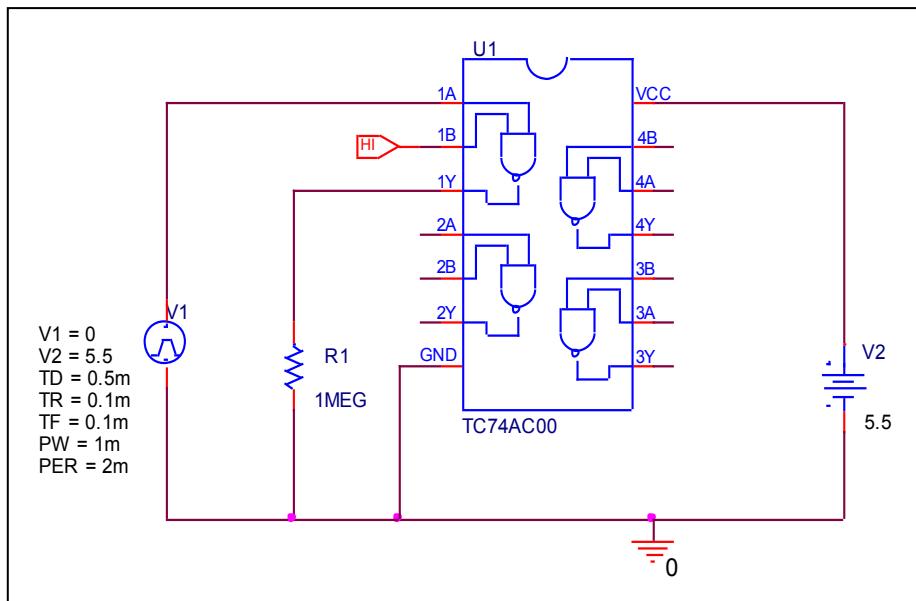
| Input | | Output | | %Error |
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| A _n | B _n | Y _n (Measurement) | Y _n (Simulation) | |
| H | H | L | L | 0 |

High Level and Low Level Input Voltage

Circuit simulation result



Evaluation circuit

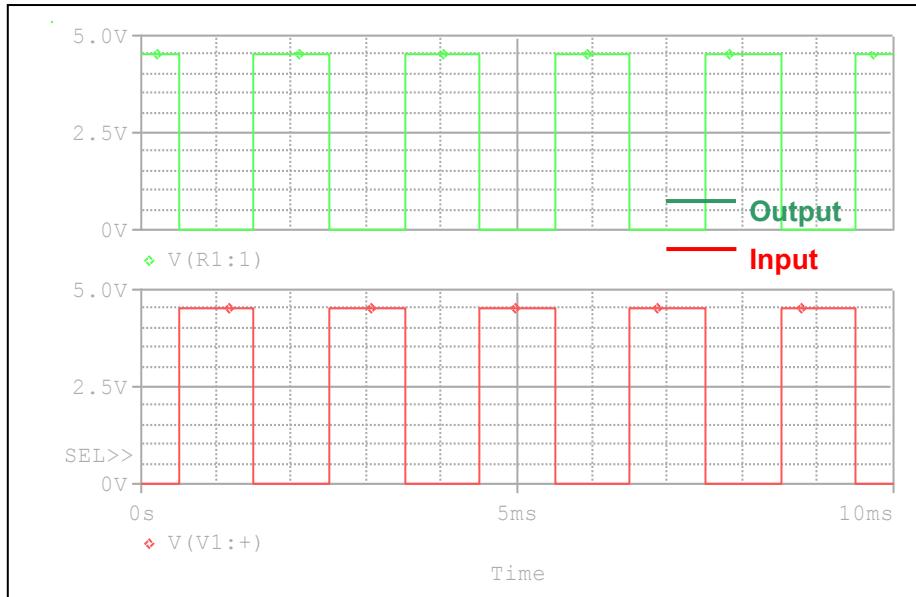


Comparison table

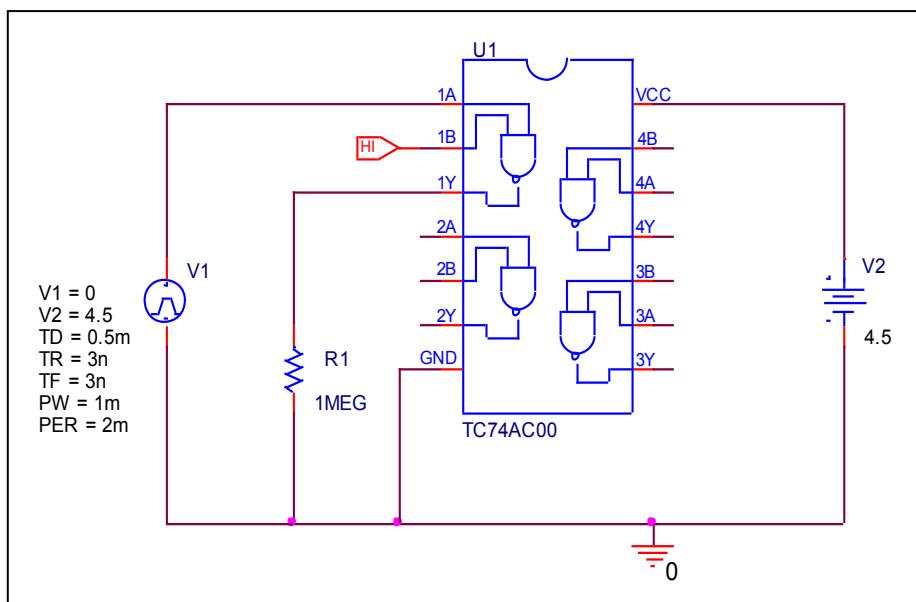
| $V_{CC} = 5.5V$ | Measurement | Simulation | %Error |
|-----------------|-------------|------------|--------|
| $V_{IH} (V)$ | 3.85 | 3.9823 | 3.436 |
| $V_{IL} (V)$ | 1.65 | 1.6445 | -0.333 |

High Level and Low Level Output Voltage

Circuit simulation result



Evaluation circuit

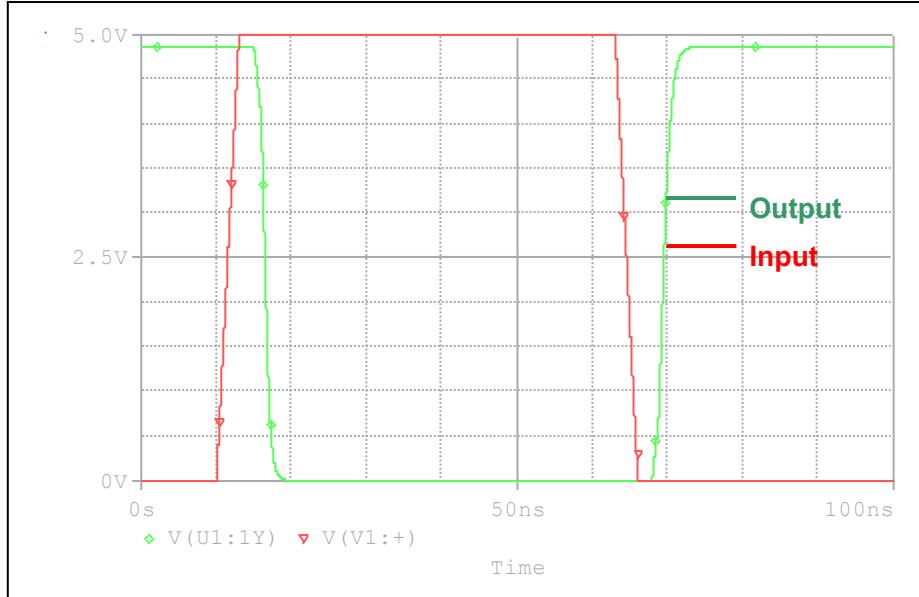


Comparison table

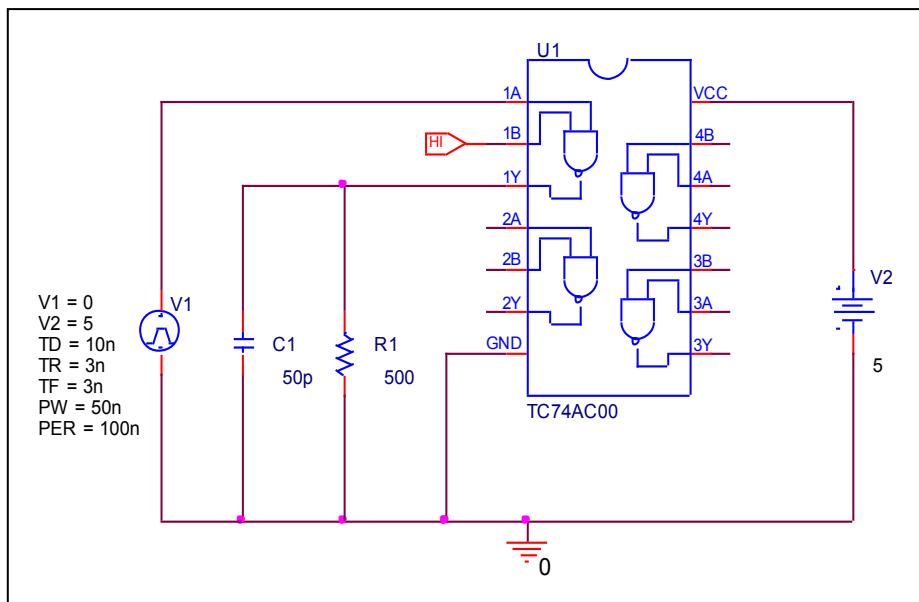
| $V_{CC} = 4.5V$ | Measurement | Simulation | %Error |
|-----------------|-------------|------------|--------|
| $V_{OH}(V)$ | 4.5 | 4.4994 | -0.013 |
| $V_{OL}(V)$ | 0 | 0 | 0 |

Propagation Delay Time

Circuit simulation result



Evaluation circuit



Comparison table

| $C_L = 50\text{pF}, R_L = 500\Omega, T_r = T_f = 3\text{ns}$ | Measurement | Simulation | %Error |
|--|-------------|------------|--------|
| $t_{pLH} (\text{ns})$ | 4.9 | 4.9551 | 1.124 |
| $t_{pHL} (\text{ns})$ | 4.9 | 4.9662 | 1.351 |