

Instruction Manual for Evaluation Board of The TB6641FG

July 28, 2016 Rev.1.0

[Outline]

The TB6641FG is a brushed DC motor driver corresponding to the constant current PWM control system and the direct PWM control system.

BiCD process is adopted. Ratings of 50V and 4.5A (@ Power supply voltage $(VM) \le 36V$) are realized. When the power supply voltage (VM) is more than 36V, please use the IC by configuring the current range of 4.0A or less.

This evaluation board mounts necessary components to evaluate the IC. Brushed DC motor can be controlled by the direct PWM drive.

In case of controlling the brushed DC motor by the constant current PWM drive, please remove the short resistor (0Ω), which is mounted to R2. Then, replace the current detection resistor that is appropriate for the constant current PWM control.

Please sense controllability of a brushed DC motor by applying the TB6641FG.

[Note]

In using, please be careful about the thermal condition sufficiently.

For each control signal, please refer to the IC specifications by accessing to the following URL:

http://toshiba.semicon-storage.com/info/lookup.jsp?pid=TB6641FG®ion=jp&lang=ja Further, the application of this evaluation board is limited to the purpose of evaluating and learning the motor control. Please do not ship them to a market.

Connection to Evaluation Board 1 [Direct PWM Control]

Correspondence table of silk name and signal name

silk name and signal name

Please pay attention that the silk name and the signal name of the board are different because the TB6641FG and the series ICs use the common board.

Silk name	Signal name
VISD	VREG

Motor control signal input

marutsu *Supply the power in using the switch on the board. Power supply for Power supply for board: VDD motor: VM (3.0V to 5.5V) (10V to 47V) VM GND @marutsu **Brushed DC motor** 100 VREF Motor control signal input

Connection to Evaluation Board 2 [Constant Current PWM Control]

Correspondence table of silk name and signal name

Please pay attention that the silk name and the signal name of the board are different because the TB6641FG and the series ICs use the common board.

Silk name	Signal name
VISD	VREG

Motor control signal input

Reference voltage for motor current set Vref (OV to 3.6V) *Supply the power in using the switch on the board.



marutsu

Setting Evaluation Board 1



Setting discharge term of constant current control



Setting motor current

Iout(max) = VREF(gain) x - F

Vref(V) RRs(Ω) marutsu

VREF(gain): Decay ratio of VREF = 1/10.0 (typ.).

Current detection resistor should be remounted on the board because the resistor of $R_{RS}(0\Omega)$ is mounted.

Waveform of motor current



Setting formula of OSC frequency

 $fOSC = 0.42/(Cosc[F] \times 10^3)$

* It is adjusted by the capacitor connected to C4/R8. Relation of the motor current waveform and



🙄 marutsu

Setting Evaluation Board 2

Setting motor operation

Jumper indicated on the right is adopted on this evaluation board to supply the high-level voltage to the switch. This voltage is supplied from 5V-regulator of the internal IC. In this case, please connect JP_VEG directly.

Jumper indicated on the right is adopted on this evaluation board to set operation of the TB6641FG. To select the function by the jumper, connect JP_VEG directly as in the above figure or supply the voltage of high level by VDD pin.

In the right figure, fixed level of the silk near the jumper is indicated inside the white frame. Please change the short position according to the configuration of the usage function.

In case of inputting the signal externally, please remove the short pin.





[Enhanced figure of jumper part]



Circuit of Evaluation Board



