IQS624 Datasheet

Combination sensor including: Hall-effect rotation sensing, along with dual-channel capacitive proximity/touch sensing, or single-channel inductive sensing.

The IQS624 ProxFusion™ IC is a multifunctional capacitive and Hall-effect sensor designed for applications where any or all of the technologies may be required. The two Hall-effect sensors calculate the angle of a magnet rotating parallel with the sensor. The sensor is fully I²C compatible and on-chip calculations enable the IC to stream the current angle of the magnet without extra calculations.

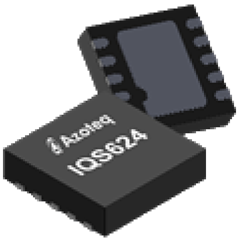
Features

- **Hall effect angle sensor:**
  - On-chip Hall plates
  - 360° Output
  - 1° Resolution, calculated on chip
  - Relative rotation angle.
  - Detect movement and the direction of movement.
  - Raw data: can be used to calculate degrees on external processor.
  - Wide operational range
  - No external components required
- **Partial auto calibration:**
  - Continuous auto-calibration, compensation for wear or small displacements of the sensor or magnet.
  - Flexible gain control
  - **A**utomatic **T**uning **I**mplementation (**ATI**) – Performance enhancement (10 bit).
- **Capacitive sensing**
  - Full auto-tuning with adjustable sensitivity
  - 2pF to 200pF external capacitive load capability

Inductive sensing

  - Only external sense coil required (PCB trace)
- **Multiple integrated UI**
  - Proximity / Touch
  - Proximity wake-up
  - Event mode
  - Wake Hall sensing on proximity
- Minimal external components
- Standard **I²C** interface
- Optional RDY indication for event mode operation
- **Low power consumption:**
- 240uA (100Hz response, Hall)**,**
- 55uA (100Hz response, capacitive)**,**
- 65uA (20Hz response, Hall)
- 15uA (20Hz response, capacitive)
- 5uA (5Hz response, capacitive)
- Supply Voltage: 2.0V to 3.6V*****

***5V solution available on demand.**



DFN10

Representations only, not actual markings

Applications

- Anemometer
- Dial or Selector knob
- Mouse wheel
- Measuring wheel
- Digital angle gauge
- Speedometer for bicycle

Available Packages	
T_A	DFN(3x3)-10
-20°C to 85°C	IQS624-xyy

Contents

List of abbreviations

PXS – ProxSense®

ATI – Automatic Tuning Implementation

LTA – Long term average

Thr – Threshold

UI – User interface

AC – Alternating current

DSP – Digital signal processing

RX – Receiving electrode

TX – Transmitting electrode

CS – Sampling capacitor

C – Capacitive

NP – Normal power

LP – Low power

ULP – Ultra low power

ACK – I^2C Acknowledge condition

NACK – I^2C Not Acknowledge condition

FG – Floating gate

1 Introduction

1.1 ProxFusion™

The ProxFusion™ sensor series provide all the proven ProxSense® engine capabilities with additional sensors types. A combined sensor solution is available within a single platform.

1.2 Packaging and Pin-Out

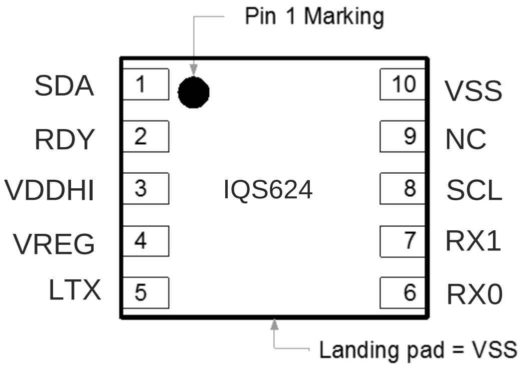


Figure 1-1 Pin out of IQS624 DFN (3X3)-10 package.

Table 1-1 IQS624 Pin-out

IQS624 Pin-out			
Pin	**Name**	**Type**	**Function**
1	SDA	Digital Input / Output	I^{2}C: SDA Output
2	RDY	Digital Output	I^{2}C: RDY Output
3	VDDHI	Supply Input	Supply Voltage Input
4	VREG	Regulator Output	Internal Regulator Pin (Connect 1µF bypass capacitor)
5	LTX	Analogue	Transmit Electrode 1
6	RX0	Analogue	Sense Electrode 0
7	RX1	Analogue	Sense Electrode 1/ Transmit Electrode 0
8	SCL	Digital Input / Output	I^{2}C: SCL Output
9	NC	Not connect	Not connect
10	VSS	Supply Input	Ground Reference

1.3 Reference schematic

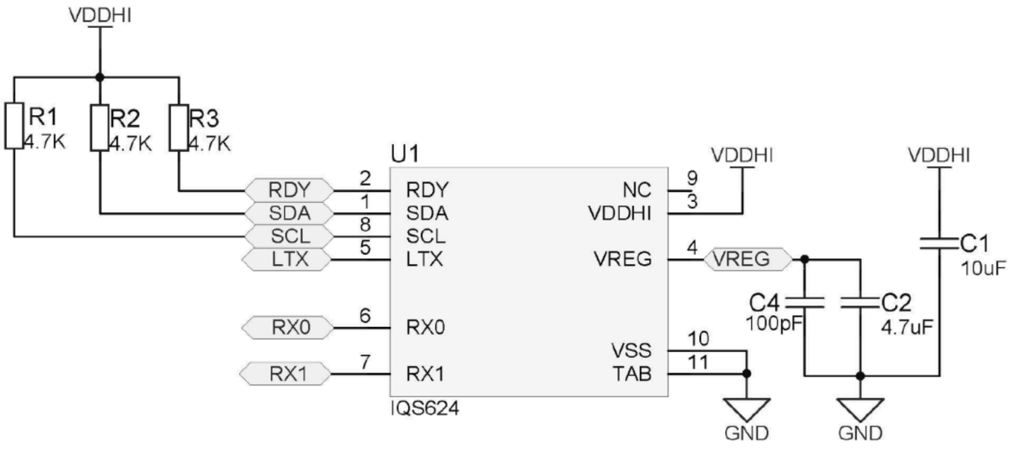


Figure 1-2 IQS624 reference schematic

1.4 Sensor channel combinations

The table below summarizes the IQS624's sensor and channel associations.

Table 1-2 Sensor - channel allocation

Sensor type	CH0	CH1	CH2	CH3	CH4	CH5
Discreet Self Capacitive	○	○				
Hall effect rotary UI			● 1st plate Positive	● 1st plate Negative	● 2nd plate Positive	● 2nd plate Negative
Mutual Inductive	○	○				

Key:

○ Optional implementation

● Fixed use for UI

2 Capacitive sensing

2.1 Introduction

Building on the previous successes from the ProxSense® range of capacitive sensors, the same fundamental sensor engine has been implemented in the ProxFusion™ series.

The capacitive sensing capabilities of the IQS624 include:

- Maximum of 2 capacitive channels to be individually configured.
    - Prox and touch adjustable thresholds
    - Individual sensitivity setups
    - Alternative ATI modes
- Discreet button UI:
    - Fully configurable 2 level threshold setup – traditional prox & touch activation levels.
    - Customizable filter halt time

2.2 Channel specifications

The IQS624 provides a maximum of 2 channels available to be configured for capacitive sensing. Each channel can be setup separately per the channel's associated settings registers.

Table 2-1 Capacitive sensing - channel allocation

Sensor type	CH0	CH1	CH2	CH3	CH4	CH5
Discreet Self Capacitive	○	○				

Key:

Optional implementation

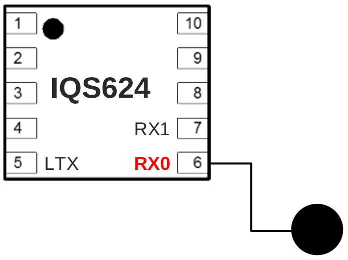
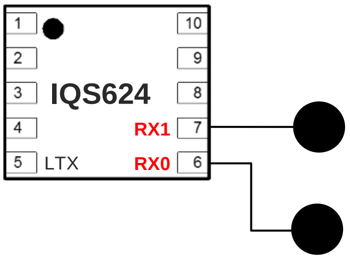
○ Optional implementation

● Fixed use for UI

2.3 Hardware configuration

In the table below are two options of configuring sensing (Rx) electrodes.

Table 2-2 Capacitive hardware description

	Self-capacitive configuration
1 button	
2 buttons	

2.4 Register configuration

2.4.1 Registers to configure for the capacitive sensing:

Table 2-3 Capacitive sensing settings registers

Address	Name	Description	Recommended setting
0x40, 0x41	Ch0/Ch1 ProxFusion Settings 0	Sensor mode and configuration of each channel.	Sensor mode should be set to capacitive mode An appropriate RX should be chosen and no TX
0x42	Ch0&Ch1 ProxFusion Settings 1	Global settings for the ProxFusion sensors	None
0x43, 0x44	Ch0/Ch1 ProxFusion Settings 2	ATI settings for ProxFusion sensors	ATI target should be more than ATI base to achieve an ATI
0x45	Ch0&Ch1 ProxFusion Settings 3	Additional Global settings for ProxFusion sensors	AC filter should be enabled
0x50, 0x52	Proximity threshold	Proximity Threshold for UI	Preferably more than touch threshold
0x51, 0x53	Touch threshold	Touch Threshold for UI	None

2.4.2 Proximity Thresholds

A proximity threshold for both channels can be selected for the application, to obtain the desired proximity trigger level. The proximity threshold is selectable between 1 (most sensitive) and 255 (least sensitive) counts. These threshold values (i.e. 1-255) are specified in Counts (CS) in the Ch0 Proximity threshold (0x50) and Ch1 Proximity threshold (0x51) registers for the discreet button UI.

2.4.3 Touch Thresholds

A touch threshold for each channel can be selected by the designer to obtain the desired touch sensitivity and is selectable between 1/256 (most sensitive) to 255/256 (least sensitive). The touch threshold is calculated as a fraction of the Long-Term Average (LTA) given by,

$$T_{THR} = {}^{x}/_{256} \times LTA$$

With lower target values (therefore lower LTA's) the touch threshold will be lower and vice versa.

Individual touch thresholds can be set for each channel, by writing to the Ch0 Touch threshold (0x51) and Ch1 Touch threshold (0x53) for the discreet button UI.

2.4.4 Example code:

Example code for an Arduino Uno can be downloaded at:

www.azoteq.com//images/stories/software/IQS62x_Demo.zip

2.5 Sensor data output and flags

The following registers should be monitored by the master to detect capacitive sensor output.

a) The **UI Flags register (0x11)** will show the IQS624's main events. Bit0&1 is dedicated to the ProxFusion activations, bit0 indicates a proximity event and bit1 indicates a touch event.

UI Flags(0x11)								
Bit Number	7	6	5	4	3	2	**1**	**0**
Data Access	Read							
Name							**PXS Touch out**	**PXS proximity out**

b) The **Proximity/Touch UI Flags (0x12)** provide more detail regarding the outputs. A proximity and touch output bit for each channel 0 and 1 is provided in the PRX UI Flags register.

Proximity/Touch UI Flags (0x12)								
Bit Number	7	6	**5**	**4**	3	2	**1**	**0**
Data Access	Read							
Name			**Chan 1 Touch out**	**Chan 0 touch out**			**Chan 1 proximity out**	**Chan 0 proximity out**

3 Inductive sensing

3.1 Introduction to inductive sensing

The IQS624 provides inductive sensing capabilities to detect the presence of metal/metal-type objects.

3.2 Channel specifications

The IQS624 requires 3 sensing lines for mutual inductive sensing.

There's only one distinct inductance user interfaces available.

a) Discreet proximity/touch UI (always enabled)

Table 3-1 Mutual inductive sensor – channel allocation

Mode	CH0	CH1	CH2	CH3	CH4	CH5
Mutual inductive	○	○				

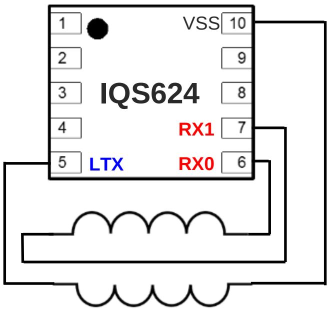
Key:

○ - Optional implementation
● - Fixed use for UI

3.3 Hardware configuration

Rudimentary hardware configurations (to be completed).

Table 3-2 Mutual inductive hardware description

	Mutual inductive
Mutual inductance	

3.4 Register configuration

Table 3-3 Inductive sensing settings registers.

Address	Name	Description	Recommended setting
0x40, 0x41	Ch0/Ch1 ProxFusion Settings 0	Sensor mode and configuration of each channel.	Sensor mode should be set to Inductive mode Choose one channel and deactivate the other channel Enable both RX for the activated channel
0x42	Ch0&Ch1 ProxFusion Settings 1	Global settings for the ProxFusion sensors	CS divider should be enabled
0x43, 0x44	Ch0/Ch1 ProxFusion Settings 2	ATI settings for ProxFusion sensors	ATI target should be more than ATI base to achieve an ATI
0x45	Ch0&Ch1 ProxFusion Settings 3	Additional Global settings for ProxFusion sensors	None
0x50, 0x52	Proximity threshold	Proximity Threshold for UI	Less than touch threshold
0x51, 0x53	Touch threshold	Touch Threshold for UI	None

3.4.1 Example code:

Example code for an Arduino Uno can be downloaded at:

www.azoteq.com//images/stories/software/IQS62x_Demo.zip

3.5 Sensor data output and flags

The following registers should be monitored by the master to detect inductive sensor output.

a) The **UI Flags register (0x11)** provides the classic prox/touch two level activation outputs which can be used for inductive sensing.

UI Flags(0x11)								
Bit Number	7	6	5	4	3	2	**1**	**0**
Data Access	Read							
Name							**PXS Touch out**	**PXS proximity out**

4 Hall-effect sensing

4.1 Introduction to Hall-effect sensing

The IQS624 has two internal Hall-effect sensing plates (on die). No external sensing hardware is required for Hall-effect sensing.

The Hall-effect measurement is essentially a current measurement of the induced current through the Hall-effect-sensor plates produced by the magnetic field passing perpendicular through each plate.

Advanced digital signal processing is performed to provide sensible output data.

- Hall output is linearized by inverting signals.
- Calculates absolute position in degrees.
- Auto calibration attempts to linearize degrees output on the fly
- Differential Hall-Effect sensing:
    - Removes common mode disturbances

4.2 Channel specifications

Channels 2 to 5 are dedicated to Hall-effect sensing. Channel 2 & 4 performs the positive direction measurements and channel 3 & 5 will handle all measurements in the negative direction. Differential data can be obtained from these four channels. This differential data is used as input data to calculate the output angle of the Hall-effect rotation UI. Channel 2 & 3 is used for the one plate and channel 4 & 5 for the second plate.

Table 4-1 Hall-effect sensor – channel allocation

Mode	CH0	CH1	CH2	CH3	CH4	CH5
Hall rotary UI			● 1st plate Positive	● 1st plate Negative	● 2nd plate Positive	● 2nd plate Negative

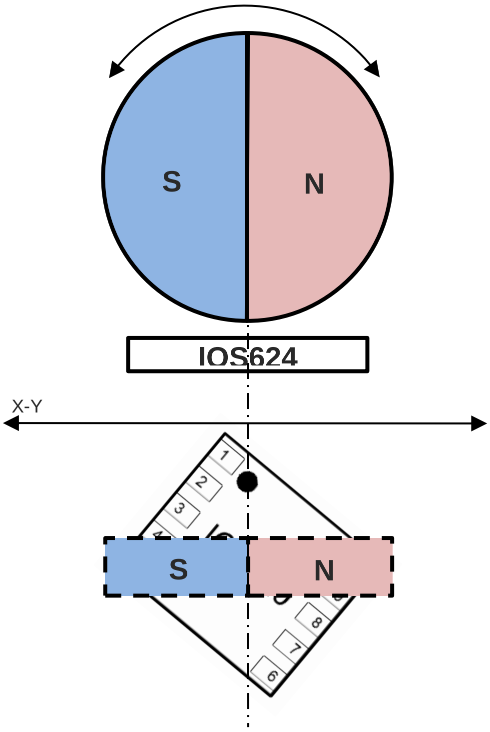
Key:

○ - Optional implementation

● - Fixed use for UI

4.3 Hardware configuration

Rudimentary hardware configurations. For more detail and alternative placement options, refer to appendix A.

	Diametrically polarized magnet (rotational purposes)
Hall Rotation UI	

4.4 Register configuration

Table 4-2 Hall sensing settings registers

Address	Name	Description	Recommended setting
70H	Hall Rotation UI Settings	Hall wheel UI settings	Hall UI should be enabled for degree output
71H	Hall sensor settings	Auto ATI and charge frequency settings	Auto ATI should be enabled for temperature drift compensation
72H, 73H	Hall ATI Settings(1)	Hall channels ATI settings	ATI Target should be more than base
78H	Hall ratio Settings	Invert Direction setting for Hall UI	None
79H	Sin(phase) constant	Sin phase calibration value	Calculate this value using the GUI or the calculations in the appendix A
7AH	Cos(phase) constant	Cos phase calibration value	Calculate this value using the GUI or the calculations in the appendix A

(1) – Check errata

4.4.1 Example code:

Example code for an Arduino Uno can be downloaded at:

www.azoteq.com//images/stories/software/IQS62x_Demo.zip

4.5 Sensor data output and flags

a) The **Hall UI Flags (0x14) register**. Bit7 is dedicated to indicating a movement of the magnet. Bit6 indicates the direction of the movement. Bit 1 is set when the movement counts are negative and bit 0 is set when the relative angle is negative. Bit 1 & 0 is used for on-chip angle calculation, bit 6 can be used to determine the magnet direction.

Hall UI Flags (0x14)								
Bit Number	**7**	**6**	5	4	3	2	**1**	**0**
Data Access	Read							
Name	**Wheel movement**	**Movement direction**					**Count sign**	**Difference sign**

b) The **Degree Output (0x81-0x80).** A 16-bit value for the degrees can be read from these registers. (0-360 degrees)

Degree Output (0x81-0x80)																
Bit Number	**15**	**14**	**13**	**12**	**11**	**10**	**9**	**8**	**7**	**6**	**5**	**4**	**3**	**2**	**1**	**0**
Data Access	Read/Write															
Name	**Degrees High Byte**								**Degrees Low Byte**							

c) The **Relative Rotation Angle (0x8E).** The delta in degrees from the previous cycle to the current cycle can be read from this register. (0-180 degrees)

Relative Rotation Angle (0x8E)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	**Relative degrees**							

5 Device clock, power management and mode operation

5.1 Device main oscillator

The IQS624 has a **16MHz** main oscillator (default enabled) to clock all system functionality.

An option exists to reduce the main oscillator to 8MHz. This will result in charge transfers to be slower by half of the default implementations.

To set this option:

- ○ As a software setting – Set the General System Settings (0xD0): bit4 = 1, via an I^{2}C command.
- ○ As a permanent setting – Set the OTP option in FG Bank 0: bit2 = 1, using Azoteq USBProg program.

The ProxFusion channels charges at half of the main oscillator frequency. Therefore the frequency multiplier selected in Ch0&1 ProxFusion Settings 1 (0x42; bit 4-5) and Hall sensor settings (0x71; bit 4-5) is multiplied by half of the main oscillator frequency.

5.2 Device modes

The IQS624 supports the following modes of operation;

- **Normal mode** (Fixed report rate)
- **Low Power mode** (Reduced report rate, no UI execution)
- **Ultra-Low Power mode** (Only channel 0 is sensed for a prox)
- **Halt Mode** (Suspended/disabled)
  Note: Auto modes must be disabled to enter or exit halt mode.

The device will automatically switch between the different operating modes by default. However, this Auto mode feature may be disabled by setting the Disable Auto Modes bit (Power Mode Settings 0xD2; bit 5) to confine device operation to a specific power mode. The Power Mode bits (Power Mode Settings 0xD2; bit 3-4) can then be used to specify the desired mode of operation.

5.2.1 Normal mode

Normal mode is the fully active sensing mode to function at a fixed report rate specified in the Normal Mode report rate (0xD3) register. This 8-bit value is adjustable from 0ms – 255ms in intervals of 1ms.

Note: The device's low power oscillator has an accuracy as specified in section 9.

5.2.2 Low power mode

Low power mode is a reduced sensing mode where all channels are sensed but no UI code are executed. The sample rate can be specified in the Low Power Mode report rate (0xD4) register. The 8-bit value is adjustable from 0ms – 255ms in intervals of 1ms. Reduced report rates also reduce the current consumed by the sensor.

Note: The device's low power oscillator has an accuracy as specified in section 9.

5.2.3 Ultra-low power mode

Ultra-low power mode is a reduced sensing mode where only channel 0 is sensed and no other channels or UI code are executed. Set the Enable ULP Mode bit (Power Mode Settings 0xD2; bit 6) to enable use of the ultra-low power mode. The sample rate can be specified in the Low Power Mode report rate (0xD5) register. The 8-bit value is adjustable from 0ms – 4sec in intervals of 16ms.

When in Ultra-low power mode the IQS624 can be configured to update all channels at a specific rate defined in Power Mode Settings (0xD2) register. A flag will be set in the System flags (0x10; bit 0) register when a normal power update is performed. Wake up will occur on proximity detection on channel 0.

5.2.4 Halt mode

Halt mode will suspend all sensing and will place the device in a dormant or sleep state. The device requires an I²C command from a master to explicitly change the power mode out of the halt state before any sensor functionality can continue.

5.2.5 Mode time

The mode time is specified in the Auto Mode Timer (0xD6) register. The 8-bit value is adjustable from 0ms – 2 min in intervals of 500ms.

5.3 Streaming and event mode:

Streaming mode is the default. Event mode is enabled by setting bit 5 in the General System Settings (0xD0) register.

5.3.1 Streaming mode

The ready is triggered every cycle and per the report rate.

5.3.2 Event mode

The ready is triggered only when an event has occurred.

The events which trigger the ready:

- Hall wheel movement (If the hall UI is enabled)
- Touch or proximity events on channel 0 or 1

Note: Both these events have built in hysteresis which filters out very slow changes

5.4 Report rates

5.4.1 Normal Power Maximum Report rate

Note: Assuming normal mode report rate set to 0 (maximum speed) and Auto Power Modes turned off.

Hall UI State	Channels	Register Address	Bytes	Functionality[1]	Report Rate[2]
On	2 x Prox 4 x Hall	0x02 (PXS Flags) 0x80-0x81 (Degrees)	3	On-chip calculation of rotation angle and prox channels.	4.87 ms
On	4 x Hall	0x80-0x81 (Degrees)	2	On-chip calculation of rotation angle.	3.29 ms
Off	2 x Prox 4 x Hall	0x02 (PXS Flags) 0x24-0x2B (Counts)	9	Off-chip calculation of rotation angle and on-chip prox channels.	3.93 ms
Off	4 x Hall	0x24-0x2B (Counts)	8	Off-chip calculation of rotation angle.	2.94 ms
Off	1 x Hall 2 x Prox	0x24 (CH2 Counts) 0x02 (PXS Flags)	3	Off-chip RPM-calculation and 2 Prox channels on-chip	2.25 ms
Off	1 x Hall 1 x Prox	0x24 (CH2 Counts) 0x02 (PXS Flags)	3	Off-chip RPM-calculation and 1 Prox channels on-chip	1.63 ms
Off	1 x Hall	0x24 (CH2 Counts)	2	Off-chip RPM-calculation	0.82 ms

- Report rates are not necessarily an accurate indication of maximum observable rotation rate. On-chip calculations are only accurate at low rotation rates.

(1) Contact Azoteq for further information on functionality.
(2) These values were calculated by design and not by testing.

Normal Power Segment rate

To be completed.

Auto modes change rates

To be completed.

Streaming/event mode rates

To be completed.

5.5 System reset

The IQS624 device monitor's system resets and events.

a) Every device power-on and reset event will set the Show Reset bit in the System Flags (0x10; bit 7) register and the master should explicitly clear this bit by setting the Ack Reset bit in the General System Settings (0xD0; bit 6) register.

b) The system events will also be indicated with the Event bit in the System Flags (0x10; bit 1) register if any system event occur such as a reset. This event will continuously trigger until the reset has been acknowledged.

6 Communication

The **IQS624** device interfaces to a master controller via a 3-wire (SDA, SCL and RDY) serial interface bus that is I^{2}C^{TM} compatible with a maximum communication speed of 400 kHz. The communications interface of the IQS624 supports the following:

- Streaming data as well as event mode.
- The master may address the device at any time. If the IQS624 is not in a communication window, the device returns an ACK after which clock stretching is induced until a communication window is entered. Additional communication checks are included in the main loop in order to reduce the average clock stretching time.
- The provided interrupt line (RDY) is push-pull active low implementation and indicates a communication window.

6.1 Control Byte

The Control byte indicates the 7-bit device address (44H default) and the Read/Write indicator bit. The structure of the control byte is shown in Figure 3.1.

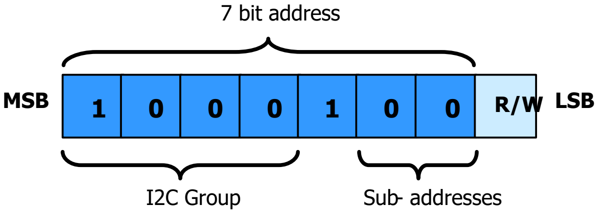


Figure 3.1 IQS624 Control Byte

The I^{2}C device has a 7 bit Slave Address (default 0x44H) in the control byte as shown in Figure 3.1. To confirm the address, the software compares the received address with the device address. Sub-address values can be set by OTP programming options.

6.2 I^{2}C Read

To read from the device a *current address read* can be performed. This assumes that the address-command is already setup as desired.

Current Address Read

Start	Control Byte		Data n		Data n+1		Stop
S		ACK		ACK		NACK	S

Figure 3.2 Current Address Read

If the address-command must first be specified, then a *random read* must be performed. In this case a WRITE is initially performed to setup the address-command, and then a repeated start is used to initiate the READ section.

Random Read

Start	Control Byte		Address-command		Start	Control Byte		Data n		Stop
S	Adr + WRITE	ACK		ACK	S	Adr + READ	ACK		NACK	S

Figure 6.3 Random Read

6.3 I^{2}C Write

To write settings to the device a *Data Write* is performed. Here the Address-Command is always required, followed by the relevant data bytes to write to the device.

DATA WRITE

Start	Control Byte		Address-Command		Data n		Data n+1		Stop
S	Adr + WRITE	ACK		ACK		ACK		ACK	S

Figure 6.4 I^{2}C Write

6.4 End of Communication Session / Window

Similar to other Azoteq I^{2}C devices, to end the I^{2}C communication session, a STOP command is given. When sending numerous read and write commands in one communication cycle, a repeated start command must be used to stack them together (since a STOP will jump out of the communication window, which is not desired).

The STOP will then end the communication, and the **IQS624** will return to process a new set of data. Once this is obtained, the communication window will again become available (RDY set LOW).

6.4 Device address and sub-addresses

The default device address is **0x44 = DEFAULT_ADDR**.

Alternative sub-address options are definable in the following one-time programmable bits: **OTP Bank0 (bit3; 0; bit1; bit0) = SUB_ADDR_0** to **SUB_ADDR_7**

a) Default address: **0x44 = DEFAULT_ADDR OR SUB_ADDR_0**

b) Sub-address: **0x45 = DEFAULT_ADDR OR SUB_ADDR_1**

c) Sub-address: **0x46 = DEFAULT_ADDR OR SUB_ADDR_2**

d) Sub-address: **0x47 = DEFAULT_ADDR OR SUB_ADDR_3**

e) Sub-address: **0x4C = DEFAULT_ADDR OR SUB_ADDR_4**

f) Sub-address: **0x4D = DEFAULT_ADDR OR SUB_ADDR_5**

g) Sub-address: **0x4E = DEFAULT_ADDR OR SUB_ADDR_6**

h) Sub-address: **0x4F = DEFAULT_ADDR OR SUB_ADDR_7**

6.5 Additional OTP options

All one-time-programmable device options are located in FG bank 0.

Floating Gate Bank0								
Bit Number	7	6	5	4	3	2	1	0
Name	-	Comms ATI	-	Rdy active high	Sub address 2	8MHz	Sub address 0-1	
Default	-	1	-	0	0	0	0	0

Bit definitions:

- Bit 0,1,3: I2C sub-address
    - I2C address = 0x44
- Bit 2: Main Clock frequency selection
    - 0: Run FOSC at 16MHz
    - 1: Run FOSC at 8MHz
- Bit 4: Rdy active high
    - 0: Rdy active low enabled
    - 1: Rdy active high enabled
- Bit 6: Comms mode during ATI
    - 0: No streaming events are generated during ATI
    - 1: Comms continue as setup regardless of ATI state.

6.5 RDY Hand-Shake Routine

The master or host MCU has the capability to request a communication window at any time, by writing the device address to the IQS624. The communication window will open directly following the current conversion cycle. The RDY line can be configured as active high by setting the additional OTP bits (bit 4). For more details please refer to the communication interface guide.

6.6 I^{2}C Specific Commands

6.6.1 Show Reset

After start-up, and after every reset event, the “Show Reset” flag will be set in the System Flags register (0x10H; bit 7).

The “Show Reset” bit can be read to determine whether a reset has occurred on the device (it is recommended to be continuously monitored). This bit will be set ’1’ after a reset.

The “Show Reset” flag will be cleared (set to ’0’) by writing a ’1’ into the “Ack reset” bit in the General system settings register (0xD0; bit 6) . A reset will typically take place if a timeout during communication occurs.

6.6.2 I2C Timeout

If no communication is initiated from the master/host MCU within the first t_{COMMS} (t_{COMMS} = 2.038 ms default) of the RDY line indicating that data is available (i.e. RDY = low), the device will resume with the next cycle of charge transfers and the data from the previous conversions will be lost. There is also a timeout (t_{I2C}) that cannot be disabled, for when communication has started but not been completed, for example when the bus is being held by another device (t_{I2C} = 33 ms).

6.7 I^{2}C I/O Characteristics

The **IQS624** requires the input voltages given in 0, for detecting high (“1”) and low (“0”) input conditions on the I^{2}C communication lines (SDA, SCL and RDY).

Table 6-1 IQS624 I²C Input voltage

	Input Voltage (V)
Vin_{LOW}	0.3*VDDHI
Vin_{HIGH}	0.7*VDDHI

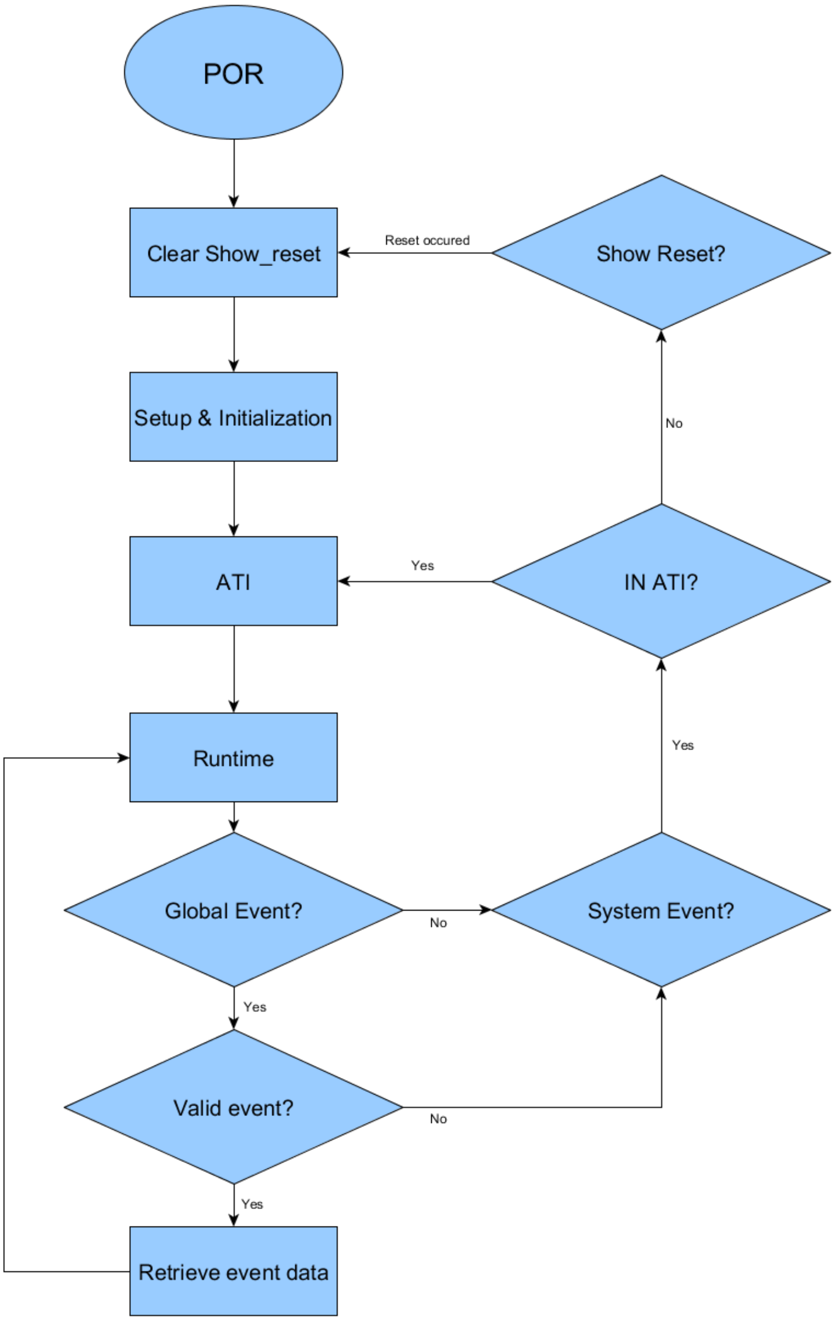
0 provides the output voltage levels of the IQS624 device during I²C communication.

Table 6-2 IQS624 I²C Output voltage

	Output Voltage (V)
$Vout_{LOW}$	GND +0.2 (max.)
$Vout_{HIGH}$	VDDHI – 0.2 (min.)

6.6 Recommended communication and runtime flow diagram

The following is a basic master program flow diagram to communicate and handle the device. It addresses possible device events such as output events, ATI and system events (resets).



Master command structure and runtime event handling flow diagram

It is recommended that the master verifies the status of the System Flags (0x10) bits to identify events and resets. Detecting either one of these should prompt the master to the next steps of handling the IQS624.

Streaming mode communication is used for detail sensor evaluation during prototyping and/or development phases.

Event mode communication is recommended for runtime use of the IQS624. Streaming mode communication is used for detail sensor evaluation during prototyping/development.

7 IQS624 Memory map

Table 7-1 IQS624 Register map

Register Address	Group	Register Name
00H	Device Information	Product Number
01H		Software Number
02H		Hardware Number
10H	Device Specific Data	System Flags
11H		UI Flags
12H		Proximity/Touch UI Flags
14H		HALL UI Flags
15H		Hall Ratio Flags
20H	Count Data	CH0 CS Low
21H		CH0 CS High
22H		CH1 CS Low
23H		CH1 CS High
24H		CH2 CS Low
25H		CH2 CS High
26H		CH3 CS Low
27H		CH3 CS High
28H		CH4 CS Low
29H		CH4 CS High
2AH		CH5 CS Low
2BH		CH5 CS High
30H		CH0 LTA Low
31H		CH0 LTA High
32H		CH1 LTA Low
33H		CH1 LTA High
40H	ProxFusion sensor settings	Ch0 ProxFusion Settings 0
41H		Ch1 ProxFusion Settings 0
42H		Ch0&1 ProxFusion Settings 1
43H		Ch0 ProxFusion Settings 2
44H		Ch1 ProxFusion Settings 2
45H		Ch0&1 ProxFusion Settings 3
46H		Ch0 Compensation
47H		Ch1 Compensation
48H		Ch0 Multipliers
49H		Ch1 Multipliers
50H	Touch / Proximity UI settings	Ch0 Proximity threshold
51H		Ch0 Touch threshold
52H		Ch1 Proximity threshold
53H		Ch1 Touch threshold
54H		UI Halt period

Register Address		Register Name
70H	HALL Sensor Settings	Hall Rotation UI Settings
71H		Hall Sensor Settings
72H		Ch2&3 Hall ATI Settings
73H		Ch4&5 Hall ATI Settings
74H		Ch2&3 Compensation
75H		Ch4&5 Compensation
76H		Ch2&3 Multipliers
77H		Ch4&5 Multipliers
78H		Hall Ratio Settings
79H		Sin Constant
7AH		Cos Constant
80H	HALL Wheel Output	Degree Output (Low byte)
81H		Degree Output (High byte)
82H		Ratio Output (Low byte)
83H		Ratio Output (High byte)
84H		Numerator of Ratio (Low byte)
85H		Numerator of Ratio (High byte)
86H		Denominator of Ratio (Low byte)
87H		Denominator of Ratio (High byte)
88H		Rotation Correction factor (Low byte)
89H		Rotation Correction factor (High byte)
8AH		Max Numerator of Ratio (Low byte)
8BH		Max Numerator of Ratio (High byte)
8CH		Max Denominator of Ratio (Low byte)
8DH		Max Denominator of Ratio (High byte)
8EH		Relative Rotation Angle
8FH		Movement counter/timer
D0H	Device and Power mode Settings	General System Settings
D1H		Active Channels
D2H		Power Mode Settings
D3H		Normal mode report rate
D4H		Low power mode report rate
D5H		Ultra-low power mode report rate
D6H		Auto Mode time

7.1 Device Information

7.1.1 Product Number

Product Number (0x00)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name	Device Product Number							

Bit definitions:

- Bit 0-7: Device Product Number = D'67'

7.1.2 Software Number

Software Number (0x01)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name	Device Software Number							

Bit definitions:

- Bit 0-7: Device Software Number = D'02'

7.1.3 Hardware Number

Hardware Number (0x02)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name	Device Hardware Number							

Bit definitions:

- Bit 0-7: Device Hardware Number = D'162' for 5V solution, D'130' for 3.3V solution

7.2 Device Specific Data

7.2.1 System Flags

System flags (0x10)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name	Show Reset	Ready active high		Current power mode		ATI Busy	Event	NP Segment Active

Bit definitions:

- Bit 7: Reset Indicator:
  - 0: No reset event
  - 1: A device reset has occurred and needs to be acknowledged
- Bit 6: Ready Active High
  - 0: Ready active Low set (Default)
  - 1: Ready active High set
- Bit 4-3: Current power mode indicator:
  - 00: Normal power mode
  - 01: Low power mode
  - 10: Ultra-Low power mode
  - 11: Halt power mode
- Bit 2: ATI Busy Indicator:
  - 0: No channels are in ATI
  - 1: One or more channels are in ATI
- Bit 1: Global Event Indicator:
  - 0: No new event to service
  - 1: An event has occurred and should be handled
- Bit 0: Normal Power segment indicator:
  - 0: Not performing a normal power update
  - 1: Busy performing a normal power update

7.2.2 UI Flags

UI Flags(0x11)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name							PXS Touch out	PXS proximity out

Bit definitions:

- Bit 1: ProxFusion Sensing Touch indicator:
  - 0: No event to report
  - 1: A global touch event has occurred and should be handled
- Bit 0: ProxFusion Sensing proximity indicator:
  - 0: No event to report
  - 1: A global proximity event has occurred and should be handled

7.2.3 Proximity/Touch UI Flags

Proximity/Touch UI Flags (0x12)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name			Chan 1 Touch out	Chan 0 touch out			Chan 1 proximity out	Chan 0 proximity out

Bit definitions:

- Bit 5: Channel 1 touch indicator:
  - 0: Channel 1 delta below touch threshold
  - 1: Channel 1 delta above touch threshold
- Bit 4: Channel 0 touch indicator:
  - 0: Channel 0 delta below touch threshold
  - 1: Channel 0 delta above touch threshold
- Bit 1: Channel 1 Proximity indicator:
  - 0: Channel 1 delta below proximity threshold
  - 1: Channel 1 delta above proximity threshold
- Bit 0: Channel 0 Proximity indicator:
  - 0: Channel 0 delta below proximity threshold
  - 1: Channel 0 delta above proximity threshold

7.2.4 Hall UI Flags

Hall UI Flags (0x14)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name	Wheel movement	Movement direction					Count sign	Difference sign

Bit definitions:

- Bit7: Wheel movement indicator:
  - 0: No wheel movement detected
  - 1: Wheel movement detected
- Bit6: Movement direction indicator:
  - 0: If movement is detected it is in positive direction
  - 1: If movement is detected it is in negative direction
- Bit1: Count sign:
  - 0: Indicates that the movement counts are positive
  - 1: Indicates that the movement counts are negative
- Bit0: Difference sign:
  - 0: Indicates that the angle delta is positive
  - 1: Indicates that the angle delta is negative

7.2.5 Hall Ratio Flags

Hall Ratio Flags (0x15)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read							
Name						Move counter full	Max Denominator set	Max Numerator set

Bit definitions:

- Bit 2: Move counter full indicator:
  - 0: Movement counter is not full
  - 1: Movement counter is full
- Bit 1: Max Denominator set indicator:
  - 0: Max denominator has not changed
  - 1: Max denominator has changed (used for auto calibration)
- Bit 0: Max Numerator set indicator:
  - 0: Max Numerator has not changed
  - 1: Max Numerator has changed (used for auto calibration)

7.3 Count Data

7.3.1 Count CS Values

Count CS values (0x20/0x21-0x2A/0x2B)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read															
Name	Count High Byte								Count Low Byte							

Bit definitions:

- Bit 15-0: Counts
  - AC filter or raw value

7.3.2 LTA Values

LTA values (0x30/0x31-0x32/0x33)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read															
Name	LTA High Byte								LTA Low Byte							

Bit definitions:

- Bit 15-0: LTA Values
  - LTA filter value

7.4 ProxFusion sensor settings

7.4.1 Ch0/1 ProxFusion Settings 0

Capacitive Sensing

Ch0/1 ProxFusion Settings 0 (0x40/0x41)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Sensor mode				TX select		RX select	
Default	0	0	0	0	0	0		

Bit definitions:

- Bit 7-4: Sensor mode select:
  - 0000: Self capacitive mode
- Bit 3-2: TX-select:
  - 00: TX 0 and TX 1 is disabled
- Bit 1-0: RX select:
  - 00: RX 0 and RX 1 is disabled
  - 01: RX 0 is enabled
  - 10: RX 1 is enabled
  - 11: RX 0 and RX 1 is enabled

Inductive Sensing

Ch0/1 ProxFusion Settings 0 (0x40/0x41)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Sensor mode				TX select		RX select	
Default	1	0	0	1	0	0	1	1

Bit definitions:

- Bit 7-4: Sensor mode select:
  - 1001: Mutual Inductive mode
- Bit 3-2: TX-select:
  - 00: TX 0 and TX 1 is disabled
- Bit 1-0: RX select:
  - 11: RX 0 and RX 1 is enabled

7.4.2 Ch0&1 ProxFusion Settings 1

Ch0&1 ProxFusion Settings 1 (0x42)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	-	CS PXS	Charge Freq		Proj bias pxs		Auto ATI Mode	
Default	5BH							

Bit definitions:

- Bit 6: ProxFusion Sensing Capacitor size select:
  - 0: ProxFusion storage capacitor size is 15 pF
  - 1: ProxFusion storage capacitor size is 60 pF
- Bit 5-4: Charge Frequency select:
  - 00: 1/2
  - 01: 1/4
  - 10: 1/8
  - 11: 1/16
- Bit 3-2: Projected bias:
  - 00: 2.5µA / 88kΩ
  - 01: 5µA / 66kΩ
  - 10: 10µA / 44kΩ
  - 11: 20µA / 22kΩ
- Bit 1-0: Auto ATI Mode select:
  - 00: ATI Disabled
  - 01: Partial ATI (Multipliers are fixed)
  - 10: Semi Partial ATI (Coarse multipliers are fixed)
  - 11: Full ATI

7.4.3 Ch0/1 ProxFusion Settings 2

Ch0/1 ProxFusion Settings 2 (0x43-0x44)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	ATI Base		ATI Target					
Default	50H							

Different addresses:

- 0x43: Channel 0 ATI settings
- 0x44: Channel 1 ATI settings

Bit definitions:

- Bit 7-6: ATI Base value select:
  - 00: 75
  - 01: 100
  - 10: 150
  - 11: 200
- Bit 5-0: ATI Target:
  - ATI Target is 6-bit value x 32

7.4.4 Ch0&1 ProxFusion Settings 3

Ch0&1 ProxFusion Settings 3 (0x45)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	-	CS Div	Two sided PXS	ACF Disable	LTA Beta		ACF Beta	
Default	00H							

Bit definitions:

- Bit 6: CS divider
  - 0: Sampling capacitor divider disabled
  - 1: Sampling capacitor divider enabled
- Bit 5: Two sided ProxFusion Sensing
  - 0: Bidirectional detection disabled
  - 1: Bidirectional detection enabled
- Bit 4: ACF Disable
  - 0: AC Filter Enabled
  - 1: AC Filter Disabled
- Bit 3-2: LTA Beta 0
  - 00: 7
  - 01: 8
  - 10: 9
  - 11: 10
- Bit 1-0: ACF Beta 1
  - 00: 1
  - 01: 2
  - 10: 3
  - 11: 4

7.4.5 Ch0/Ch1 Compensation

Ch0/Ch1 Compensation (0x46,0x47)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Compensation (7-0)							

Bit definitions:

- Bit 7-0: 0-255: Lower 8 bits of the Compensation Value

Different addresses:

- 0x46: Channel 0 Lower 8 bits of the Compensation Value
- 0x47: Channel 1 Lower 8 bits of the Compensation Value

7.4.6 Ch0/Ch1 Multipliers values

Ch0/1 Multipliers values(0x48/0x49)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Compensation (9-8)		Coarse multiplier		Fine multiplier			

Bit definitions:

- Bit 7-6: Compensation upper two bits
    - 0-3: Upper 2-bits of the Compensation value.
- Bit 5-4: Coarse multiplier Selection:
    - 0-3: Coarse multiplier selection
- Bit 3-0: Fine Multiplier Selection:
    - 0-15: Fine Multiplier selection

7.5 Touch / Proximity UI settings

7.5.1 Ch0/1 Proximity/touch threshold

Proximity/touch threshold Ch0,1(0x50-0x53)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Threshold							

- Bit 7-0: Proximity and touch thresholds:
  If a difference between the LTA and counts value would exceed this threshold the appropriate event would be flagged (either Touch or Proximity Event).

Different addresses:

- 0x50 Ch0 Proximity Threshold Value
- 0x51 Ch0 Touch Threshold Value
- 0x52 Ch1 Proximity Threshold Value
- 0x53 Ch1 Touch Threshold Value

7.5.2 UI Halt period

UI Halt period (0x54)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	UI Halt period							
Default	28H = 20 sec							

Bit definitions:

- Bit 7-0: Halt time in 500 ms ticks

7.6 HALL Sensor Settings

7.6.1 Hall Rotation UI Settings

Hall Rotation UI Settings (0x70)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Hall Wheel UI disable	-				Auto calibration	-	Wheel wakeup
Default	0	-				1	-	0

Bit definitions:

- Bit 7: Hall Wheel UI disable
    - 0: Hall wheel UI is enabled
    - 1: Hall wheel UI is disabled
- Bit 2: Auto calibration
    - 0: Auto calibration disabled
    - 1: Auto calibration enabled
- Bit 0: Wheel wakeup select
    - 0: Wheel wakeup mode disabled
    - 1: Wheel wakeup mode enabled (wakes up on Ch0 touch)

7.6.2 Hall Sensor Settings

Hall Sensor Settings (0x71)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	-		Charge Freq		-		Auto ATI mode Hall	
Default	-		0	0	-		1	1

Bit definitions:

- Bit 5-4: Charge Frequency: The rate at which our measurement circuit samples
    - 00: 1/2
    - 01: 1/4
    - 10: 1/8
    - 11: 1/16
- Bit 1-0: Auto ATI Mode(1)
    - 00: ATI disabled: ATI is completely disabled
    - 01: Partial ATI: Only adjusts compensation
    - 10: Semi-Partial ATI: Only adjusts compensation and the fine multiplier.
    - 11: Full-ATI: Compensation and both coarse and fine multipliers is adjusted

(1) - Check errata

7.6.3 Ch2/3, Ch4/5 Hall ATI Settings

Ch2/3, Ch4/5 Hall ATI Settings (0x72/0x73)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	ATI Base		ATI Target					
Default	73H							

Different addresses:

- 0x72: Channel 2 & 3 ATI settings
- 0x73: Channel 4 & 5 ATI settings

Bit definitions:

- Bit 7-6: ATI Base value select:
    - 00: 75
    - 01: 100
    - 10: 150
    - 11: 200
- Bit 5-0: ATI Target:
    - ATI Target is 6-bit value x 32

7.6.4 Ch2/3, Ch4/5 Hall Compensation

Ch2/3, Ch4/5 Hall Compensation (0x74,0x75)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Compensation (7-0)							

Bit definitions:

- Bit 7-0: 0-255: Lower 8 bits of the compensation value

Different addresses:

- 0x74: Channel 2/3 Lower 8 bits of the compensation Value
- 0x75: Channel 4/5 Lower 8 bits of the compensation Value

6.7.1 Ch2/3, Ch4/5 Hall Multipliers

Ch2/3, Ch4/5 Hall Multipliers (0x76-0x77)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Compensation 9-8		Coarse Multiplier		Fine Multiplier			

Different addresses:

- 0x76 – Channel 2/3 Multipliers selection
- 0x77 – Channel 4/5 Multipliers selection

Bit definitions:

- Bit 7-6: Compensation 9-8:
    - 0-3: Upper 2-bits of the compensation value
- Bit 5-4: Coarse multiplier selection
    - 0-3: Coarse multiplier selection
- Bit 3-0: Fine multiplier selection
    - 0-15: Fine multiplier selection

7.6.5 Hall Ratio Settings

Hall ratio settings (0x78)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read				Read/Write	Read		
Name		Octant flag	Y negative		Direction invert / Cos negative	Ratio Negative	Denominator negative	Numerator negative

Bit definitions:

- Bit 6-5: Quadrature output for octant changes (per 45 degrees)
  - 0-3: Quadrature output
- Bit 3: Invert direction of degrees
  - 0 – Invert not active
  - 1 – Invert active
- Bit 2: Ratio negative (Used for on-chip angle calculation)
  - 0 – Ratio is positive
  - 1 – Ratio is negative
- Bit 1: Denominator negative (Used for on-chip angle calculation)
  - 0 – Denominator is positive
  - 1 – Denominator is negative
- Bit 0: Numerator negative (Used for on-chip angle calculation)
  - 0 – Numerator is positive
  - 1 – Numerator is negative

7.6.6 Sin Constant

Sin constant (0x79)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Sin constant							

Bit definitions:

- Bit 7-0: Sin constant:
  - Sin (phase difference) x 255

7.6.7 Cos Constant

Cos constant (0x7A)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Cos constant							

Bit definitions:

- Bit 7-0: Cos constant:
  - Cos (phase difference) x 255

Phase difference:

Phase difference measured between the signals obtained from the two Hall sensor plates. This can be calculated with a simple calibration, see Appendix B.

7.7 HALL Wheel Output

7.7.1 Degree Output

Degree Output (0x81-0x80)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Degrees High Byte								Degrees Low Byte							

Bit definitions:

- 0-360: Absolute degree position of magnet

7.7.2 Ratio Output

Ratio Output (0x83-0x82)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Degrees High Byte								Degrees Low Byte							

Bit definitions:

- 16-bit value: Ratio used to calculate degrees

7.7.3 Numerator

Numerator (0x85-0x84)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Numerator High Byte								Numerator Low Byte							

Bit definitions:

- 16-bit value: Numerator used to calculate ratio

7.7.4 Denominator

Denominator (0x87-0x86)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Denominator High Byte								Denominator Low Byte							

Bit definitions:

- 16-bit value: Denominator used to calculate ratio

7.7.5 Rotation Correction factor

Rotation Correction factor (0x89-0x88)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Rotation Correction Factor High Byte								Rotation Correction Factor Low Byte							

Bit definitions:

- 16-bit value: Used for auto calibration

7.7.6 Max Numerator

Max Numerator (0x8B-0x8A)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Max Numerator High Byte								Max Numerator Low Byte							

Bit definitions:

- 16-bit value: Used during auto calibration

7.7.7 Max Denominator

Max Denominator (0x8D-0x8C)																
Bit Number	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Data Access	Read/Write															
Name	Max Denominator High Byte								Max Denominator Low Byte							

Bit definitions:

- 16-bit value: Used during auto calibration

7.7.8 Relative Rotation Angle

Relative Rotation Angle (0x8E)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Relative degrees							

Bit definitions:

- 0-180: Delta in degrees from previous cycle

7.7.9 Movement counter/timer

Movement counter/timer (0x8F)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Movement Timer				Movement Counter			

Bit definitions:

- Bit 7-4: Movement Timer
  - 0-15: Timer used to detect movement
- Bit 3-0: Movement Counter
  - 0-15: Counter used to detect movement

7.8 Device and Power Mode Settings

7.8.1 General System Settings

General System Settings (0xD0)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Soft reset	Ack reset	Event mode	8Mhz	Comms in ATI	Small ATI band	Redo ATI all	Do reseed
Default			1	0	0	0		

Bit definitions:

- Bit 7: Soft Reset **(Set only, will clear when done)**
    - 1 – Causes the device to perform a WDT reset
- Bit 6: Acknowledge reset **(Set only, will clear when done)**
    - 1 – Acknowledge that a reset has occurred. This event will trigger until acknowledged
- Bit 5: Communication mode select:
    - 0 – Streaming communication mode enabled
    - 1 – Event communication mode enabled
- Bit 4: Main clock frequency selction
    - 0 – Run FOSC at 16Mhz
    - 1 – Run FOSC at 8 Mhz
- Bit 3: Communication during ATI select:
    - 0 – No communication during ATI
    - 1 – Communications continue regardless of ATI state
- Bit 2: ATI band selection
    - 0 – Re-ATI when outside 1/8 of ATI target
    - 1 – Re-ATI when outside 1/16 of ATI target
- Bit 1: Redo ATI on all channels (Set only, will clear when done)
    - 1 – Start the ATI process
- Bit 0: Reseed All Long term filters (Set only, will clear when done)
    - 1 – Start the Reseed process

7.8.2 Active Channels Mask

Active Channels Mask (0xD1)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name			CH5	CH4	CH3	CH2	CH1	CH0
Default	3FH							

Bit definitions:

- Bit 5: CH5 (***note: Ch2, Ch3, Ch4 and Ch5 must all be enabled for Hall effect rotation UI to be functional***)
  - 0: Channel is disabled
  - 1: Channel is enabled
- Bit 4: CH4 (***note: Ch2, Ch3, Ch4 and Ch5 must all be enabled for Hall effect rotation UI to be functional***)
  - 0: Channel is disabled
  - 1: Channel is enabled
- Bit 3: CH3 (***note: Ch2, Ch3, Ch4 and Ch5 must all be enabled for Hall effect rotation UI to be functional***)
  - 0: Channel is disabled
  - 1: Channel is enabled
- Bit 2: CH2 (***note: Ch2, Ch3, Ch4 and Ch5 must all be enabled for Hall effect rotation UI to be functional***)
  - 0: Channel is disabled
  - 1: Channel is enabled
- Bit 1: CH1
  - 0: Channel is disabled
  - 1: Channel is enabled
- Bit 0: CH0
  - 0: Channel is disabled
  - 1: Channel is enabled

7.8.3 Power Mode Settings

Power Mode Settings (0xD2)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	NP Segment All	Enable ULP Mode	Disable Auto Modes	Power mode		NP segment rate		
Default	03H							

Bit definitions:

- Bit 7: NP Segment All
  - 0: NP Segment disabled
  - 1: NP Segment enabled
- Bit 6: Enable Ultra-Low Power Mode
  - 0: ULP is disabled during auto-mode switching
  - 1: ULP is enabled during auto-mode switching
- Bit 5: Disable auto mode switching
  - 0: Auto mode switching is enabled
  - 1: Auto mode switching is disabled
- Bit 4-3: Manually select Power Mode (***note: bit 5 must be set***)
  - 00: Normal Power mode. The device runs at the normal power rate, all enabled channels and UIs will execute.
  - 01: Low Power mode. The device runs at the low power rate, all enabled channels and UIs will execute.
  - 10: Ultra-Low Power mode. The device runs at the ultra-low power rate, Ch0 is run as wake-up channel. The other channels execute at the NP-segment rate.
  - 11: Halt Mode. No conversions are performed; the device must be removed from this mode using an I2C command.
- Bit 2-0: Normal Power Segment update rate
  - 000: ½ ULP rate
  - 001: ¼ ULP rate
  - 010: 1/8 ULP rate
  - 011: 1/16 ULP rate
  - 100: 1/32 ULP rate
  - 101: 1/64 ULP rate
  - 110: 1/128 ULP rate
  - 111: 1/256 ULP rate

7.8.4 Normal/Low/Ultra-Low power mode report rate

Normal/Low/Ultra-Low power mode report rate (0xD3 - 0xD5)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Normal/Low power/Ultra-low power mode report rate							

Different addresses:

- 0xD3: Normal mode report rate in ms (Default: 10 ms) ***(note: LPOSC timer has +- 4 ms accuracy)***
- 0xD4: Low power mode report rate in ms (Default: 48 ms) ***(note: LPOSC timer has +- 4 ms accuracy)***
- 0xD5: Ultra-low power mode report rate in 16 ms ticks (Default: 128 ms)

7.8.5 Auto Mode Time

Auto Mode Time (0xD6)								
Bit Number	7	6	5	4	3	2	1	0
Data Access	Read/Write							
Name	Mode time							
Default	14H = 10 sec							

Bit definitions:

- Bit 7-0: Auto modes switching time in 500 ms ticks

8 Electrical characteristics

8.1 Absolute Maximum Specifications

The following absolute maximum parameters are specified for the device:

Exceeding these maximum specifications may cause damage to the device.

Table 8-1 Absolute maximum specification

Parameter	IQS624-3yy	IQS624-5yy
Operating temperature	-20°C to 85°C	
Supply voltage range (VDDHI – GND)	2.00V - 3.6V	2.4V - 5.5V
Maximum pin voltage	VDDHI + 0.5V (may not exceed VDDHI max)	
Maximum continuous current (for specific Pins)	10mA	
Minimum pin voltage	GND - 0.5V	
Minimum power-on slope	100V/s	
ESD protection	±4kV (Human body model)	

8.2 Voltage regulation specifications

Table 8-2 Internal regulator operating conditions

Description	Chipset	Parameter	MIN	TYPICAL	MAX	UNIT
Supply Voltage	IQS624-300	V_{DDHI}	2	-	3.6	V
Internal Voltage Regulator		V_{REG}	1.63	1.66	1.69	V
Supply Voltage	IQS624-500	V_{DDHI}	2.4	-	5.5	V
Internal Voltage Regulator		V_{REG}	1.67	1.7	1.73	V

8.3 Power On-reset/Brown out

Table 8-3 Power on-reset and brown out detection specifications

Description	Conditions	Parameter	MIN	MAX	UNIT
Power On Reset	V_{DDHI} Slope ≥ 100V/s @25°C	POR	1.15	1.6	V
Brown Out Detect	V_{DDHI} Slope ≥ 100V/s @25°C	BOD	1.2	1.6	V

8.4 Current consumptions

Table 8-4 IC subsystem current consumption

Description	TYPICAL	MAX	UNIT
Core active	339	377	µA
Core sleep	0.63	1	µA
Hall sensor active	1.5	2	mA

Table 8-5 IC subsystem typical timing

Description	Core active	Core sleep	Hall sensor active	Total	Unit
Normal	5	5	0.5	10	ms
Low	5	43	0.5	48	ms
Ultra-low	1.75	128	0	129.75	ms

8.4.1 Capacitive sensing alone

Table 8-6 Capacitive sensing current consumption

Solution	Power mode	Conditions	Report rate	TYPICAL	UNIT
3.3V	NP mode	VDD = 2.0V	10 ms	43.5	µA
3.3V	NP mode	VDD = 3.3V	10 ms	44.4	µA
3.3V	LP mode	VDD = 2.0V	48 ms	13.3	µA
3.3V	LP mode	VDD = 3.3V	48 ms	13.8	µA
3.3V	ULP mode	VDD = 2.0V	128 ms	3.9	µA
3.3V	ULP mode	VDD = 3.3V	128 ms	4.5	µA
5V	NP mode	VDD = 2.5V	10 ms	51.3	µA
5V	NP mode	VDD = 5.5V	10 ms	52.3	µA
5V	LP mode	VDD = 2.5V	48 ms	14.5	µA
5V	LP mode	VDD = 5.5V	48 ms	15.5	µA
5V	ULP mode	VDD = 2.5V	128 ms	3.9	µA
5V	ULP mode	VDD = 5.5V	128 ms	5.1	µA

-These measurements where done on the default setup of the IC

8.4.2 Hall-effect sensing alone

Table 8-7 Hall-effect current consumption

Solution	Power mode	Conditions	Report rate	TYPICAL	UNIT
3.3V	NP mode	VDD = 2.0V	10 ms	215.2	µA
3.3V	NP mode	VDD = 3.3V	10 ms	212.6	µA
3.3V	LP mode	VDD = 2.0V	48 ms	58.3	µA
3.3V	LP mode	VDD = 3.3V	48 ms	55.1	µA
3.3V	LP mode	VDD = 2.0V	128 ms	TBA	µA
3.3V	LP mode	VDD = 3.3V	128 ms	19.65	µA
5V	NP mode	VDD = 2.5V	10 ms	240.0	µA
5V	NP mode	VDD = 5.5V	10 ms	239.3	µA
5V	LP mode	VDD = 2.5V	48 ms	64.1	µA
5V	LP mode	VDD = 5.5V	48 ms	64.8	µA
5V	ULP mode	VDD = 2.5V	128 ms	TBA	µA
5V	ULP mode	VDD = 5.5V	128 ms	TBA	µA

-These measurements where done on the default setup of the IC

(1) –It is not advised to use the IQS624 in ULP without capacitive sensing. This is due to the Hall-effect sensor being disabled in ULP.

8.4.3 Halt mode

Table 8-8 Halt mode current consumption

Solution	Power mode	Conditions	TYPICAL	UNIT
3.3V	Halt mode	VDD = 2.0V	1.6	µA
3.3V	Halt mode	VDD = 3.3V	1.9	µA
5V	Halt mode	VDD = 2.5V	1.1	µA
5V	Halt mode	VDD = 5.5V	2.2	µA

8.5 Capacitive loading limits

To be completed.

8.6 Hall-effect measurement limits

To be completed.

9 Package information

9.1 DFN10 package and footprint specifications

DFN-10 Package dimensions (bottom)

Dimension	[mm]
A	3 ±0.1
B	0.5
C	0.25
D	n/a
F	3 ±0.1
L	0.4
P	2.4
Q	1.65

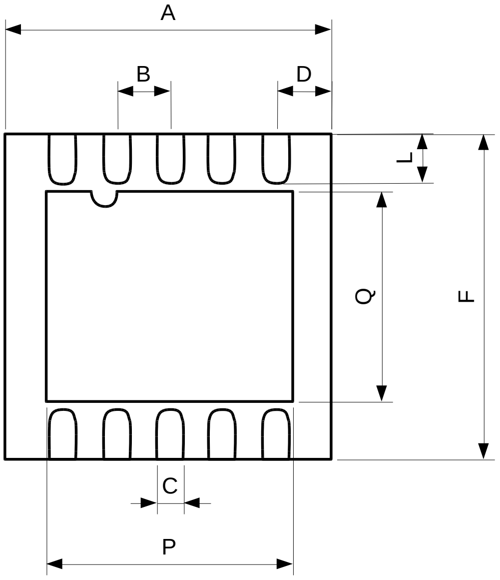
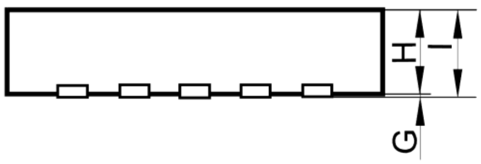


Figure 9.1 DFN-10 Package dimensions (bottom). Note that the saddle need to be connected to GND on the PCB.

DFN-10 Package dimensions (side)

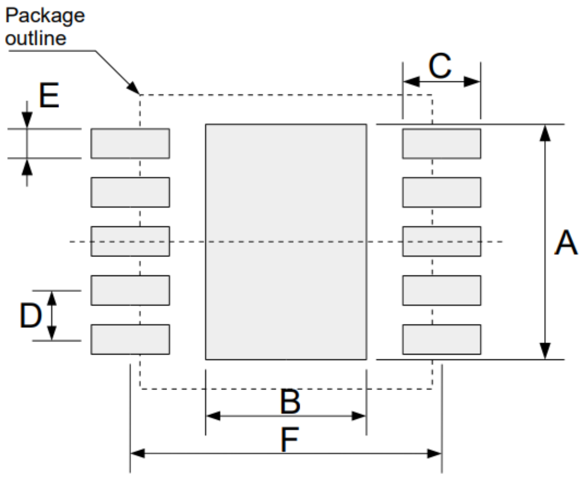
Dimension	[mm]
G	0.05
H	0.65
I	0.7-0.8



DFN-10 Package dimensions (side)

DFN-10 Landing dimensions

Dimension	[mm]
A	2.4
B	1.65
C	0.8
D	0.5
E	0.3
F	3.2



DFN-10 Landing dimension

9.2 Device marking and ordering information

9.2.1 Device marking:

<u>IQS624-xyy</u> <u>z</u> <u>t</u> <u>P</u> <u>WWYY</u>
A B C D E

A. Device name: IQS624-xyy
   x – Version
      3: 3V version
      5: 5V version⁽¹⁾
   yy – Config⁽²⁾
      00: 44H sub-address
      01: 45H sub-address
B. IC revision number: z
C. Temperature range: t
      i: -20° to 85°C
D. For internal use
E. Date code: WWYY
F. Pin 1: Dot

Notes:

(1) 5V version is not in mass production, only available on special request.

(2) Other sub-addresses available on special request, see section 6.2.

9.2.2 Ordering Information:

<u>IQS624-xyyppb</u>

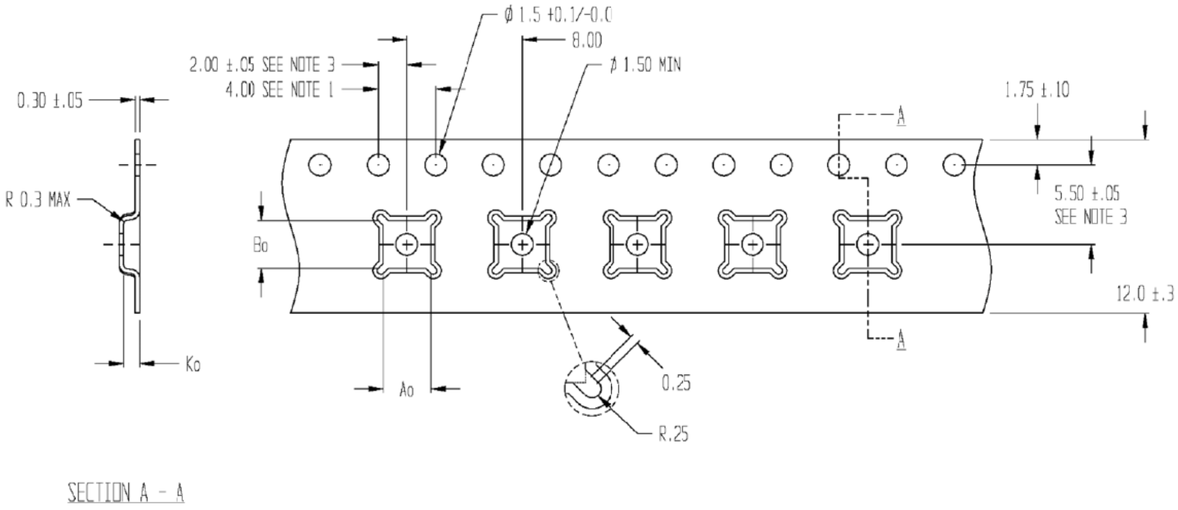
x – Version
   3 or 5
yy – Config
   00 or 01
pp – Package type
   DN (DFN (3x3)-10)
b – Bulk packaging
   R (3k per reel, MOQ=1 Reel)

Example:

IQS624-300DNR

- 3 - refers to 3V version
- 00 - config is default (44H sub-address)
- DN - DFN(3x3)-10 package
- R - packaged in Reels of 3k (has to be ordered in multiples of 3k)

9.3 Tape and reel specification



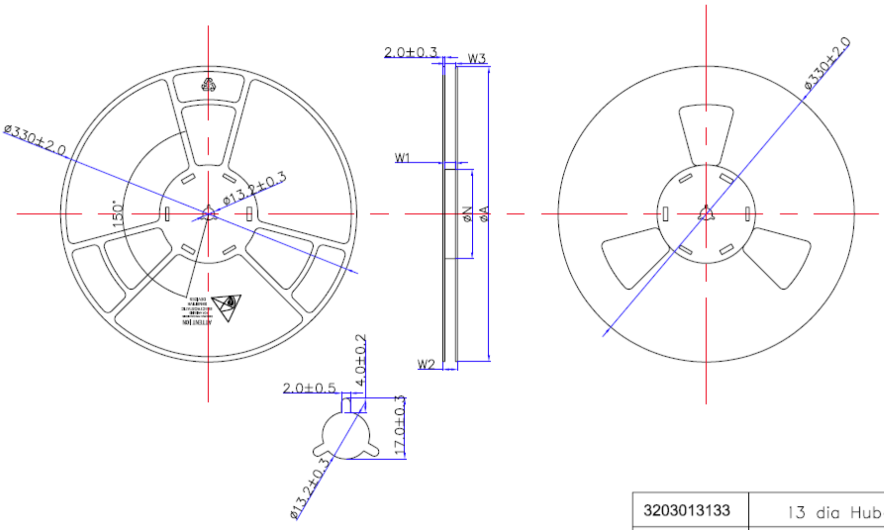
A0=3.30
B0=3.30
K0=1.10

NOTES:

1、10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2

2、CAMBER IN COMPLIANCE WITH EIA 481

3、POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE



3203013133	13 dia Hub4 12mm width PS B
3203013213	13 dia Hub4 16mm width PS B
3203013253	13 dia Hub4 24mm width PS B

PRODUCT SPECIFICATIONS

TYPE WIDTH	ϕA	ϕN	W1(Min)	W2(Max)	W3(Max)
12MM	330±2.0	100±1.0	12.4	18.4	15.4
16mm	330±2.0	100±1.0	16.4	22.4	19.4
24MM	330±2.0	100±1.0	24.4	30.4	27.4

9.4 MSL Level

Moisture Sensitivity Level (MSL) relates to the packaging and handling precautions for some semiconductors. The MSL is an electronic standard for the time period in which a moisture sensitive device can be exposed to ambient room conditions (approximately 30°C/85%RH see J-STD033C for more info) before reflow occur.

Package	Level (duration)
DFN(3x3)-10	MSL 1 (Unlimited at ≤30 °C/85% RH) Reflow profile peak temperature < 260 °C for < 25 seconds Number of Reflow ≤ 3

10 Datasheet revisions

10.1 Revision history

V0.1 – Preliminary structure
V1.03a – Preliminary datasheet
V1.04a – Corrected the following:
    Updated 0x43-0x44 registers: ATI base is [7:6] and not [7:5]
    Added 0x72 and 0x73 registers: ATI settings for CH 2-5
    Added Streaming and event mode chapters
    Added 5V and 3.3V solution
V1.05a - Corrected the following:
    Changed ESD rating
    Added calibration and magnet orientation appendix
    Added induction to summary page
    Updated schematic
    Updated disclaimer
    Updated software and hardware number
V1.10 – Changed from preliminary to production datasheet
  Added:
    Hall ATI Explanation
    Current measurements for power modes
    Register Configuration
  Updated:
    Calibration calculations
    Current consumption on overview
    Appendices
    Pinout update, pin 9 - NC
V1.11 – Updated datasheet
  Added:
    Device markings, order information
    Relative/ absolution summary to appendix
  Updated:
    Supply voltage range
    Reference schematic
    Updated MSL data
V1.12 – Minor updates
  Updated:
    Title
    Images
V1.14 – Minor updates:
  Updated
    Corrected low and high byte order in Register table
V1.15 – Minor Spec corrections:
    Corrected minimum temperature and voltage spec
V1.16 – Magnet spec update
    Corrected magnet specification
V1.17 – Appendix Update
    Updated magnet spec in appendix
V.1.18 – Normal Power Maximum report rate added

V1.19 – Added:
    Errata: Hall ATI values
    I2C Protocol
  Updated:
    IQS624 Memory Map
  Removed:
    Small User Interaction Detection UI
V1.20 – Updated:
    Errata: Hall ATI values
    IQS624 Memory Map
V1.21 – Updated: MSL data
    Appendix A
    Errata: Hall ATI values
V1.22 – Added: Voltage regulation specification

10.2 Errata

10.2.1 Hall ATI values

A software setup change is required for the hall ATI compensation values. During setup of the IQS624, wait for the ATI busy flag to clear in the System flags (10H) register. The following sequence should be followed after the ATI busy flag is cleared:

1. I2C Start
2. Write 0xD4 to register 0xF0
3. Write 0xFF to register 0xF1
4. Write 0xD5 to register 0xF0
5. Write 0x00 to register 0xF1
6. I2C Stop

This setup change will fix errors regarding the hall ATI algorithm that may occur under certain conditions.

This setup requires one rotation for the compensation values to be accurately adjusted. The following procedure should be followed if an accurate absolute degree value is required at startup.

- Follow the startup procedure as usual – write the registers and do an ATI
- Rotate the wheel 360 degrees
- Read the updated compensation values
  - I2C Start
  - Write 0xD4 to register 0xF0
  - I2C Stop
  - I2C Start
  - Read from register 0xF1
  - I2C Stop
  - I2C Start
  - Write 0xD5 to register 0xF0
  - I2C Stop
  - I2C Start
  - Read from register 0xF1
  - I2C Stop
- The two values that has been read should replace 0xFF and 0x00 respectively in the procedure described in 10.2.1. This calibration only needs to be done once and the absolute degree value at startup should be correct.

11 Contact Information

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*Please visit **www.azoteq.com** for a list of distributors and worldwide representation.*

The following patents relate to the device or usage of the device: US 6,249,089; US 6,952,084; US 6,984,900; US 7,084,526; US 7,084,531; US 8,395,395; US 8,531,120; US 8,659,306; US 8,823,273; US 9,209,803; US 9,360,510; EP 2,351,220; EP 2,559,164; EP 2,656,189; HK 1,156,120; HK 1,157,080; SA 2001/2151; SA 2006/05363; SA 2014/01541; SA 2015/023634

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info@azoteq.com

12 Appendices

12.1 Appendix A: Magnet orientation

The IQS624 is able to calculate the angle of a magnet using two Hall sensors which are located in two corners of the die within the package. The two Hall sensors gather data of the magnet field strength in the z-axis. The difference between the two Hall sensors' data can be used to calculate a phase. This phase difference can then be transformed to degrees.

Key considerations for the IQS624:

- There must be a phase difference of 20º-50° between the two Hall sensors.
  It's impossible to calculate the angle if the phase difference is 0° or 180°.
- Reasonable N/S swing on each Hall sensor
  A reasonable peak to peak signal is needed on the plates to ensure optimal on-chip angle calculation.

Table 12-1 Typical recommended magnet

Outer Radius	Inner Radius	Width	Grade	Distance between IC and Magnet axis
2.5 mm	1 mm	3 mm	N40	4 mm

Note: Increasing the width of the magnet can improve error caused by movement in the axis direction.

Ideal design considerations:

- Stable phase difference – This helps with the linearity of the maths.
- Big phase difference – The maths involved has better results with bigger phase difference.
- Distance between the sensors and the magnet should be the same for both – this insures that the magnet fields observed on both sensors are relatively the same.

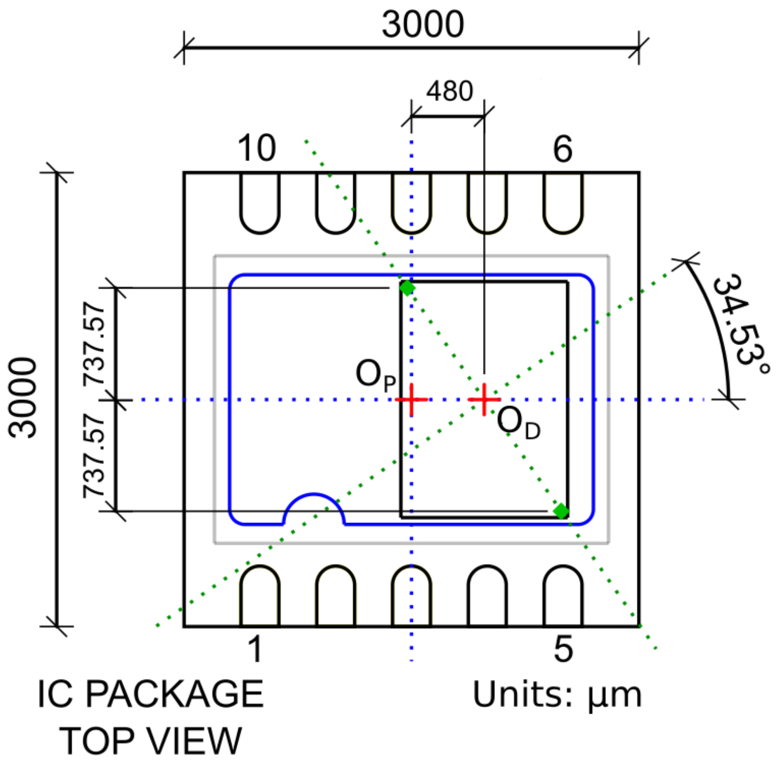


Figure 12-1 - Technical Drawing showing DIE placement within the package. The Hall-Plates are shown as the two green pads in the corners of the DIE. Package axis and hall-plate axis are also shown.

12.1.1 Absolute or relative applications

There are two general applications for a Hall sensor, absolute and relative.

An **absolute application** requires the physical absolute angle of the magnet as an input. It is only possible to obtain the physical angle from a **dipole magnet**.

A **relative application** requires the difference between two positions of the magnet as an input. This makes it possible to use either a **dipole or multipole magnet**. The relative application can also be referred to as an incremental application.

12.1.2 Absolute off-axis magnet position relative to IC:

The IQS624 can be used as an off-axis hall rotation sensor. This means that the IC is placed on a PCB with the PCB parallel to the axis which it is measuring.

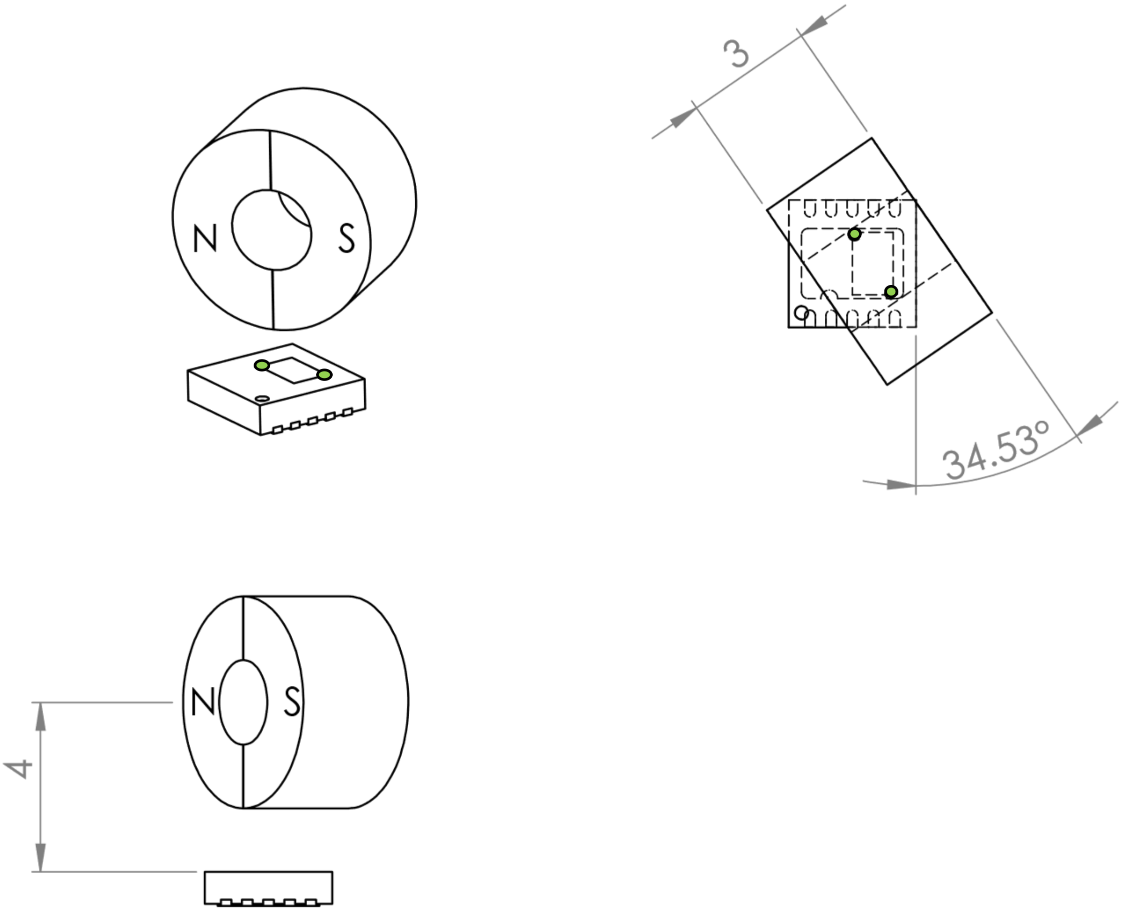


Figure 12-2 Magnet's postion reletave to IC with off-axis orientation

Table 12-2 Typical specifications of off-axis magnet position

	Variables	Typical
A	Outer radius	2.5 mm
B	Inner radius	1 mm
C	Thickness of magnet	1.25 mm
D	Distance between IC and Magnet Axis	3.5 mm
E	Angle of magnet relative to IC	34.53 degrees
F	Residual inductance (B_r)	1.25 T
G	Polarization	Diametrical
H	Magnetic grading	N40

12.1.3 Relative on-axis magnet position relative to IC:

The IQS624 as an on-axis hall rotation sensor. This means that the IC is placed on a PCB with the PCB perpendicular to the axis which it is measuring.

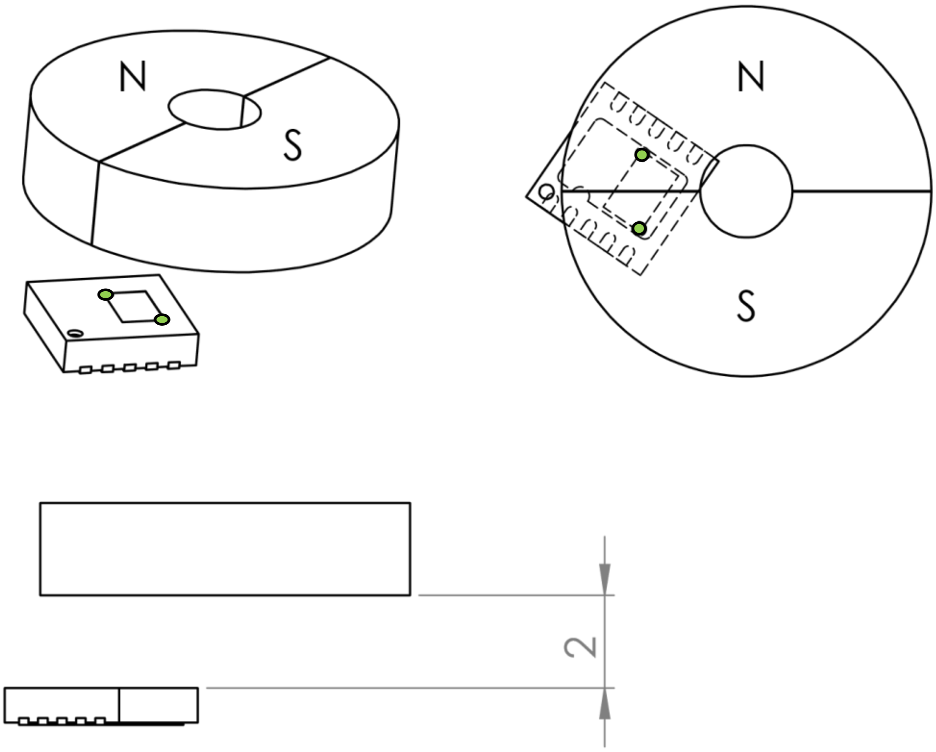


Figure 12-3 Magnet's postion relative to IC with on-axis orientation

Typical specifications of on-axis magnet position

	Variables	Typical
A	Outer radius	2.5 mm
B	Inner radius	1 mm
C	Thickness of magnet	2 mm
D	Distance between IC and Magnet	2 mm
E	Residual inductance (B_r)	1.25 T
F	Polarization	Diametrical
G	Magnetic grading	N40

Preferred magnet orientation comments

Both solutions promote the ideal conditions. However, the EV kit with the magnet parallel with the IC could be more ideal as shown previously. This design was chosen to display the ease of placement our product offers with the built-in corrections and linearization algorithms.

Small movements of the magnet have less impact on the phase difference.

The distance between the magnet and the two sensors are relatively equivalent.

12.1.4 Alternative orientation

Off-centred perpendicular diametrical magnet

Here are two possible solutions. Note that both are off-centred. This is to ensure that a phase difference between the two signals are detected.

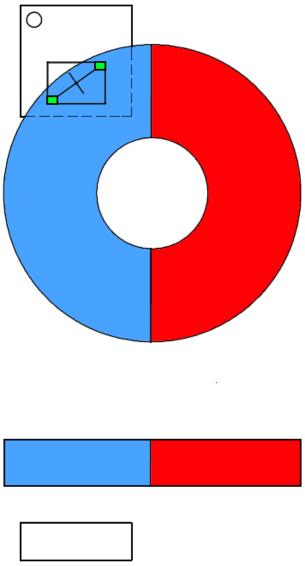


Figure 12-5 - A slightly off centred diametrical ring magnet

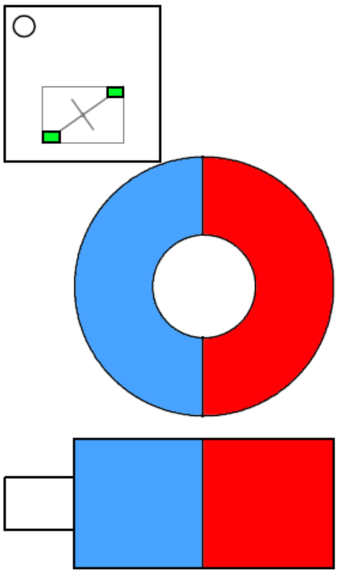


Figure 12-4 - A diametrical barrel magnet next to the IC. The distance between the sensor and the magnet is greater in this solution, thus a stronger magnet is suggested.

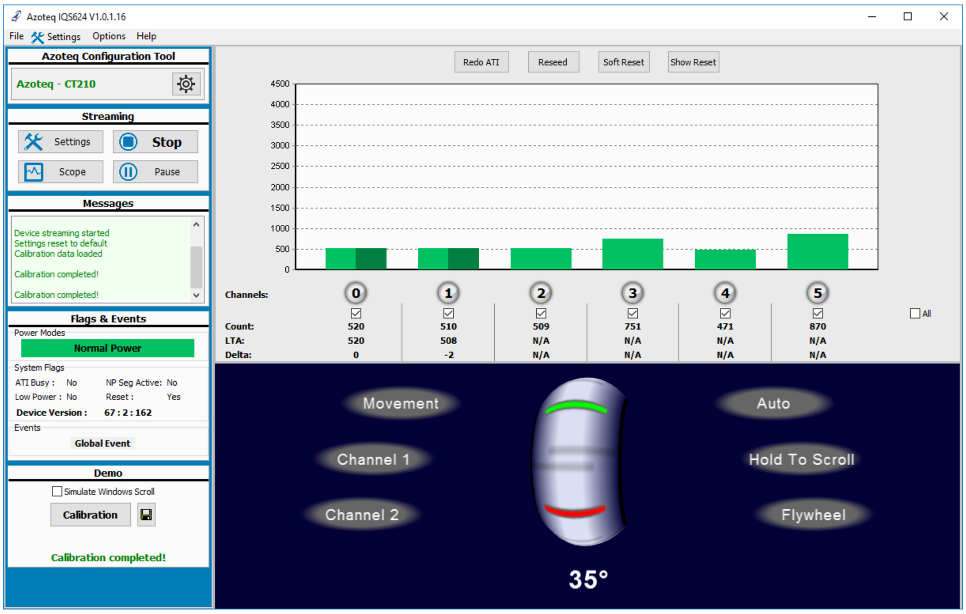
Please note: The rectangles which represent the hall sensors in these diagrams are only approximations of where the hall sensors can be found and is not to scale.

Even though these solutions will work we do not encourage their use. We designed this product with the idea to promote easy usage and fewer physical restrictions to the usage. These solutions require more critical design on the physical layout and rigidness in the final project.

12.2 Appendix B: Magnet calibration

12.2.1 How to calculate the calibration constants using the IQS624 GUI

Step 1: Open the IQS624 GUI, connect the device and start.



Device streaming started
Settings reset to default
Calibration data loaded

Calibration completed!

Calibration completed!

Power Modes: Normal Power

System Flags
ATI Busy : No
NP Seg Active: No
Low Power : No
Reset : Yes
Device Version : 67 : 2 : 162

Events: Global Event

☐ Simulate Windows Scroll

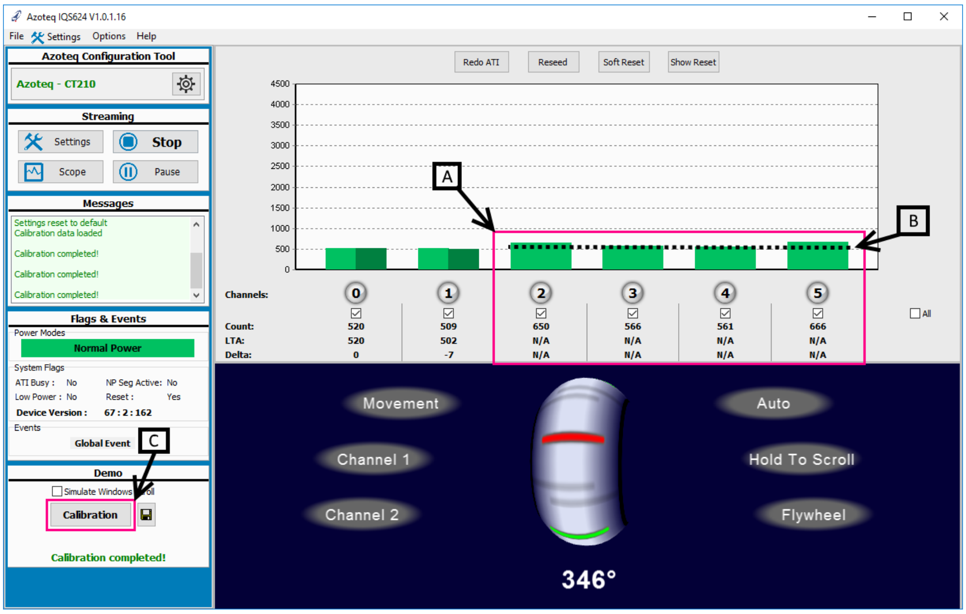
Calibration completed!

Channels:	0	1	2	3	4	5	☐ All
	☑	☑	☑	☑	☑	☑	
Count:	520	510	509	751	471	870	
LTA:	520	508	N/A	N/A	N/A	N/A	
Delta:	0	-2	N/A	N/A	N/A	N/A	

35°

If the IQS624 device is connected the GUI should look like the previous figure.

Step 2: Align the Hall sensor channels and start the calibration



Settings reset to default
Calibration data loaded

Calibration completed!

Calibration completed!

Calibration completed!

Power Modes: Normal Power

System Flags
ATI Busy : No
NP Seg Active: No
Low Power : No
Reset : Yes
Device Version : 67 : 2 : 162

Events: Global Event

☐ Simulate Windows Scroll

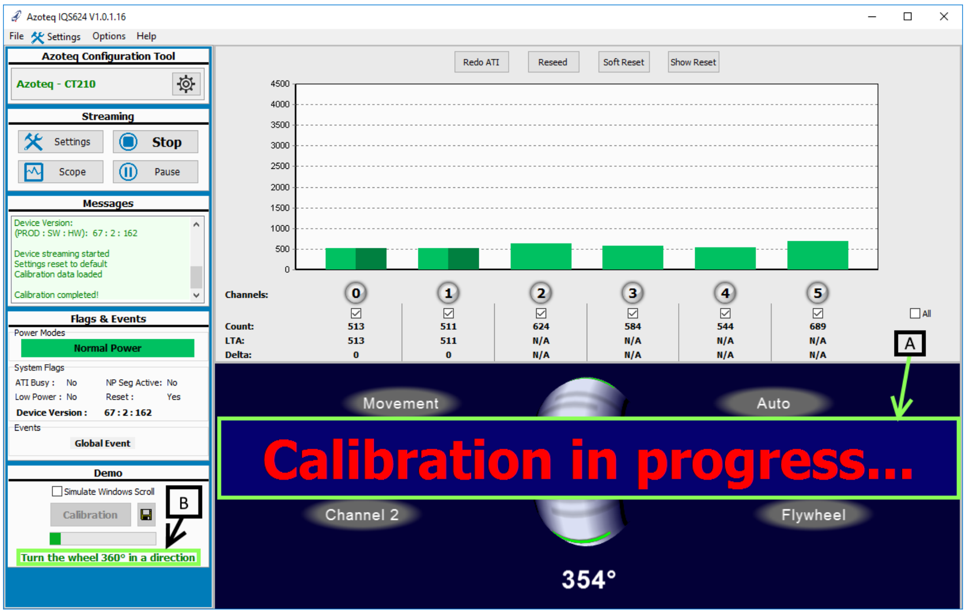
Calibration completed!

Channels:	0	1	2	3	4	5	☐ All
	☑	☑	☑	☑	☑	☑	
Count:	520	509	650	566	561	666	
LTA:	520	502	N/A	N/A	N/A	N/A	
Delta:	0	-7	N/A	N/A	N/A	N/A	

346°

A. The four Hall channels.
B. The channels should be lined up or as lined up as possible. This step can be skipped but it has been observed that better results has been obtained by adding this step.
C. The calibration button. If this button is clicked, the calibration process will start.

Step 3a: Calibrating the device



Device Version:
(PROD : SW : HW): 67 : 2 : 162

Device streaming started
Settings reset to default
Calibration data loaded

Calibration completed!

ATI Busy : No
NP Seg Active: No
Low Power : No
Reset : Yes
Device Version : 67 : 2 : 162

☐ Simulate Windows Scroll

Turn the wheel 360° in a direction

Channels:	0	1	2	3	4	5
	☑	☑	☑	☑	☑	☑
Count:	513	511	624	584	544	689
LTA:	513	511	N/A	N/A	N/A	N/A
Delta:	0	0	N/A	N/A	N/A	N/A

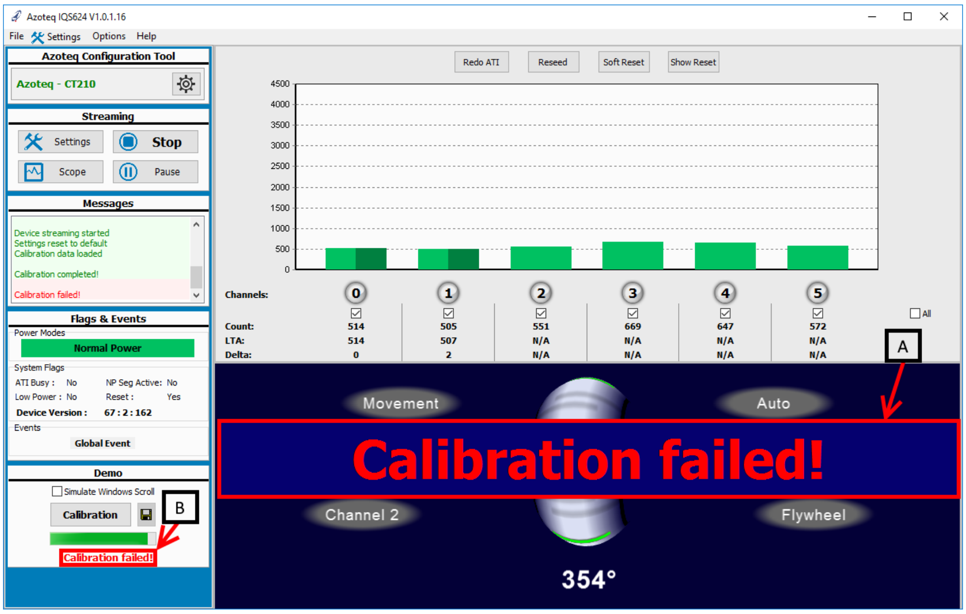
Calibration in progress...

A. This banner indicates that the calibration progress has started.

B. Like this text instructs, the user must rotate the wheel on the IQS624 device 360 degrees.

It is encouraged that the wheel must be rotated at a constant and low speed.

Step 3b: Calibration failure



Device streaming started
Settings reset to default
Calibration data loaded

Calibration completed!

Calibration failed!

ATI Busy : No
NP Seg Active: No
Low Power : No
Reset : Yes
Device Version : 67 : 2 : 162

☐ Simulate Windows Scroll

Calibration failed!

Channels:	0	1	2	3	4	5
	☑	☑	☑	☑	☑	☑
Count:	514	505	551	669	647	572
LTA:	514	507	N/A	N/A	N/A	N/A
Delta:	0	2	N/A	N/A	N/A	N/A

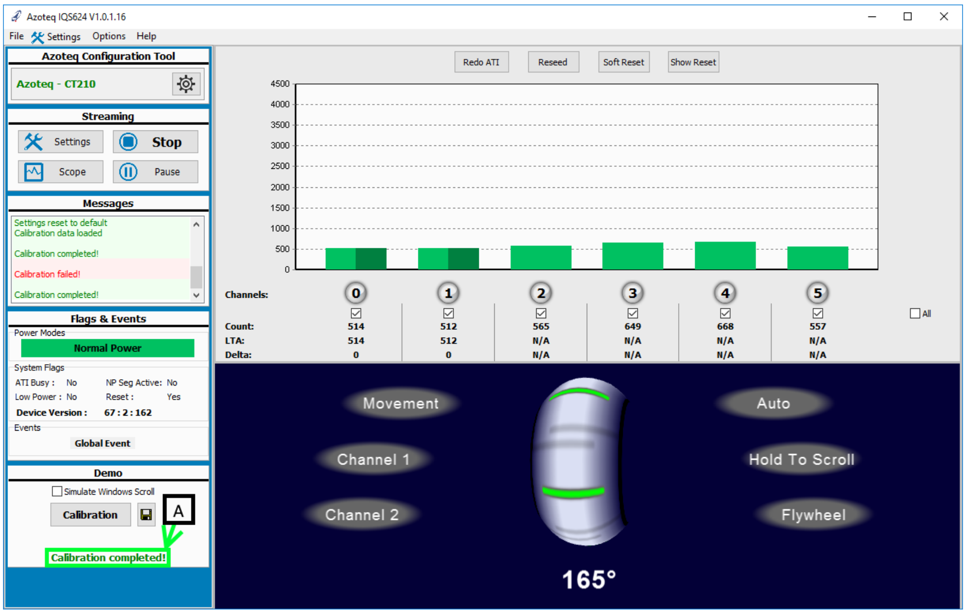
Calibration failed!

A. If this banner pop's up while rotating the wheel an error was received while calibrating the device.

B. This text also informs an error has occurred.

If an error occurs step 2-3a should be repeated.

Step 3c: Calibration complete and successful



Messages:
Settings reset to default
Calibration data loaded
Calibration completed!
Calibration failed!
Calibration completed!

Power Modes: Normal Power

System Flags
ATI Busy : No NP Seg Active: No
Low Power : No Reset : Yes
Device Version : 67 : 2 : 162

Events: Global Event

☐ Simulate Windows Scroll

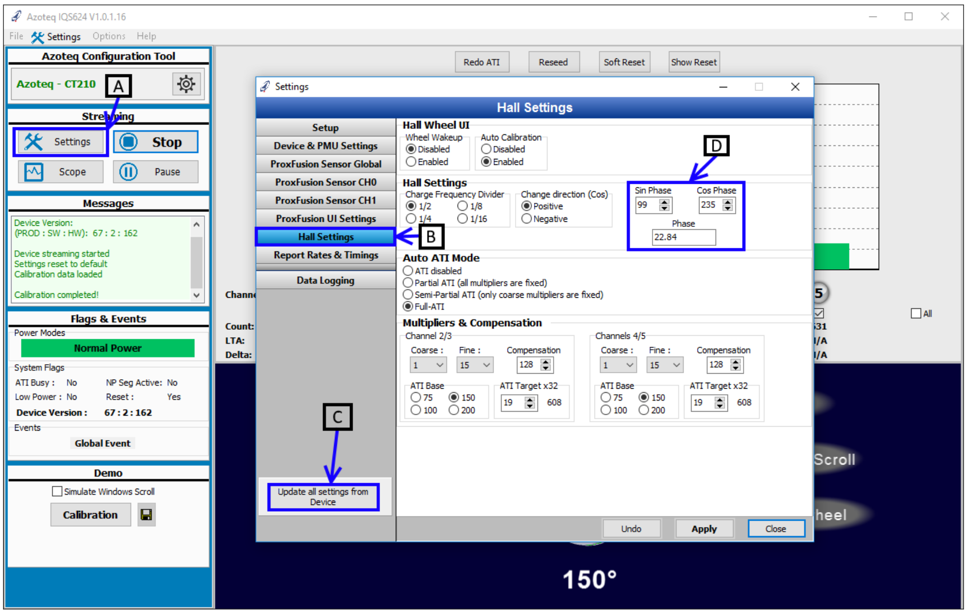
Calibration completed!

Channels:	0	1	2	3	4	5
	☑	☑	☑	☑	☑	☑
Count:	514	512	565	649	668	557
LTA:	514	512	N/A	N/A	N/A	N/A
Delta:	0	0	N/A	N/A	N/A	N/A

☐ All

A. This text confirms that the calibration is completed and successful and that the constants have been written to the device.

Step 4: Obtaining the calibration constants



Messages:
Device Version:
(PROD : SW : HW): 67 : 2 : 162
Device streaming started
Settings reset to default
Calibration data loaded
Calibration completed!

Power Modes: Normal Power

System Flags
ATI Busy : No NP Seg Active: No
Low Power : No Reset : Yes
Device Version : 67 : 2 : 162

Events: Global Event

☐ Simulate Windows Scroll

Hall Wheel UI
Wheel Wakeup: ● Disabled ○ Enabled
Auto Calibration: ○ Disabled ● Enabled

Hall Settings
Charge Frequency Divider: ● 1/2 ○ 1/8 ○ 1/4 ○ 1/16
Change direction (Cos): ● Positive ○ Negative
Sin Phase: 99
Cos Phase: 235
Phase: 22.84

Auto ATI Mode
○ ATI disabled
○ Partial ATI (all multipliers are fixed)
○ Semi-Partial ATI (only coarse multipliers are fixed)
● Full-ATI

Multipliers & Compensation
Channel 2/3: Coarse : 1 Fine : 15 Compensation 128
ATI Base: ○ 75 ● 150 ○ 100 ○ 200
ATI Target x32: 19 608
Channels 4/5: Coarse : 1 Fine : 15 Compensation 128
ATI Base: ○ 75 ● 150 ○ 100 ○ 200
ATI Target x32: 19 608

A. The settings button to open the settings window.
B. The Hall settings tab which contains all the settings for the Hall UI
C. This button updates the settings window from the connected device. Its recommended that this button should be clicked before the values are used from this window.
D. The calibration constants. The sin phase and cos phase are the two constants which are written to the device. The phase (displayed in degrees) can also be used to obtain both of these constants.

If this calibration is done on a product the constants obtained from the calibration can be used for projects with the same physical layout and magnet. This means that only one calibration is needed per product.

12.2.2 How to calculate the calibration constants using the raw data

There are two Hall Plates that make up the sensor, separated by a fixed distanced in the IC package, as described previously. These plates, designated Plate 1 & Plate 2, each have two associated data channels that sense the North-South magnetic field coincident on the plates.

For Plate 1: CH2 is the non-inverted channel, and CH3 is the inverted channel.

For Plate 2: CH4 is the non-inverted channel, and CH5 is the inverted channel.

E.g. on Plate 1, if CH2 increases in value in the presence of an increasing North field, then CH3 decreases in value in the presence of an increasing North field.

The phase delta observed between the plates can be calculated from either the non-inverted, or the inverted channel pairs.

To calculate the phase delta:

Symbols

P_n	Non-inverted channel of Plate n: $where\ P_1 = CH_2, and\ P_2 = CH_4$
P_n'	Inverted channel of Plate n: $P_1' = CH_3, and\ P_2' = CH_5$
$P_n\|_{max}$	Max value of the channel
$P_n\|_{min}$	Min value of the channel
θ_Δ	Phase observed between the plates

Calculations

To calculate the phase, for at least one full rotation of the magnet, capturing all four channels:

First normalize the data for each channel, to obtain.

$$N(CH_n) = \frac{\frac{CH_n|_{max} - CH_n}{CH_n}}{\frac{CH_n|_{max} - CH_n|_{min}}{CH_n|_{min}}} \tag{1}$$

The data will now range between 0 – 1.

For the non-inverted pair: $\{P_2, P_1\} = \{CH_4, CH_2\}$ sample both channels where $N(CH_4) \approx 0.5$. With these values, the phase delta can be calculated:

$$\theta_\Delta = sin^{-1}(|N(CH_4) - N(CH_2)| \cdot 2) \tag{2}$$

Likewise, the phase delta can be calculated from the inverted pair: $\{P_2', P_1'\} = \{CH_5, CH_3\}$ sample both channels where $N(CH_5) \approx 0.5$.

$$\theta_\Delta' = sin^{-1}(|N(CH_5) - N(CH_3)| \cdot 2) \tag{3}$$

And, while the phase angles are theoretically equal, due to misalignments, $\theta_\Delta \approx \theta_\Delta'$.

To increase accuracy of the observed phase, the two calculated phases can be averaged, leading the final Observed phase as:

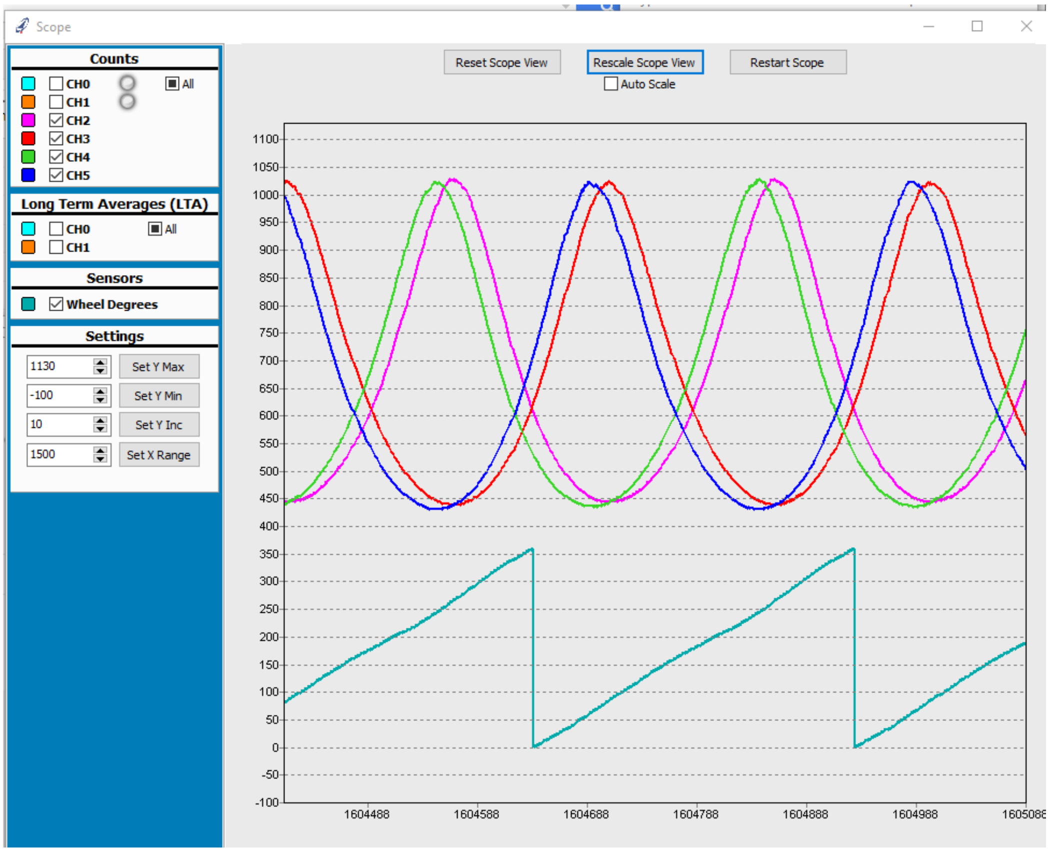
$$\theta_\Delta = \frac{sin^{-1}(|N(CH_4) - N(CH_2)| \cdot 2) + sin^{-1}(|N(CH_5) - N(CH_3)| \cdot 2)}{2} \tag{4}$$

NB: Remember that $\{CH_4, CH_2\}$ are evaluated at $N(CH_4) \approx 0.5$. While separately, $\{CH_5, CH_3\}$ are evaluated at $N(CH_5) \approx 0.5$. Even when used together in Equation (4).

The IQS624 uses this phase delta as a constant to calculate the angle. The phase delta is saved on the IC after it has been converted to $(sin(\theta_{\Delta}) \cdot 256)$ and $(cos(\theta_{\Delta}) \cdot 256)$. This is done to lessen computations and memory usage on the chip.

This means that if the phase were to change, the constants would need to be recalculated. If the application of this IC ensures nothing or little movement, the master device would only need to write the values each time the IC resets and would not need to re-calculate it. Making it possible to calculate the phase delta once before production and using that value for the application.

An example of well aligned channels, the phase offset visible between the inverted and non-inverted channel pairs of the two plates:



Counts
☐ CH0
☐ CH1
☑ CH2
☑ CH3
☑ CH4
☑ CH5

Long Term Averages (LTA)
☐ CH0
☐ CH1

Sensors
☑ Wheel Degrees

Settings
1130 Set Y Max
-100 Set Y Min
10 Set Y Inc
1500 Set X Range

☐ Auto Scale

Experimentally, jog the XYZ alignment of the magnet relative to the IC and perform at least one full rotation of the magnet, assess the peaks of the channels; repeat this until all channels have approximately the same amplitude.

To change the sensitivity of the ProxEngine to Magnetic Field Strength, the ATI parameters on the IC can be adjusted as described in the following section.

12.3 Appendix C: Hall ATI

Azoteq's ProxFusion™ Hall technology has ATI Functionality; which ensures stable sensor sensitivity. The ATI functionality is similar to the ATI functionality found in ProxSense® technology. The difference is that the Hall ATI requires two channels for a single plate.

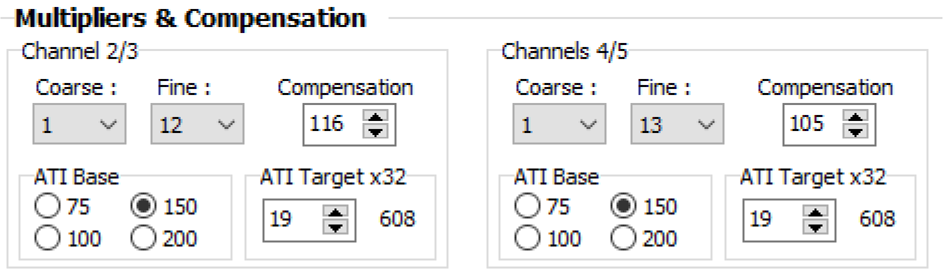
Using two channels ensures that the ATI can still be used in the presence of the magnet. The two channels are the inverse of each other, this means that the one channel will sense North and the other South. The two channels being inverted allows the capability of calculating a reference value which will always be the same regardless of the presence of a magnet.

12.3.1 Hall reference value:

The equation used to calculate the reference value, per plate:

$$Ref_n = \frac{1}{2 \cdot \left(\frac{1}{P_n} + \frac{1}{P'_n}\right)}$$

12.3.2 ATI parameters:



Multipliers & Compensation

Channel 2/3
Coarse : 1
Fine : 12
Compensation 116
ATI Base: ○ 75 ● 150 ○ 100 ○ 200
ATI Target x32: 19 608

Channels 4/5
Coarse : 1
Fine : 13
Compensation 105
ATI Base: ○ 75 ● 150 ○ 100 ○ 200
ATI Target x32: 19 608

The ATI process adjusts three values (Coarse multiplier, Fine multiplier, Compensation) using two parameters per plate (ATI base and ATI target). The ATI process is used to ensure that the sensor's sensitivity is not severely affected by external influences (Temperature, voltage supply change, etc.).

12.3.3 Coarse and Fine multipliers:

In the ATI process the compensation is set to 0 and the coarse and fine multipliers are adjusted such that the counts of the reference value (Ref) are roughly the same as the ATI Base value. This means that if the base value is increased, the coarse and fine multipliers should also increase and vice versa.

12.3.4 ATI-Compensation:

After the coarse and fine multipliers are adjusted, the compensation is adjusted till the reference value (Ref) reaches the ATI target. A higher target means more compensation and therefore more sensitivity on the sensor.

The ATI-Compensation adjusts chip sensitivity; and, must not be confused with the On-chip Compensation described below. On-chip Compensation corrects minor displacements or magnetic non-linearities. This compensation ensures that both channels of each plate – which represent North and South individually – have the same swing. On-chip compensation is performed in the UI and is not observable on the raw channel data.

The ATI process ensures that long term temperature changes, or bulk magnetic interference (e.g. the accidental placement of another magnet too close to the setup), do not affect the sensor's ability to detect the rotating magnet.

12.3.5 Recommended parameters:

There are recommended parameters to ensure optimal use. Optimally the settings would be set up to have a max swing of 1000 from peak to peak and a reference value below 1000 counts.

The recommended parameters are:

- ATI Base: 100 or 150
- ATI Target: 500 – 1000

It is not assured that these settings will always set up the channels in the optimal region but it is recommended to rather adjust the magnet's position a little as this also influences the signal received. If the magnet is too close to the IC the swing will be too large, and thus it is recommended to increase the distance between the IC and the Magnet.