

Data sheet

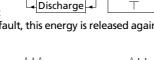
Short description

The buffer unit is a supplementary device for regulated DC 24V power supplies. It buffers load currents during typical mains faults and switching events or load peaks.

Working principle

in a regulated process.

In times when the power supply provides sufficient voltage, the buffer unit stores energy in integrated electrolytic capacitors. In case of a mains voltage fault, this energy is released again



Buffer unit

Buffer

Charge

Bridges mains faults without interruption

Statistics show that 80 percent of all mains faults last less than 0.2s. These mains faults are completely bridged by the buffer unit and will have no

influence on the DC power (startup-delay of power supply used might be taken under consideration. This increases the reliability of the system as a whole.

Short Overview - Technical Data

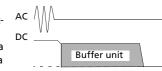
DC 24V
DC 2428.8V
selectable by front jumper setting Vin -1V: 23 - 27.8V (variable threshold) 22.5V fixed: 22.5V (fixed threshold)
max. +35V
max35V
<600mA
020A
>20A
1827s (primary charge)
see diagramm (page 2) 0,2s (22,5V/20A) or 28s (22,5V/100mA) 0,31s (22,5V/20A) or 43s (22,5V/100mA)

Order information

Order number SLV20.200 XF-1x4s/270-60 SLZ11 SLZ02

Extended hold-up time

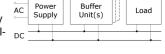
Once the main power fails or is switched off, the buffer unit will continue to provide the load current for a defined period of time. Process data can be saved and processes can be



terminated before the DC power switches off. Controlled restarts are subsequently possible.

Easy to handle, expandable and

maintenance-free The buffer unit does not require any control wiring. It can be added parallel to the load circuit at any given



point. Any given number of buffer units can be switched parallel to increase the output capacity or the hold-up time. The dual terminals allow for easy wiring. In addition, there is a housing connection.

Short Overview - Technical Data

Idling input current	typ. 80mA	
Power dissipation	typ. 1.9W	
Degree of protection	IP20 (EN 60529)	
Dimensions (W x H x D) 64mm x 124mm x 102mm (without DIN rail)		
Weight	740g	

Safety

Terminal voltage	SELV, IEC/EN 60950	
Classification	PELV (IEC364-4-41) PELV (EN 60204)	PELV (EN50178)
Isolation resistance	5MOhm (terminal→housing)	
Degree of protection	IP20 (EN 60529)	
Penetration protection	n > 3.5 x 3.5 mm	
Internal fusing	none	
Galvanic isolation to signal path	500V	

Description

DIN rail electrolytic capacitor buffer unit Mating connector for signalling terminals (part of delivery) Adapter for S7-300 rail Wall mounting set (two pcs. per package)

Technical Data

Buffer Charging

2000 C. C. C. S.		
Charging delay time	typ. 4s	
Charging current	0.40.6A	
Charging time	1827s (primary charge / cold start)	
Buffer Operation		
Rated output current	20A	
Current limitation	>20A	
Hold-up time • minimum • typical	see diagramm (page 2) 0,2s (22,5V/20A) or 28s (22,5V/100mA) 0,31s (22,5V/20A) or 43s (22,5V/100mA)	
To increase buffer surrent and/or extend hold up time any siver num		

To increase buffer current and/or extend hold-up time any given number of buffer unit can be switched parallel (max. load per terminal = 30A)

Activation threshold

Activation threshold			
"22.5V fixed"	Buffering starts if terminal voltage <22.5V, voltage is kept at 22.5V.		
"Vin -1V"	Buffering starts if terminal voltage decreases by more than 1V, faster than typ. 0.54V/s. Voltage is kept at that level. Buffering ends when voltage increases once more by 1V.		
Noise (spikes)	<200mV _{PP} (20MHz bandw., 50 Ω -measurement, buffer operation only)		
Over voltage protectionlimited to max. ±35V			
Operation indicator	Green LED (see below table 'Operating modes')		

Environmental Data

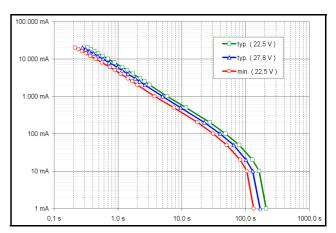
Temperature Storage/Transport Operation 	-25°C+85°C -10°C+70°C
	(measured at 25mm below the unit)
DeratingCooling	not necessary natural convection
Humidity	595% (condensation not permissible)
Vibration	
Sinus	2 – 17.8Hz: ±1.6mm
Random	17.8Hz – 500Hz 2g (IEC 60068-2-6) 2500Hz 0.5m ² (a ³) (IEC 60068-2-64)
Shock	15g/6ms and 10g/11ms (IEC 60068-2-27)
Degree of pollution	2 (EN 50178)
Installation level	2.000m above sea level

Reliability

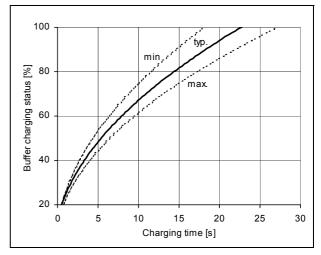
MTBF	480.000h t.b.c. (unit on stand-by, T _{amb} = +40°C)
Life time	>42.000h calculated life expectancy
Note: t.b.c. = to be cale	นโลเซฟ (เปล่าสี่มี vill follow)

Operating modes

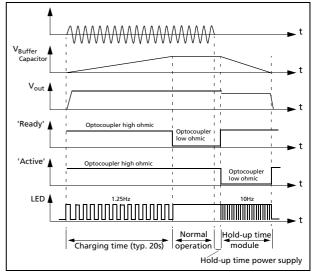
Hold-up time



Buffer charging time



Operating modes



	Current	Time	Status LED	Output 'Active'	Output 'Ready'	Bulk capacitor array
Buffer charging	400600mA	18-27s	flashes 1.25Hz	blocking	blocking	charging
Stand-by	80mA	./.	steady light	blocking	low ohmic	fully charged
Buffer operation	020A	see diagramm hold-up time	flashes 10Hz	low ohmic	blocking	discharging
Inhibit mode	15mA	./.	off	blocking	blocking	discharged
Unit not ready	15mA	./.	off	blocking	blocking	discharged

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Connections

Terminals	Fingertouch-proof terminals with captive screws for 5.5mm slotted screwdriver or Philips cross-recessed screwdriver No. 2		
Positioning	Easy to reach terminals on the front panel. Signal connectors and powers terminals are clearly separate from each other.		
Tightening torque	0.7Nm recommended		
Connector size range solid flexible 	0.5 6mm ² 20AWG 10AWG 0.5 4mm ² 20AWG 12AWG		
Ferrules	admissible		
Stripping length	7mm		

Front Elements, Operating Indicators and Elements

\oplus	Positive power in/out (twice)
Θ	Negative power in/out (twice)
Chassis Ground 州	Possibility to connect housing to ground
'Back-up Threshold' • Jumper pos. 2-3 (or missing)	Backup voltage: DC 22.5V fixed
• Jumper pos. 1-2	Backup voltage, variable: V _{in} -1V; backup activation on drop faster than typ. 0.54V/s and >1V
LED 'Status'	
• Off	Buffers are discharged, no external voltage or external voltage <22.5V
 Flashes (1.25Hz) 	Buffer capacitors are charging
• On	Unit ready for operation, buffer is fully charged
Flashes (10Hz)	Unit is buffering

Operating indicators and elements

Signalling terminals: 7 Active: unit is buffering 8 Ready: unit is on stand-by 9 Inhibit: initiates buffer discharg-102 ing, inhibits recharging of capaci-40 93 tor array 17 Jumper back-up threshold: • Pos. 1-2: variable: V_{in} -1V. Plastic slider: Buffering if voltage decreases Mounting: Place the unit onto the faster than typ. 0.54V/s and 6 + 7 Active 8 Ready 9 Inhibit DIN rail and push it downwards and > 1V against the lower front edge until it 62 Pos. 2-3: DC22.5V fixed. Voltsnaps into place. age buffering starts at Vin П **PULS** Detachment: Push downwards and <22.5V detach the unit from its DIN rail **Buffer Unit** mounting bracket **SLV 20** 124 DIN rail mounting bracket **Status LED** Indicicates charge status 阳 of buffer capacitor array 1-2 Vin -1 2 ſ 2-3 25.5 Vdc fixed **Power In/Out terminals:** dual terminals Chassi Groun Status 24-28.8V / 20A + (positive) • - (negative) Charging Housing connection 'Chassis Ground' 101 5 45

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Electromagnetic Compatibility(EMC)

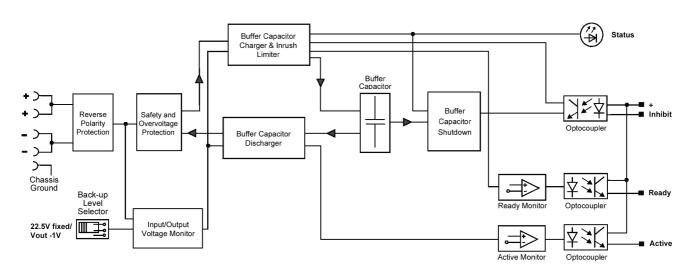
Emissions	EN 61000-6-3 (also includes EN 61000-6-4) radiated noise and interference voltage on DC lines
Immunity • Electrostatic Discharge (ESD)	EN 61000-6-2 (also includes EN 61000-6-1) EN 61000-4-2, Level 4 (withstands 8kV direct discharge, 15kV air discharge; DIN rail earthed)
 Electromagnetic radiated fields 	EN 61000-4-3, Level 3 (10V/m) ENV 50204 (10V/m)
 Burst, coupled to: DCout lines 	EN 61000-4-4, Level 3 (2 kV)
 Surge transients Differential mode (+→housing, -→housing) Common mode 	EN 61000-4-5 500V 500V
 (+ → -) Conducted noise immunity 	EN 61000-4-6, Level 3 (10V, 150kHz - 80MHz)

Approvals and Declarations of Conformity

The unit complies with all major **safety approvals**: EU (EN 60950), USA (UL 60950 recognized, UL 508 LISTED), CBscheme (IEC 60950), Canada (CAN/CSA-C22.2 No. 60950 [cUR], CAN/CSA-C22.2 No. 14 [cUL])

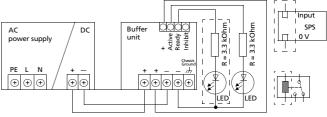
This unit has the following **declarations of conformity**: Europe (CE acc. to EMC and low voltage directive)

Schematic



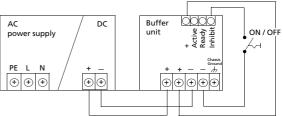
Signalling Terminals

Shared \oplus signal \rightarrow signal (e.g. Inhibit)	DC 35V max.	
Signal outputs 7 – Active 8 – Ready 	Optocoupler low ohmic, while buffer capacitors are dis- charging low ohmic, when buffer is fully charged	AC power supply PE L N
Current	10mA max. permissible	$(\oplus) \oplus) /$
Voltage drop across opto coupler	0.9V/1mA3V/5mA (while low ohmic)	Signalling o
Leakage current	<100µA (while optocoupler blocks)	 LED + R Relay (R
Signal input • 9 – Inhibit	Optocoupler 'High' input signal initiates unit shutdown and buffer discharge	 SPS inpu
Shutdown threshold	>710V	AC
Input current	<4mA	power supply
Isolation voltage	AC 500V against power path	
Signal outputs and cor open circuit and overlo	ntrol input are protected against short-circuit, pad.	PE L N (+) (+) (+) (+) (+) (+) (+) (+) (+) (+)



output variants:

- R = 3.3kOhm (see above)
- R_L = 2kOhm)
- ut



Installation Notes

Mounting position vertical; power in/out terminals below, signal terminal above Admissible area of application: The buffer unit SLV20.200 has

been designed for use in panel-board installations or other buildingin applications where a suitable mechanical enclosure shall be provided to fulfill the requirements for shock-hazard protection and/or protection from hazardous energy levels as well as for fire protection.

Unless otherwise stated, specifications are valid for 'Ready' state, DC 24V input voltage and +25°C ambient temperature. They are subject to change without prior notice.

Your partner in power supply:



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