TSOP752..., TSOP754...

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems

FEATURES

- Very low supply current
- · Photo detector and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- Improved immunity against ambient light
- Capable of side or top view
- Two lenses for high sensitivity and wide GREEN receiving angle
- Insensitive to supply voltage ripple and noise
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The TSOP75... series are miniaturized SMD IR receiver modules for infrared remote control systems. Two PIN diodes and a preamplifier are assembled on a leadframe, the epoxy package contains an IR filter.

The demodulated output signal can be directly connected to a microprocessor for decoding.

The TSOP754.. series devices are optimized to suppress almost all spurious pulses from energy saving lamps like CFLs. The AGC4 used in the TSOP754.. may suppress some data signals. The TSOP752.. series are provided primarily for compatibility with old AGC2 designs. New designs should prefer the TSOP754.. series containing the newer AGC4.

These components have not been qualified according to automotive specifications.

PARISI	ADLE			
AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4)	
	30 kHz	TSOP75230	TSOP75430	
Carrier frequency	33 kHz	TSOP75233	TSOP75433	
	36 kHz	TSOP75236	TSOP75436 ⁽¹⁾⁽²⁾⁽³⁾	
	38 kHz	TSOP75238	TSOP75438 ⁽⁴⁾⁽⁵⁾	
	40 kHz	TSOP75240	TSOP75440	
	56 kHz	TSOP75256	TSOP75456 ⁽⁶⁾⁽⁷⁾	
Package		Hein	ndall	
Pinning		1, 4 = GND, 2 = V _S , 3 = OUT		
Dimensions (mm)		6.8 W x 3.0 H x 3.2 D		
Mounting		SMD		
Application		Remote control		
Best remote control code		⁽¹⁾ RC-5 ⁽²⁾ RC-6 ⁽³⁾ Panasonic ⁽⁴⁾ NEC	⁽¹⁾ RC-5 ⁽²⁾ RC-6 ⁽³⁾ Panasonic ⁽⁴⁾ NEC ⁽⁵⁾ Sharp ⁽⁶⁾ r-step ⁽⁷⁾ Thomson RCA	

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Pinning: 1, 4 = GND, 2 = V_S , 3 = OUT

MECHANICAL DATA

ORDERING CODE

DARTS TARI F

Taping: TSOP75...TT - top view taped TSOP75...TR - side view taped



RoHS

COMPLIANT

HALOGEN FREE

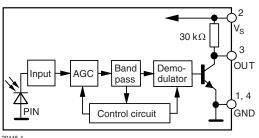
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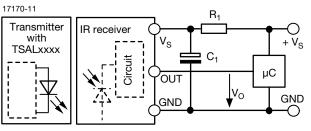


BLOCK DIAGRAM



20445-1

APPLICATION CIRCUIT



 R_1 and C_1 recommended to reduce supply ripple for $V_S < 2.8$ V

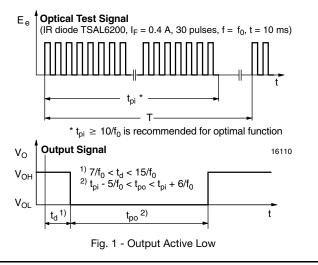
ABSOLUTE MAXIMUM RAT	INGS			
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Supply voltage		V _S	-0.3 to +6.0	V
Supply current		ا _S	3	mA
Output voltage		Vo	-0.3 to (V _S + 0.3)	V
Output current		I _O	5	mA
Junction temperature		Tj	100	°C
Storage temperature range		T _{stg}	-25 to +85	°C
Operating temperature range		T _{amb}	-25 to +85	°C
Power consumption	$T_{amb} \le 85 \ ^{\circ}C$	P _{tot}	10	mW

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND O	PTICAL CHARACTERISTICS (T _{amb} = 25	$(T_{amb} = 25 \degree C, unless otherwise specified)$				
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		Vs	2.5	-	5.5	V
Supply ourrent	$E_v = 0, V_S = 3.3 V$	I _{SD}	0.27	0.35	0.45	mA mA
Supply current	E _v = 40 klx, sunlight	I _{SH}	-	0.45	-	
Transmission distance	$E_v = 0$, test signal see Fig. 1, IR diode TSAL6200, I _F = 200 mA	d	-	45	-	m
Output voltage low	$I_{OSL} = 0.5 \text{ mA}, E_e = 0.7 \text{ mW/m}^2$, test signal see Fig. 1	V _{OSL}	-	-	100	mV
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e min.}	-	0.12	0.25	mW/m ²
Maximum irradiance	t_{pi} - 5/f _o < t_{po} < t_{pi} + 6/f _o , test signal see Fig. 1	E _{e max.}	30	-	-	W/m ²
Directivity	Angle of half transmission distance	φ1/2	-	± 50	-	deg

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



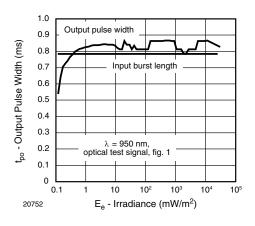


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

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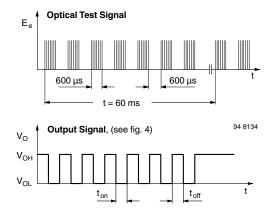


Fig. 3 - Output Function

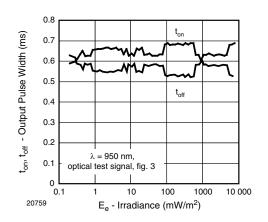


Fig. 4 - Output Pulse Diagram

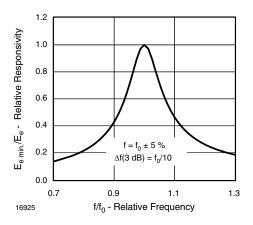


Fig. 5 - Frequency Dependence of Responsivity

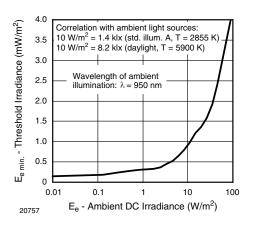


Fig. 6 - Sensitivity in Bright Ambient

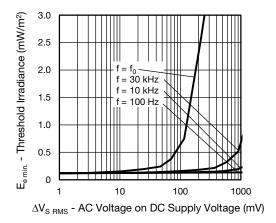


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

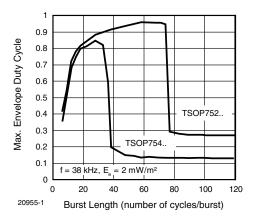


Fig. 8 - Max. Envelope Duty Cycle vs. Burst Length

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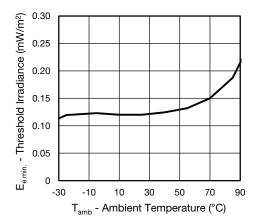


Fig. 9 - Sensitivity vs. Ambient Temperature

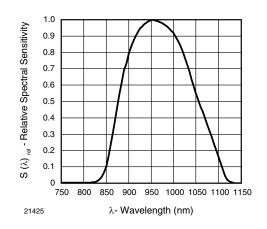


Fig. 10 - Relative Spectral Sensitivity vs. Wavelength

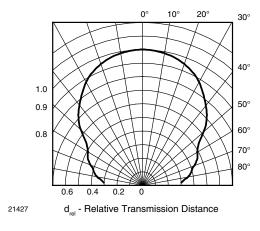


Fig. 11 - Horizontal Directivity

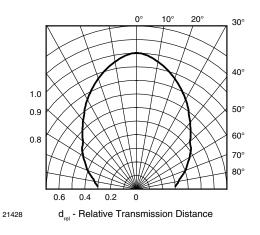


Fig. 12 - Vertical Directivity

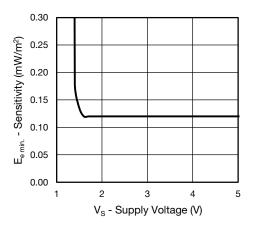


Fig. 13 - Sensitivity vs. Supply Voltage

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SUITABLE DATA FORMAT

This series is designed to suppress spurious output pulses due to noise or disturbance signals. The devices can distinguish data signals from noise due to differences in frequency, burst length, and envelope duty cycle. The data signal should be close to the device's band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the product in the presence of a disturbance, the sensitivity of the receiver is automatically reduced by the AGC to insure that no spurious pulses are present at the receiver's output.

Some examples which are suppressed are:

- DC light (e.g. from tungsten bulbs sunlight)
- Continuous signals at any frequency
- Strongly or weakly modulated patterns from fluorescent lamps with electronic ballasts (see Fig. 14 or Fig. 15)

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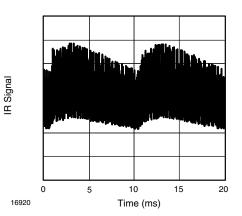


Fig. 14 - IR Disturbance from Fluorescent Lamp with Low Modulation

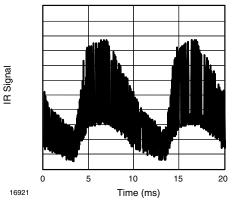


Fig. 15 - IR Disturbance from Fluorescent Lamp with High Modulation

	TSOP752	TSOP754
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	1800	1500
NEC code	Yes	Preferred
RC5/RC6 code	Yes	Preferred
Thomson 56 kHz code	Yes	Preferred
Sharp code	Yes	Preferred
Suppression of interference from fluorescent lamps	Mild disturbance patterns are suppressed (example: signal pattern of Fig. 14)	Complex and critical disturbance patterns are suppressed (example: signal pattern of Fig. 15 or highly dimmed LCDs)

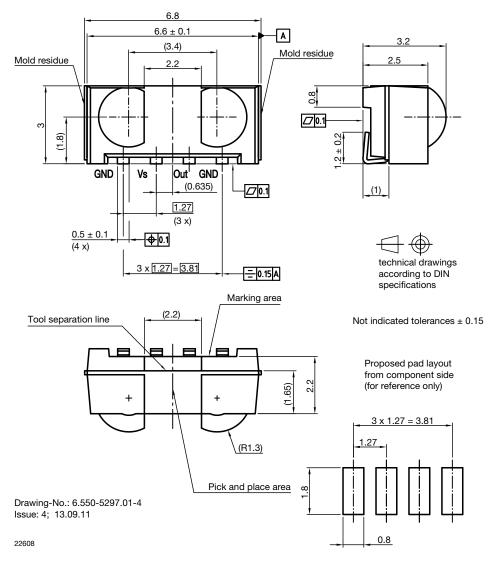
Notes

• For data formats with short bursts please see the datasheet for TSOP753.., TSOP755..

• For Sony 12, 15, and 20 bit IR codes please see the datasheet of TSOP75S40F



PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Exercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

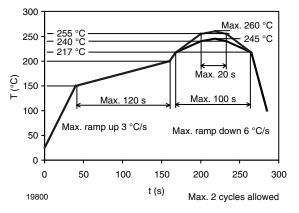
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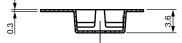
TSOP752.., TSOP754..

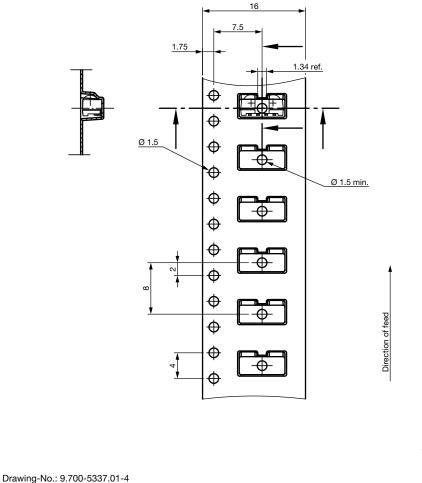
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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP..TR DIMENSIONS in millimeters





technical drawings according to DIN specifications

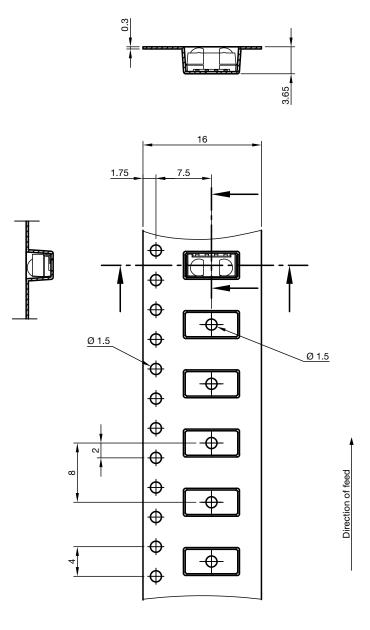
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TAPING VERSION TSOP..TT DIMENSIONS in millimeters





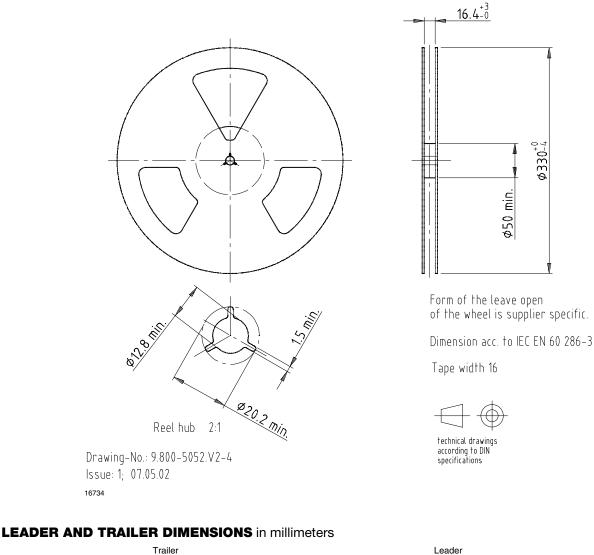
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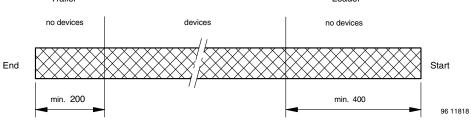
Drawing-No.: 9.700-5338.01-4 Issue: 4; 12.06.13



REEL DIMENSIONS in millimeters

TSOP752.., TSOP754..





COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3 0.1 N to 1.3 N 300 ± 10 mm/min. 165° to 180° peel angle

LABEL

Standard bar code labels for finished goods

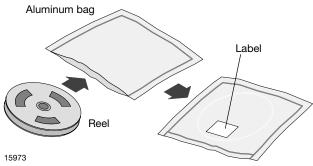
The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.



PLAIN WRITING	ABBREVIATION	LENGTH	
Item-description	-	18	
Item-number	INO	8	
Selection-code	SEL	3	
LOT-/serial-number	BATCH	10	
Data-code	COD	3 (YWW)	
Plant-code	PTC	2	
Quantity	QTY	8	
Accepted by	ACC	-	
Packed by	PCK	-	
Mixed code indicator	MIXED CODE	-	
Origin	xxxxxx+	Company logo	
Long bar code top	Туре	Length	
Item-number	Ν	8	
Plant-code	Ν	2	
Sequence-number	Х	3	
Quantity	Ν	8	
Total length	-	21	
Short bar code bottom	Туре	Length	
Selection-code	Х	3	
Data-code	Ν	3	
Batch-number	Х	10	
Filter	-	1	
Total length	-	17	

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

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After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC® standard J-STD-020 level 4 label is included on all dry bags.



EIA JEDEC standard J-STD-020 level 4 label is included on all dry bags



ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.





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